Inter-Office Memorandum

To	Dorado Core	Date	March 17, 1980
		Updated	August 8, 1980
From	Tim Diebert	Location	Palo Alto
Subject	Dorado Board Revisions	Organization	CSL

XEROX

Filed on: <DoradoDocs>RevStatus.memo
                 <DoradoDrawings>RevStatus.press

The following list represents what I believe to be the current board revision status information. It summarizes the current revision as well as all known null revisions of interest.

Board	Rev	Status
BaseBoard	Ak to Al	Null
	Al	Current
ContB	Cd	Current
ContA	Cd	Current
ProcL	Cf to Cg	Null
	Cg to Ch	Hold Fix
	Ch to Ci	Terminating resistors (Multiwire only. Stichweld rev is Null)
ProcH	Cd to Ce	Null
	Ce	Current
IFU	Bh	Old least acceptable rev. To be scrapped
	Ce to Cf	Multiwire Layout Change
	Cf to Cg	Fix interupts after XFER
	Cg	Current
MemC	Bd to Be	Null
	Be	Current
MemX	Cc	Old least acceptable rev. Not reworkable, however functional.
	Cg	Current
MemD	Bc to Bd	Null
	Bd to Be	Null
	Be	Current (1k cache chips)
	Ca	Current (4k cache chips)
MSA Stichweld	Bg	Current
PCMSA	Pc	Current
DskEth	Bd	Old least acceptable rev. To be scrapped.

	Cc to Cd	Null
	Cd to Ce	Null
	Ce	Current
DispY	Cd to Ce	Needed
	Ce to Cf	Needed
	Cf to Cg	Needed
	Cg	Current