# Interface to D0 chip tester

```
.G.T.T. Pgrp . Data .
 1 2    5      8
```

G -- Go bit (if on, start clocks following this instr.)

TT -- Type of instr:
- 0 -- Other (Pgrp is further decoded, see below)
- 1 -- Load Data into "Enable" reg. for indicated Pgrp
- 2 -- Load Data into "Data A" reg. for indicated Pgrp
- 3 -- Load Data into "Data B" reg. for indicated Pgrp

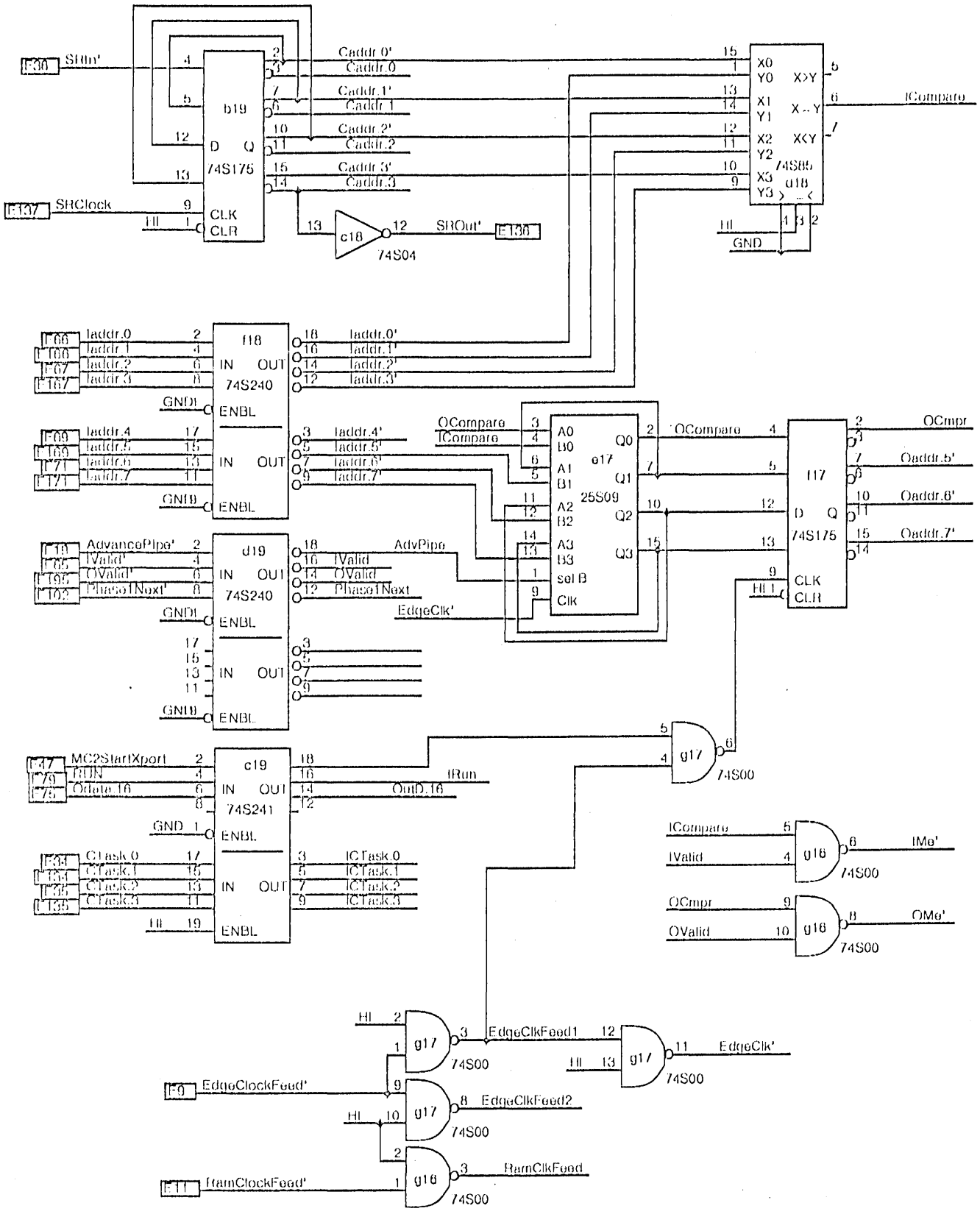
Pgrp -- Pin Group. Selects (except in "Other" type instrs) the group of 8 pins affected by the instruction.

Other group:

```
Pgrp: .X.X.I.C.C.
```

CC -- Clock parameter select:
- 0 -- None (nop)
- 1 -- Load Data into clock Parameter Reg. 1
- 2 -- Load Data into clock Parameter Reg. 2
- 3 -- Load Data into clock Parameter Reg. 3

I -- Load Data into Input Group Select Counter



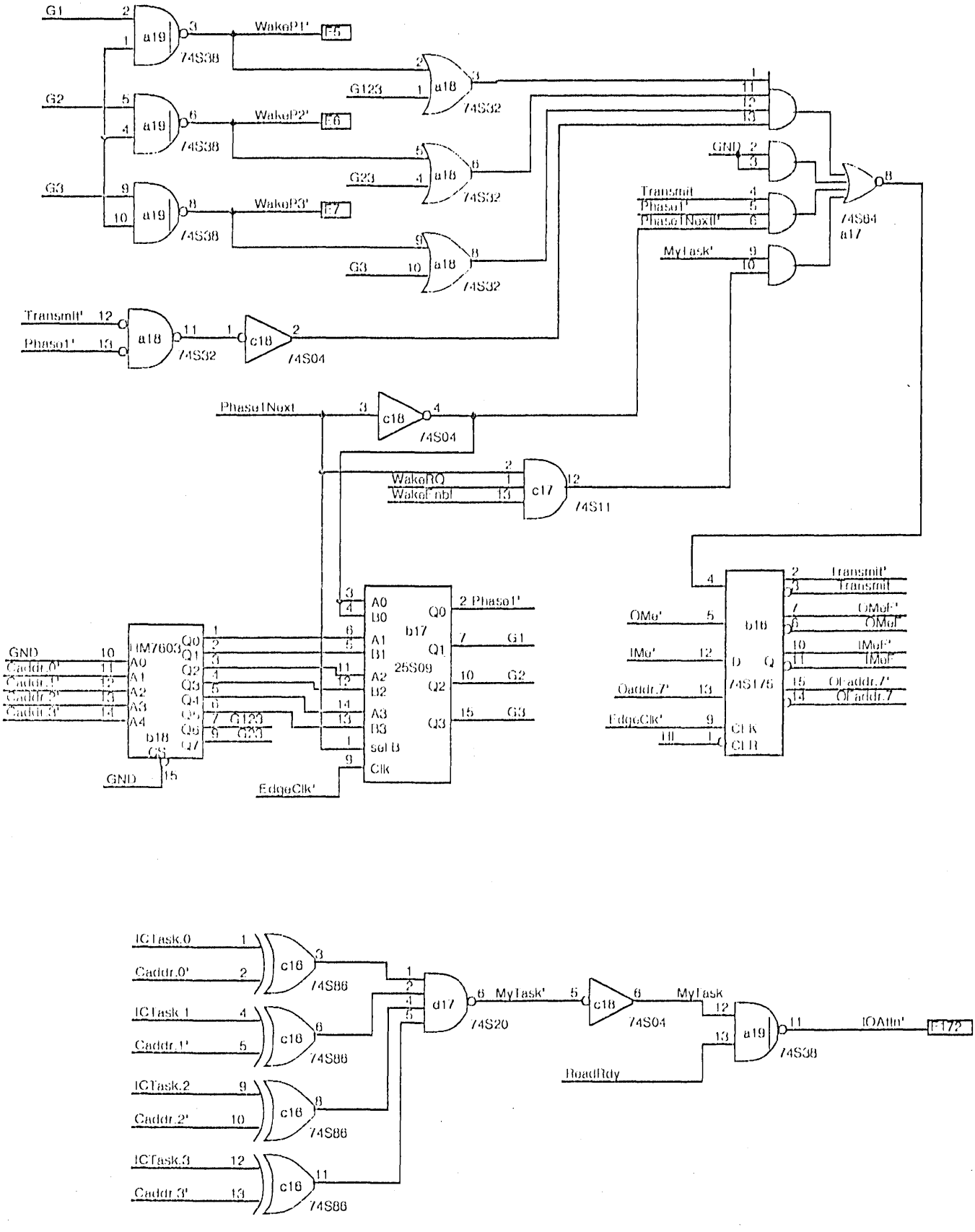

FILE: CTST11 TITLE: Chip Tester
D0 Interface

DATE:14-Oct-80 16:33

Board:
Ctest-1

Press File Written:14 Oct-80 16:34:26
LIBRARY: TTL

Drawn by PARSNIP



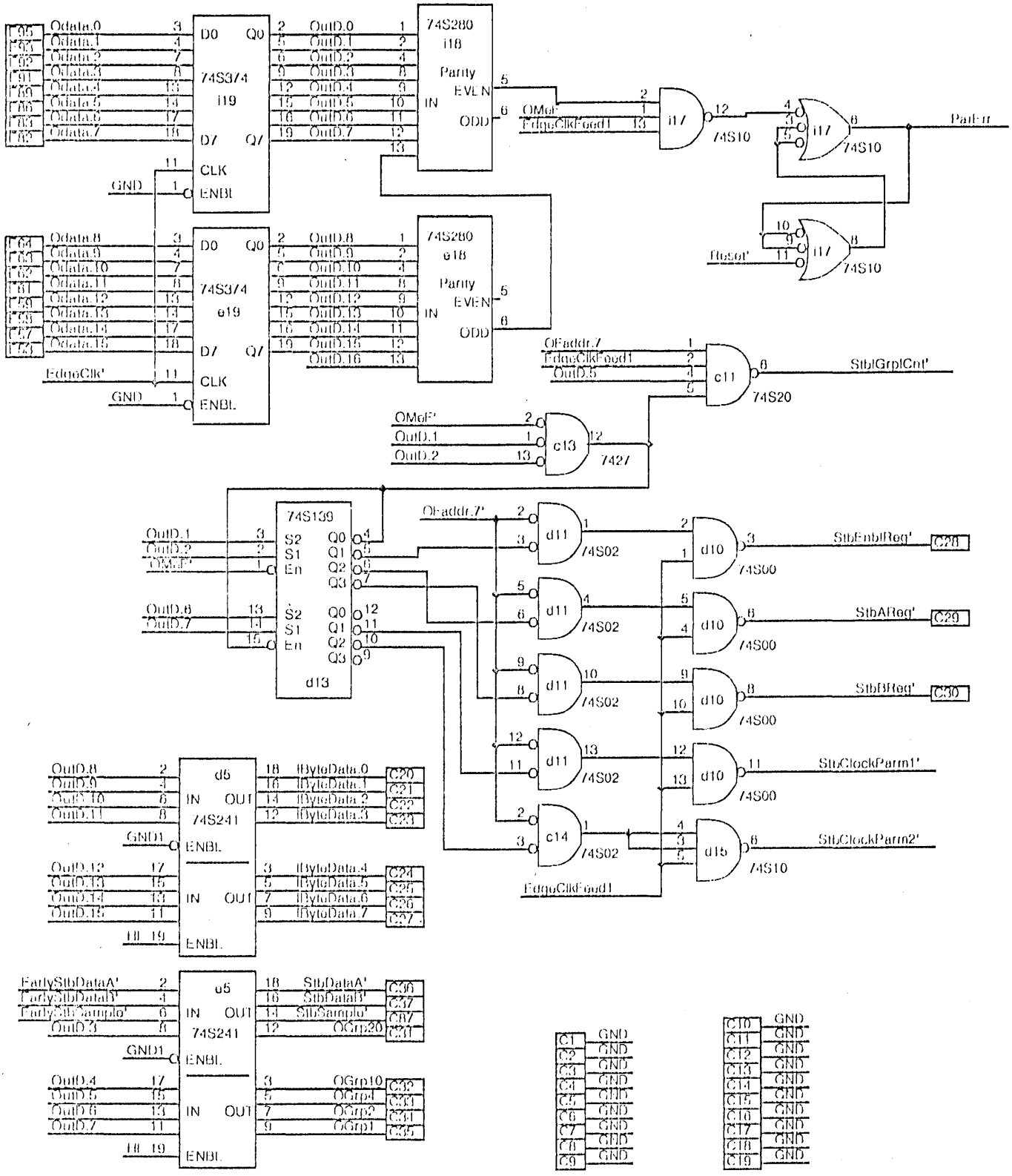

FILE: CTST12 TITLE: Chip Tester
D0 Interface

DATE:14-Oct-80 17:08

Board:
Ctest-1

Prest File Written:14-Oct-80 17:10:05
LIBRARY: TTL

Drawn by PARSNIP



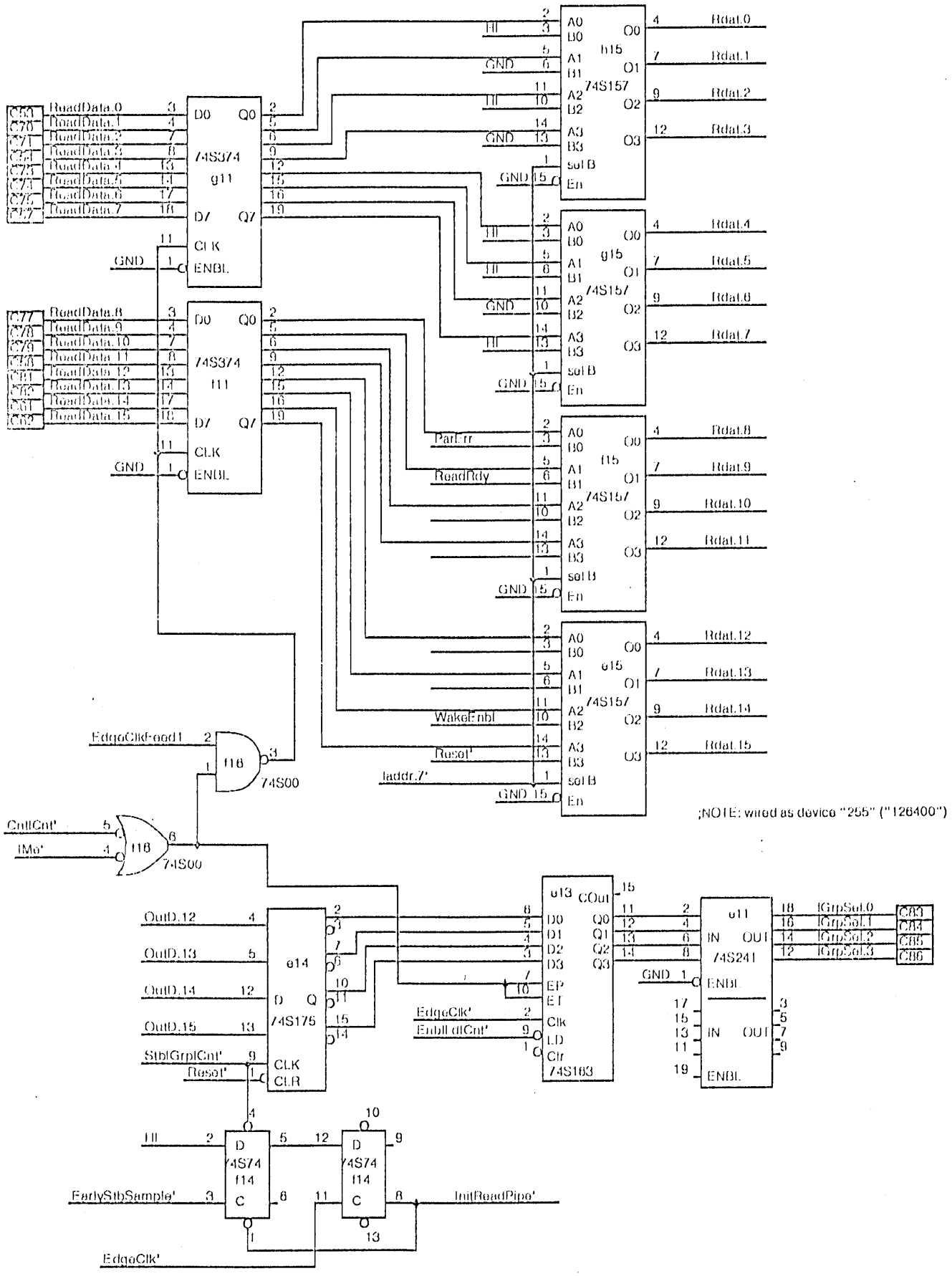

FILE: CTST13 TITLE: Chip Tester
D0 Interface

DATE:15-Oct-80 13:02

Board:
Ctest-1

Press File Written:15-Oct-80 13:03:25
LIBRARY: TTL

Drawn by PARSNIP



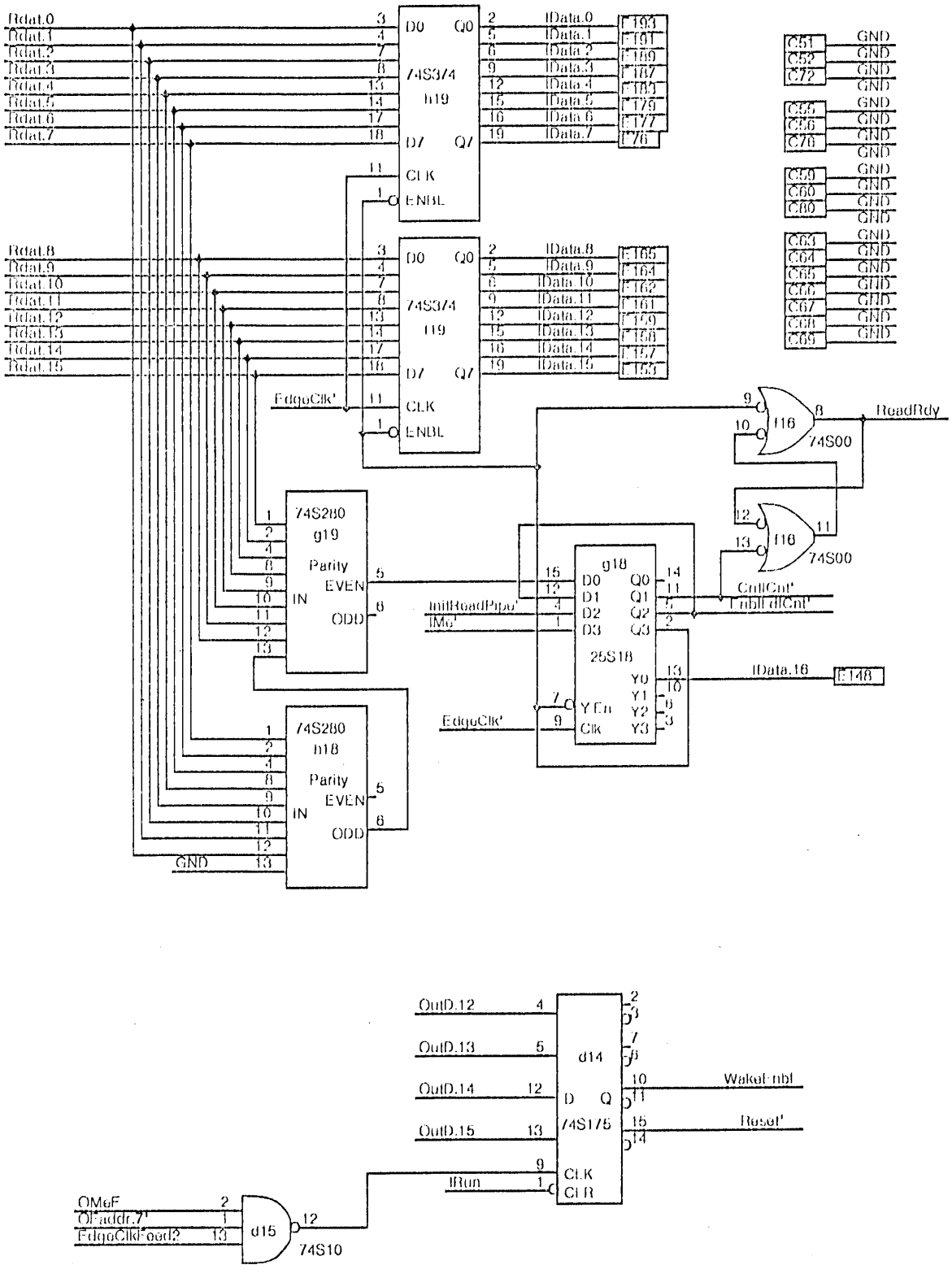

FILE: CTST14 TITLE: Chip Tester D0 Interface

DATE:14-Oct-80 16:29

Board: Ctest-1

Press File Written:14-Oct-80 16:30:05
LIBRARY: TTL

Drawn by PARSNIP



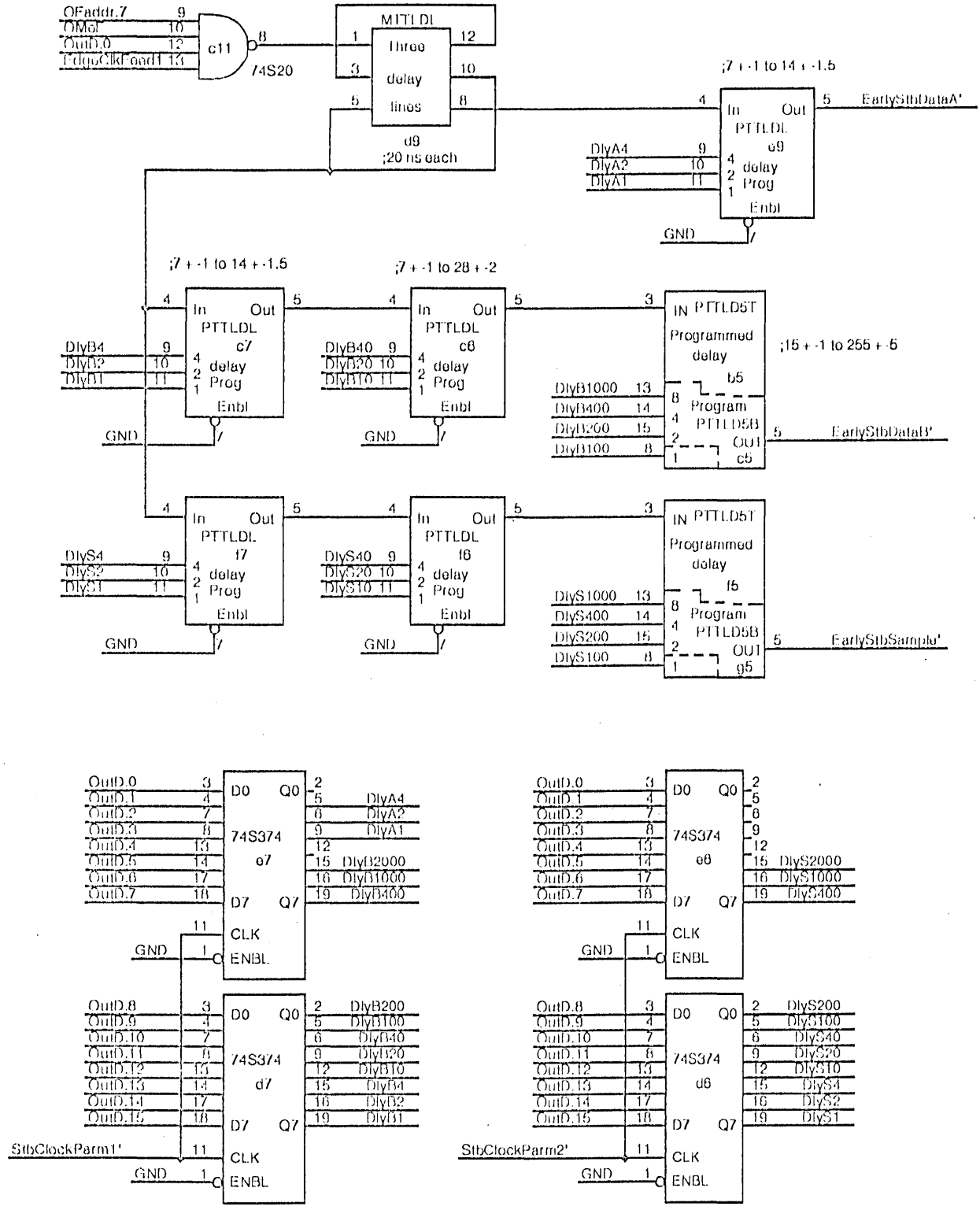

FILE: CTST15 TITLE: Chip Tester
D0 Interface

DATE:14-Oct-80 16:20

Board:
Ctest-1

Prem File Written:14-Oct-80 16:20:25
LIBRARY: TTL

Drawn by PARSNIP



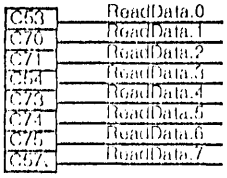

FILE: CTST16 TITLE: Chip Tester
D0 Interface

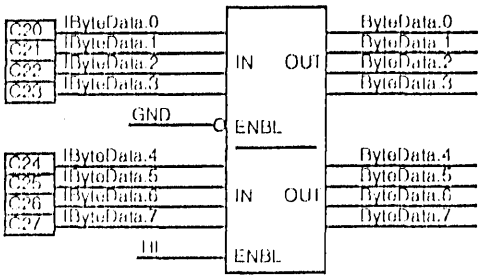
Board:
Ctest-1

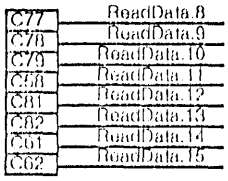
Prom File Written:16-Oct-80 17:13:33
LIBRARY: TTL

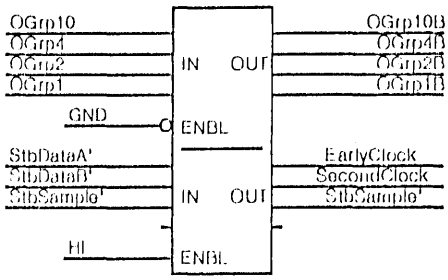
DATE:16-Oct-80 17:13

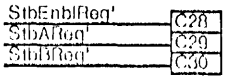
Drawn by PARSNIP

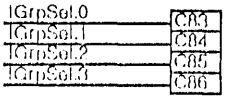


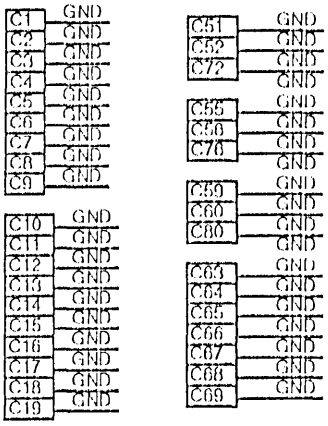



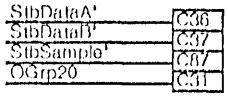



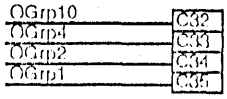



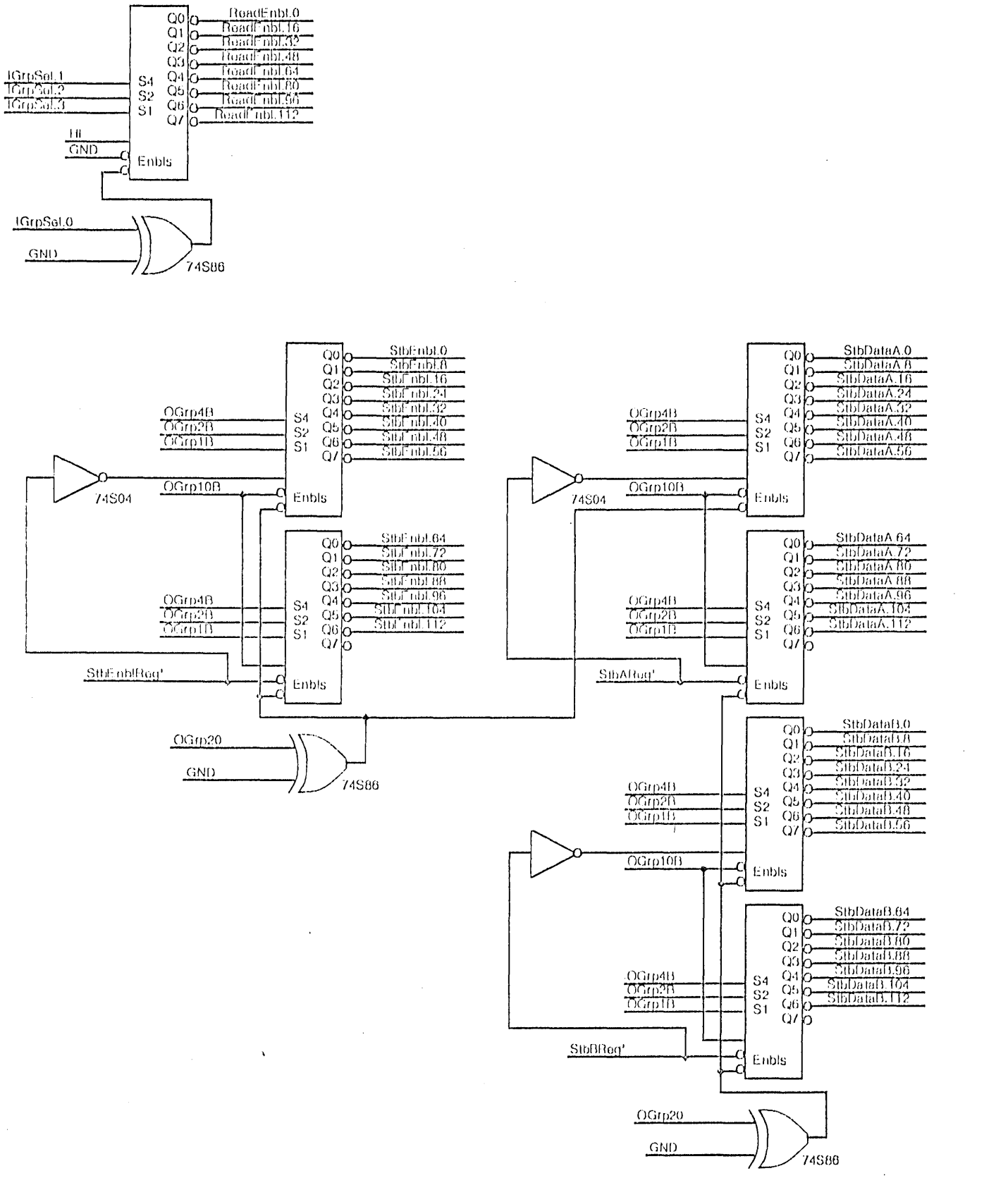

FILE: CTST26 TITLE: Chip Tester
D0 Interface

Board:
Ctest-1

Prnt File Written:16 Oct-80 13:25:07
LIBRARY: TTL

DATE:16-Oct-80 13:24

Drawn by PARSNIP



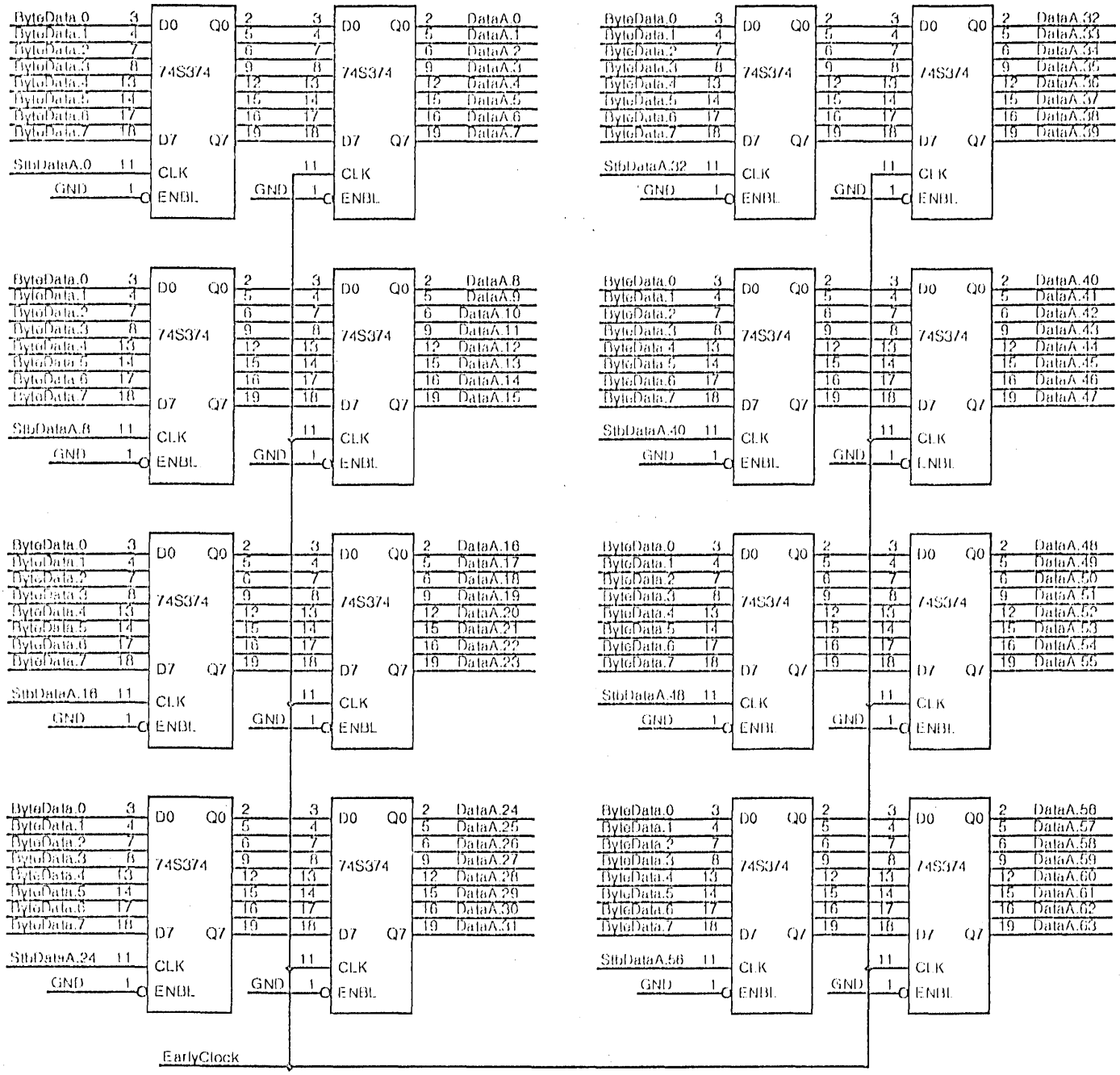

FILE: CTST27 TITLE: Chip Tester
Group Selectors

DATE:15-Oct-80 18:36

Board:
Ctest-2

Press File Written:15 Oct 80 18:37:52
LIBRARY: TTL

Drawn by PARSNIP




FILE: CTST22

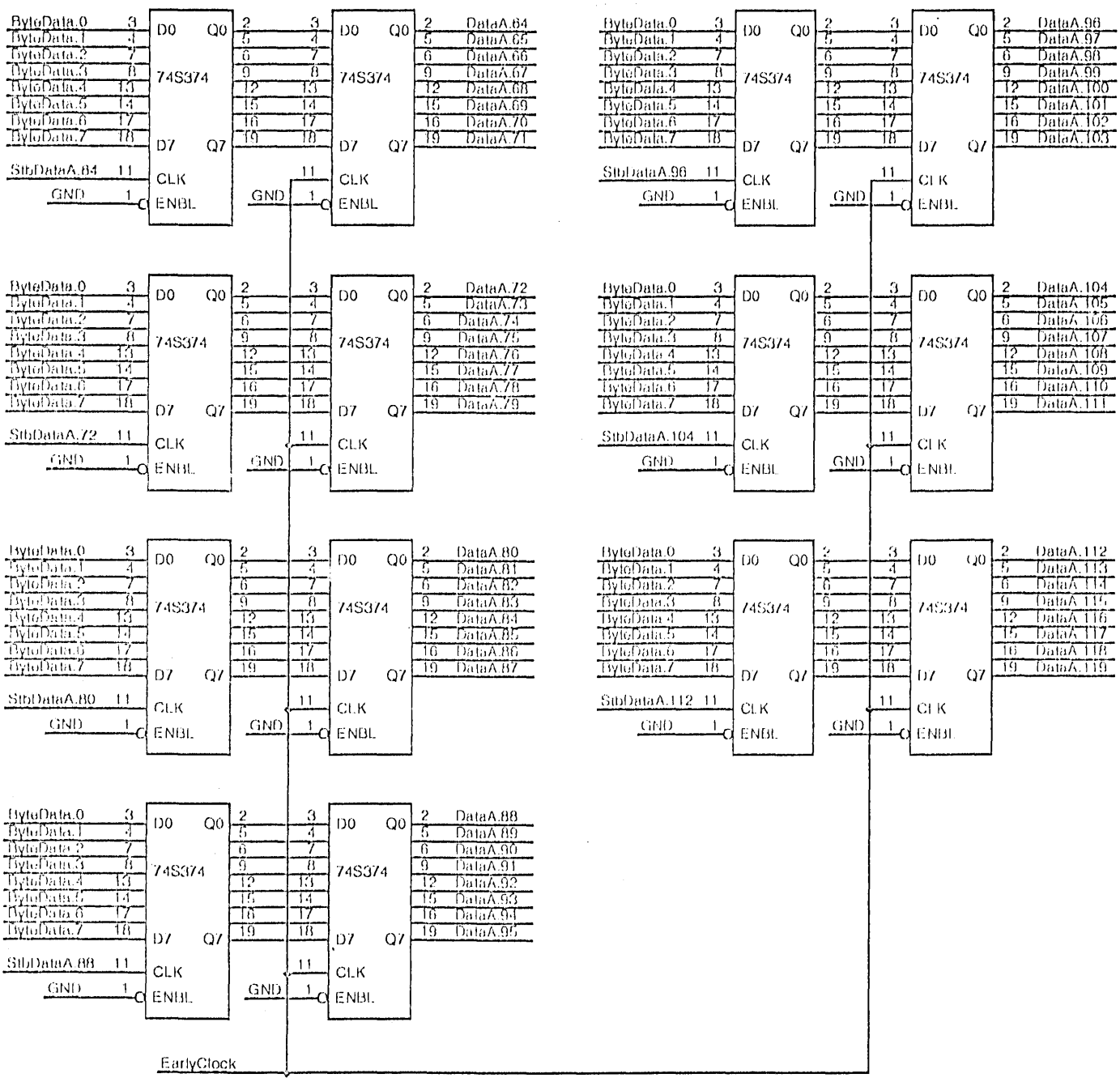
TITLE: Chip Tester
Data Register A
Early Clock

DATE: 7-Oct-80 18:11

Board:
Ctest-2

Press File Written:15-Oct-80 17:13:04
LIBRARY: TTL

Drawn by PARSNIP




FILE: CTST32

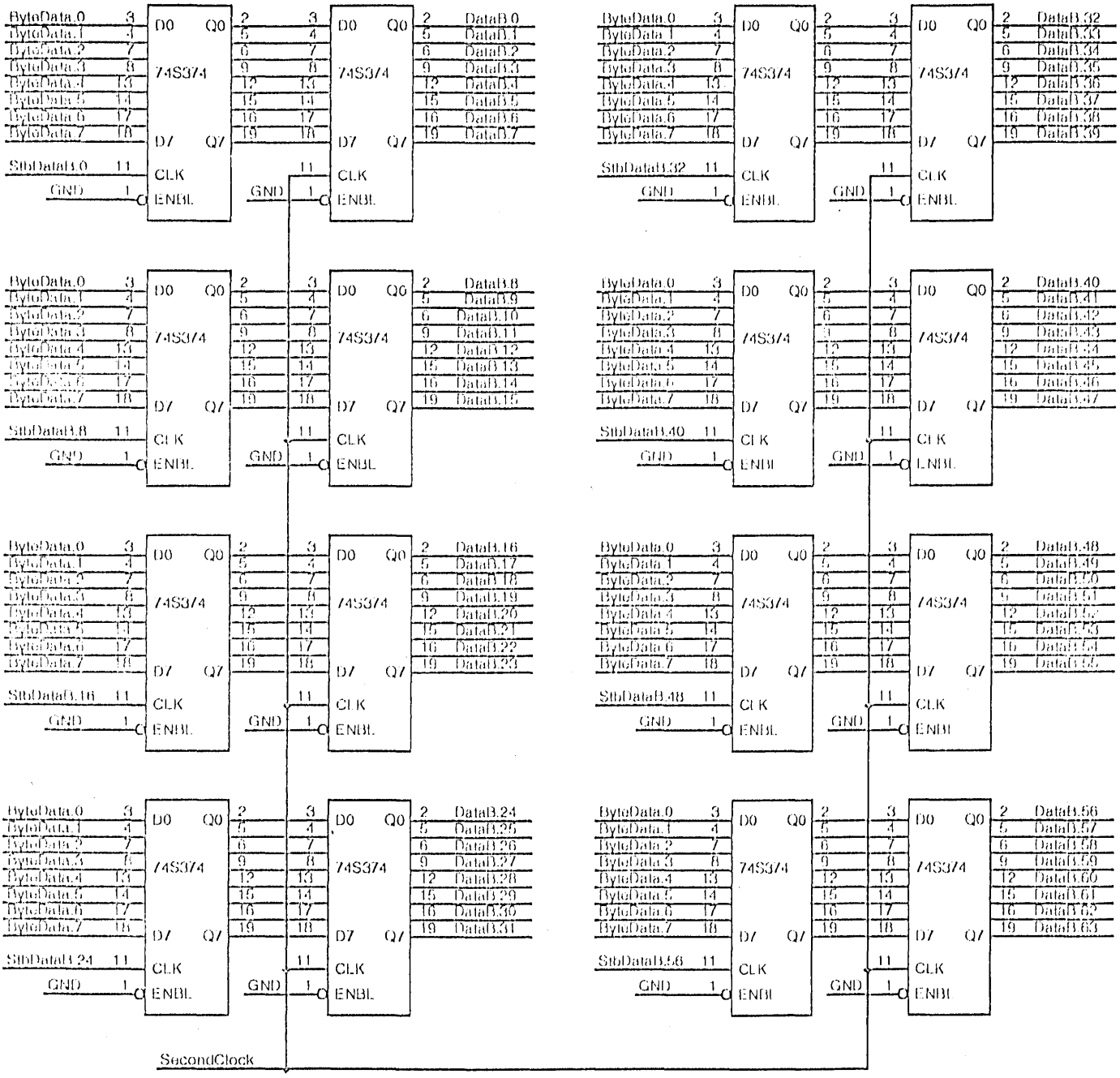
TITLE: Chip Tester
Data Register A
Early Clock

DATE:15-Oct-80 12:48

Board:
Ctest-2

Print File Written:15 Oct-80 12:48:49
LIBRARY: TTL

Drawn by PARSNIP




FILE: CTST23

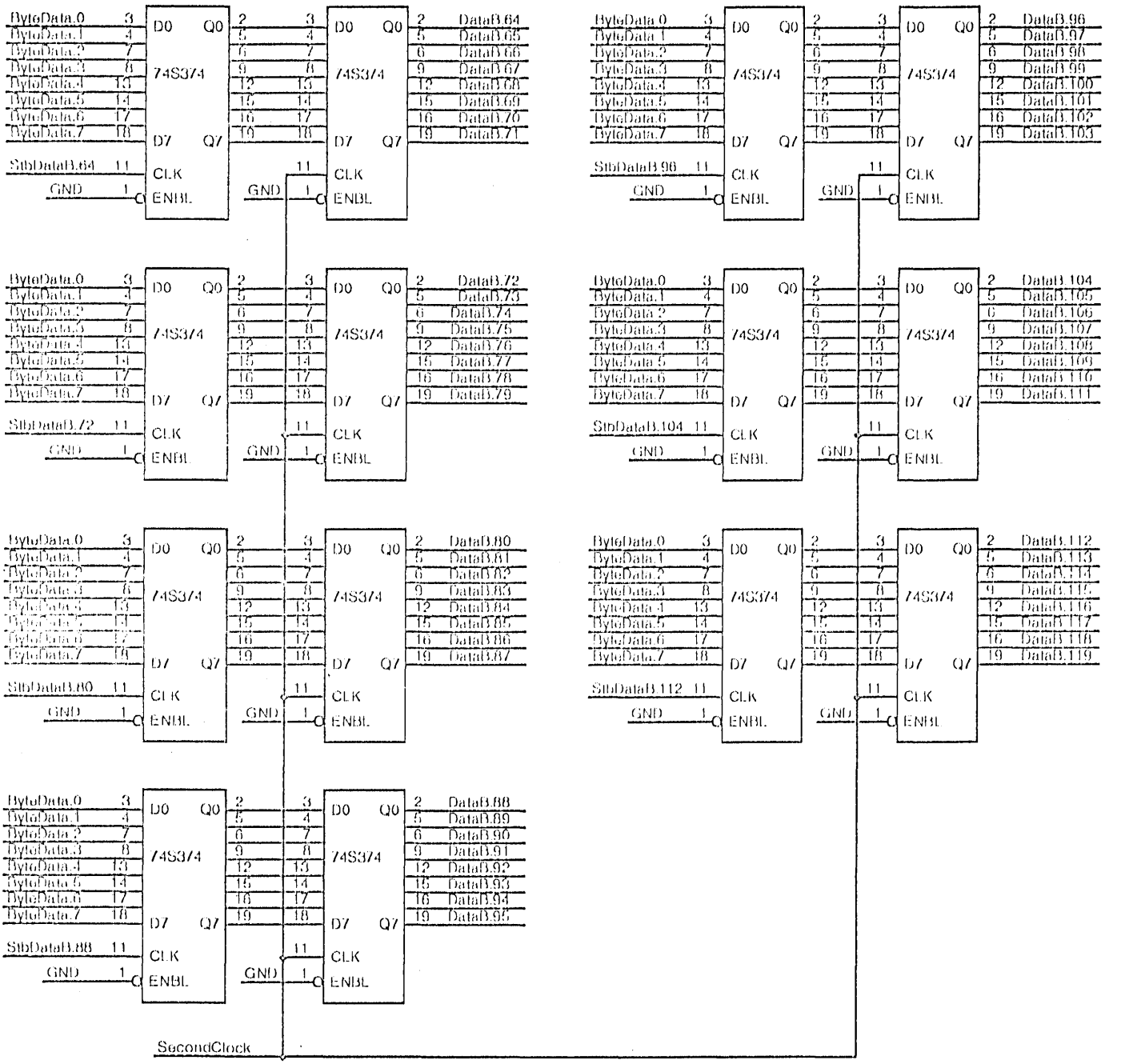
TITLE: Chip Tester
Data Register B
Second Clock

DATE: 7-Oct-80 18:09

Board:
Ctest-2

Press File Written:15 Oct 80 17:14:11
LIBRARY: TTL

Drawn by PARSNIP




FILE: CTST33

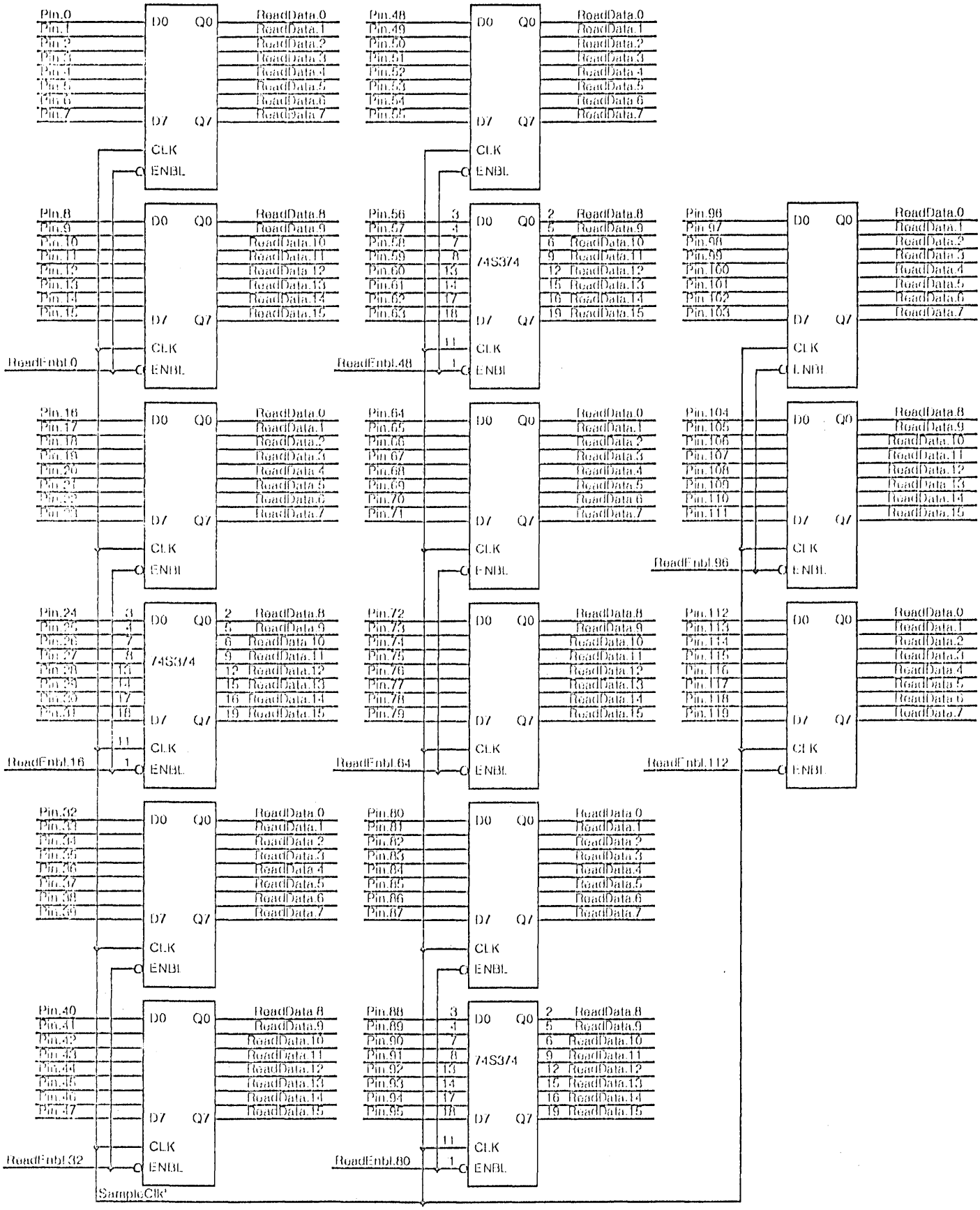
TITLE: Chip Tester
Data Register B
Second Clock

DATE:15-Oct-80 12:52

Board:
Ctest-2

Prem File Written:15 Oct 80 12:52:58
LIBRARY: TTL

Drawn by PARSNIP




FILE: CTST34

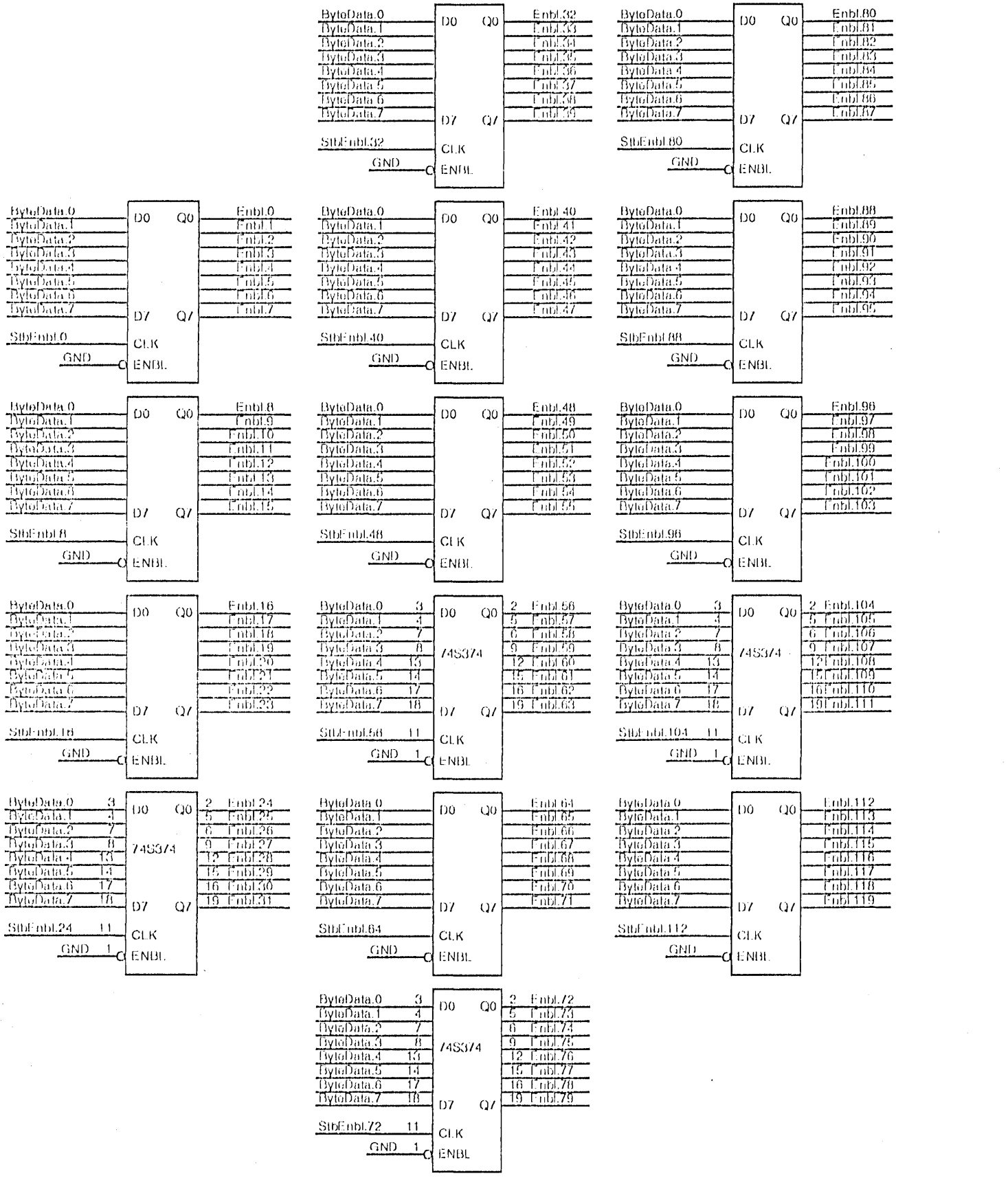
TITLE: Chip tester
Chip Pin Enables
Chip Pin Catch Reg

DATE:15-Oct-80 18:48

Board:
Ctest-2

Prem File Written:15 Oct 80 18:49:11
LIBRARY: TTL

Drawn by PARSNIP




FILE: CTST24

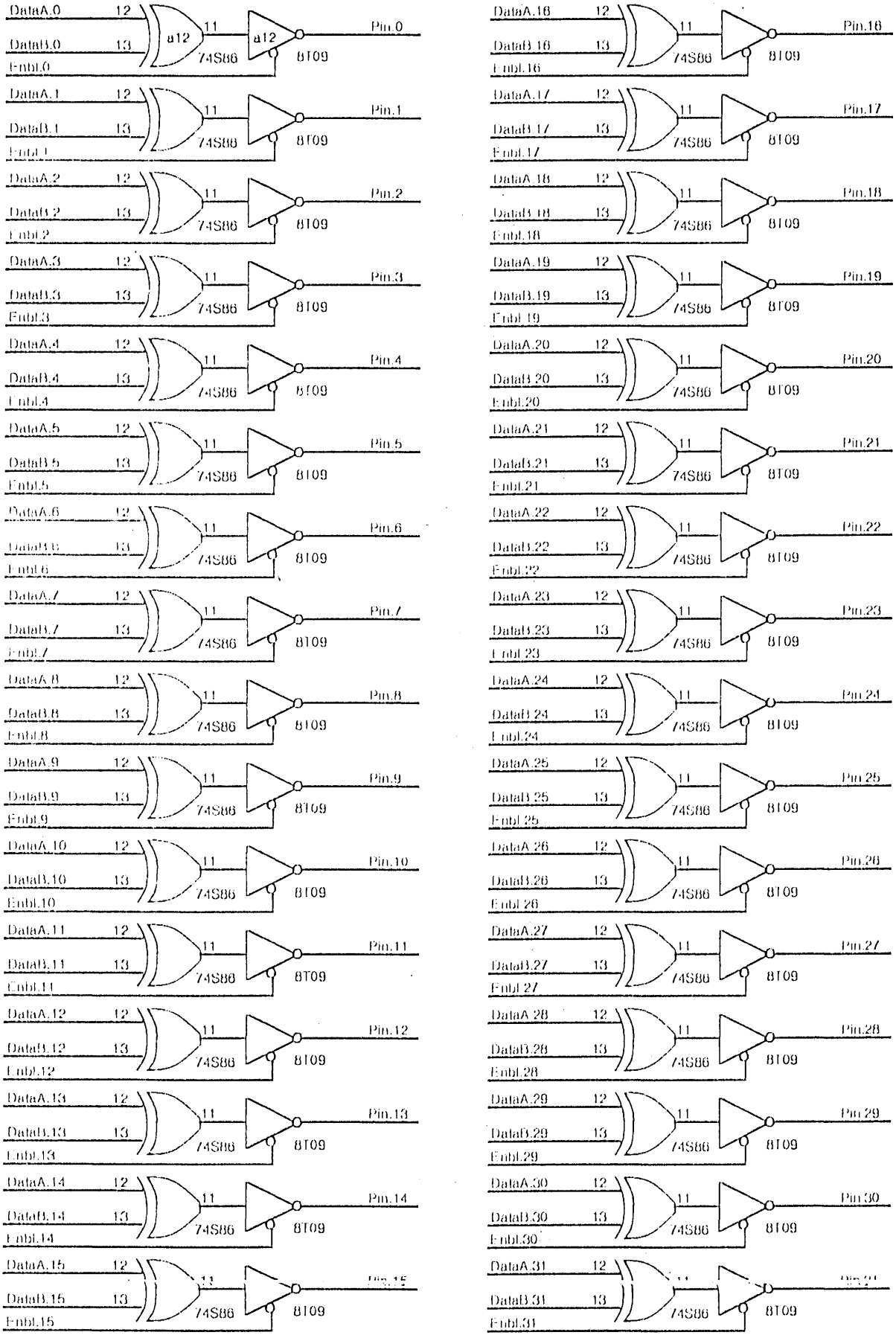
TITLE: Chip tester
Chip Pin Enables
Chip Pin Catch Reg

Board:
Ctest-2

Press File Written:15-Oct-80 17:40:07
LIBRARY: TTL

DATE:15-Oct-80 17:39

Drawn by PARSNIP



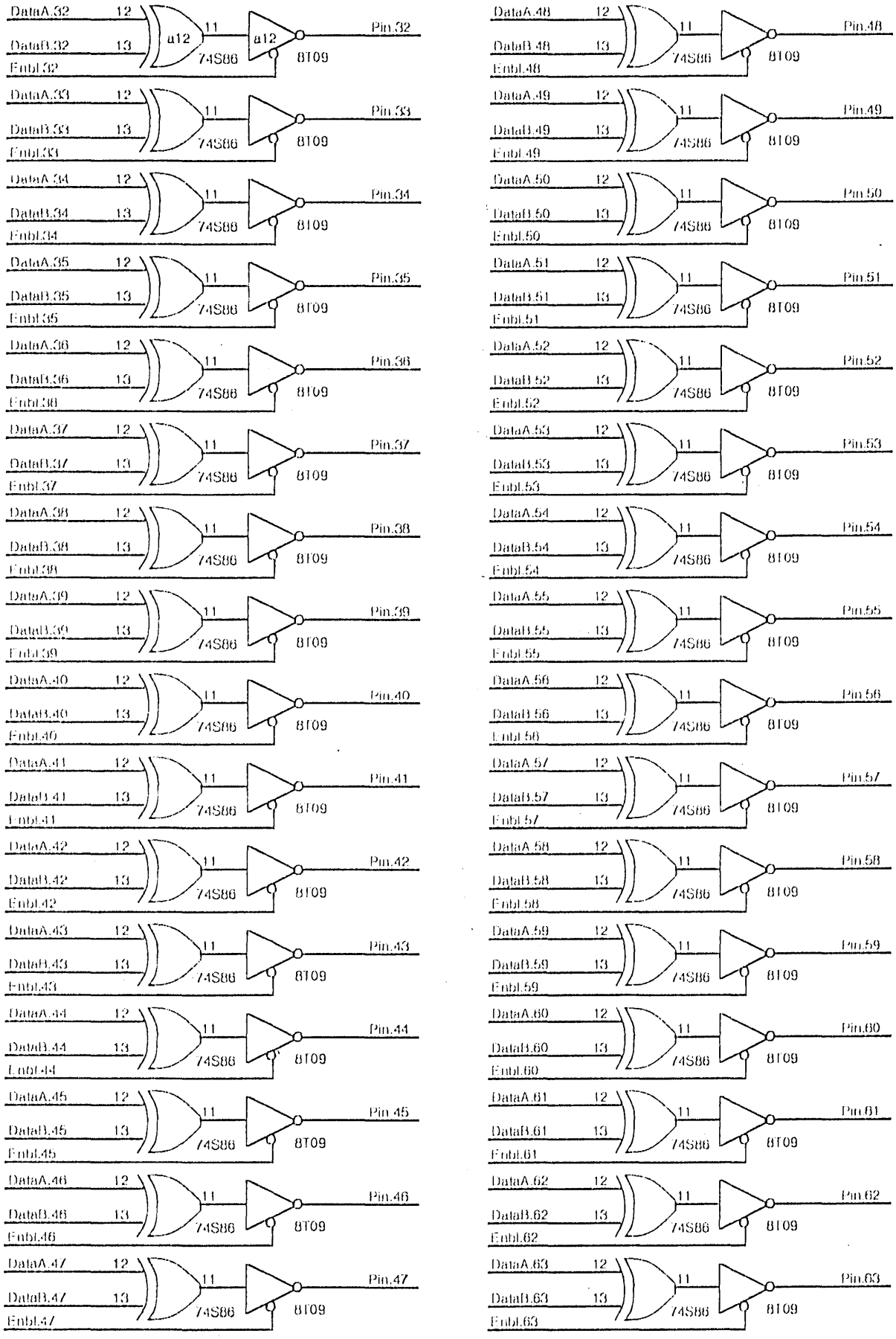

FILE: CTST21 TITLE: Chip Tester
Chip Pin Drivers

DATE:15-Oct-80 12:03

Board:
Ctest-2

Prom File Written:15 Oct 80 12:03:42
LIBRARY: TTL

Drawn by PARSNIP



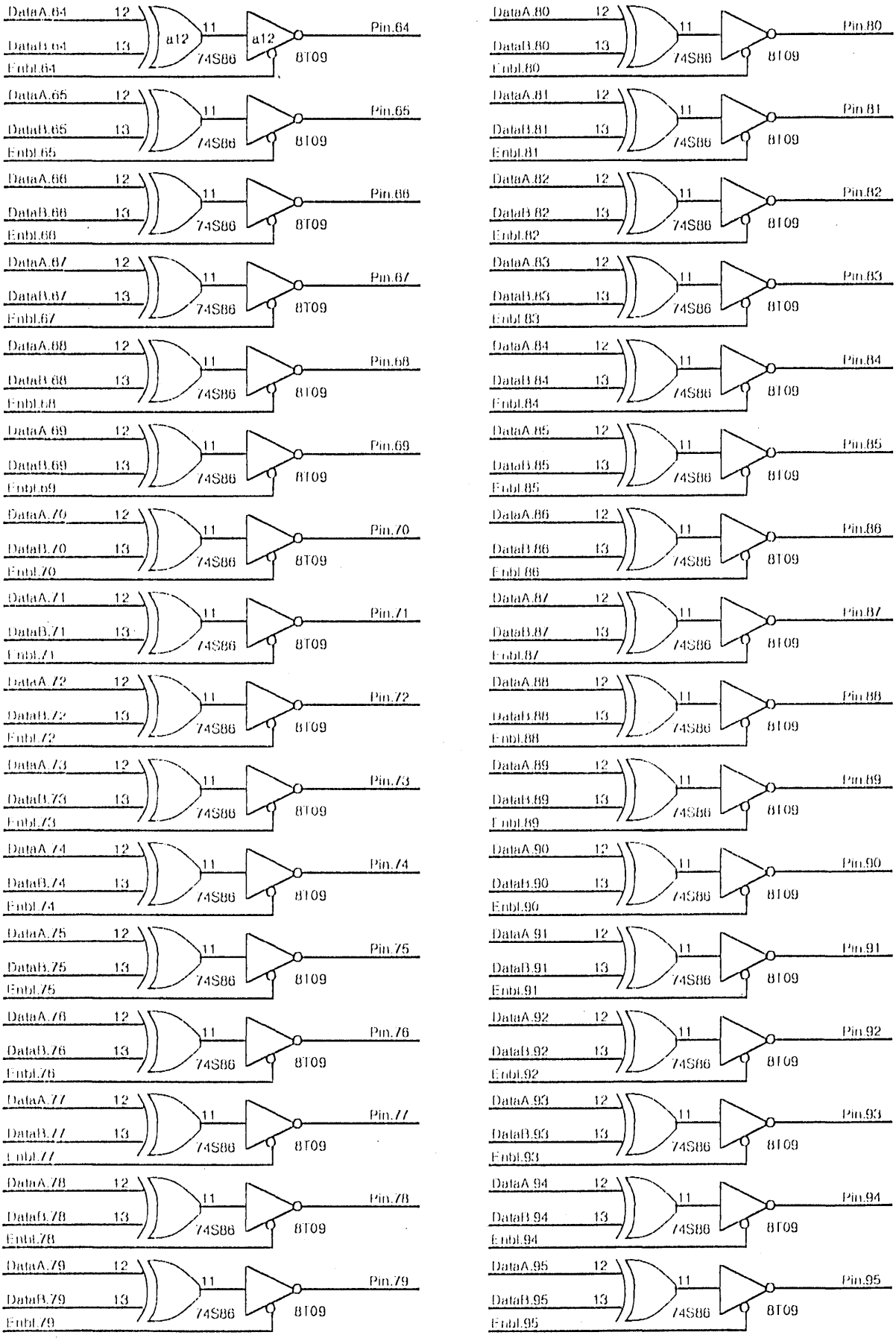

FILE: CTST25 TITLE: Chip Tester
Chip Pin Drivers

DATE:15-Oct-80 12:15

Board:
Ctest-2

Prem File Written:15-Oct-80 12:15:35
LIBRARY: TTL

Drawn by PARSNIP



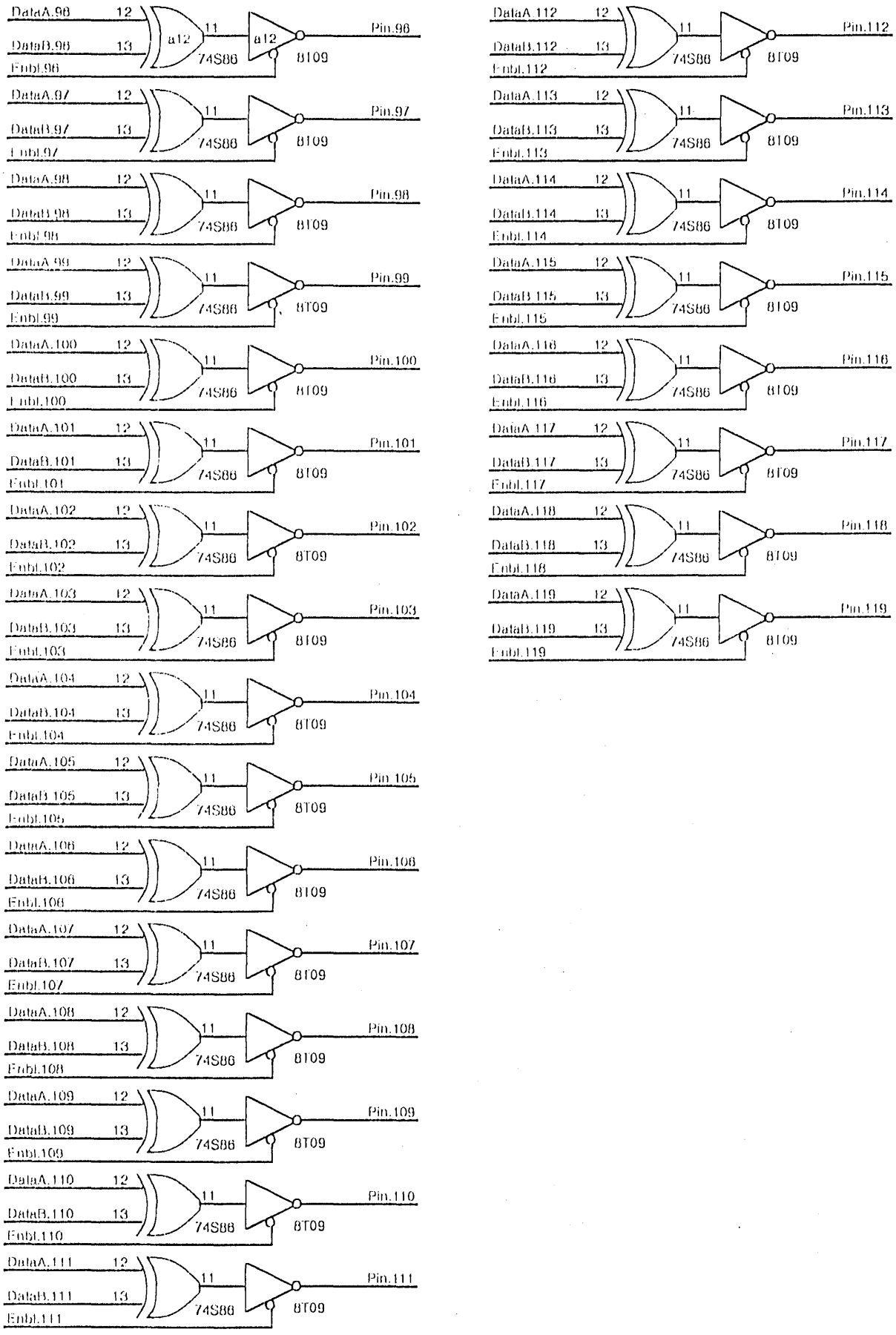

FILE: CTST31 TITLE: Chip Tester
Chip Pin Drivers

DATE:15-Oct-80 12:21

Board:
Ctest-2

Press File Written:15-Oct-80 12:22:02
LIBRARY: TTL

Drawn by PARSNIP



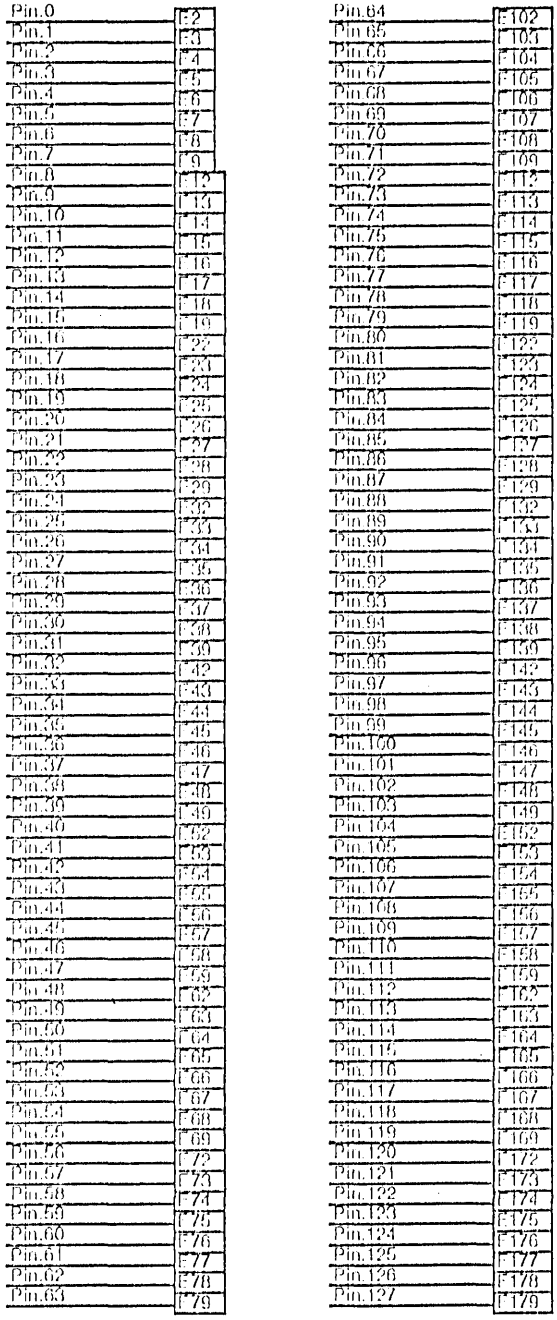

FILE: CTST35 TITLE: Chip Tester
Chip Pin Drivers

DATE:15-Oct-80 12:27

Board:
Ctest-2

Press File Written:15 Oct 80 12:27:32
LIBRARY: TTL

Drawn by PARSNIP




Pin Numbers Wrong!

FILE: CTST29

TITLE: Chip tester
Chip Pin Enables
Chip Pin Catch Reg

Board:
Ctest-2

Press File Written:15 Oct 80 19:30:56
LIBRARY: TTL

DATE:15-Oct-80 19:05

Drawn by PARSNIP