

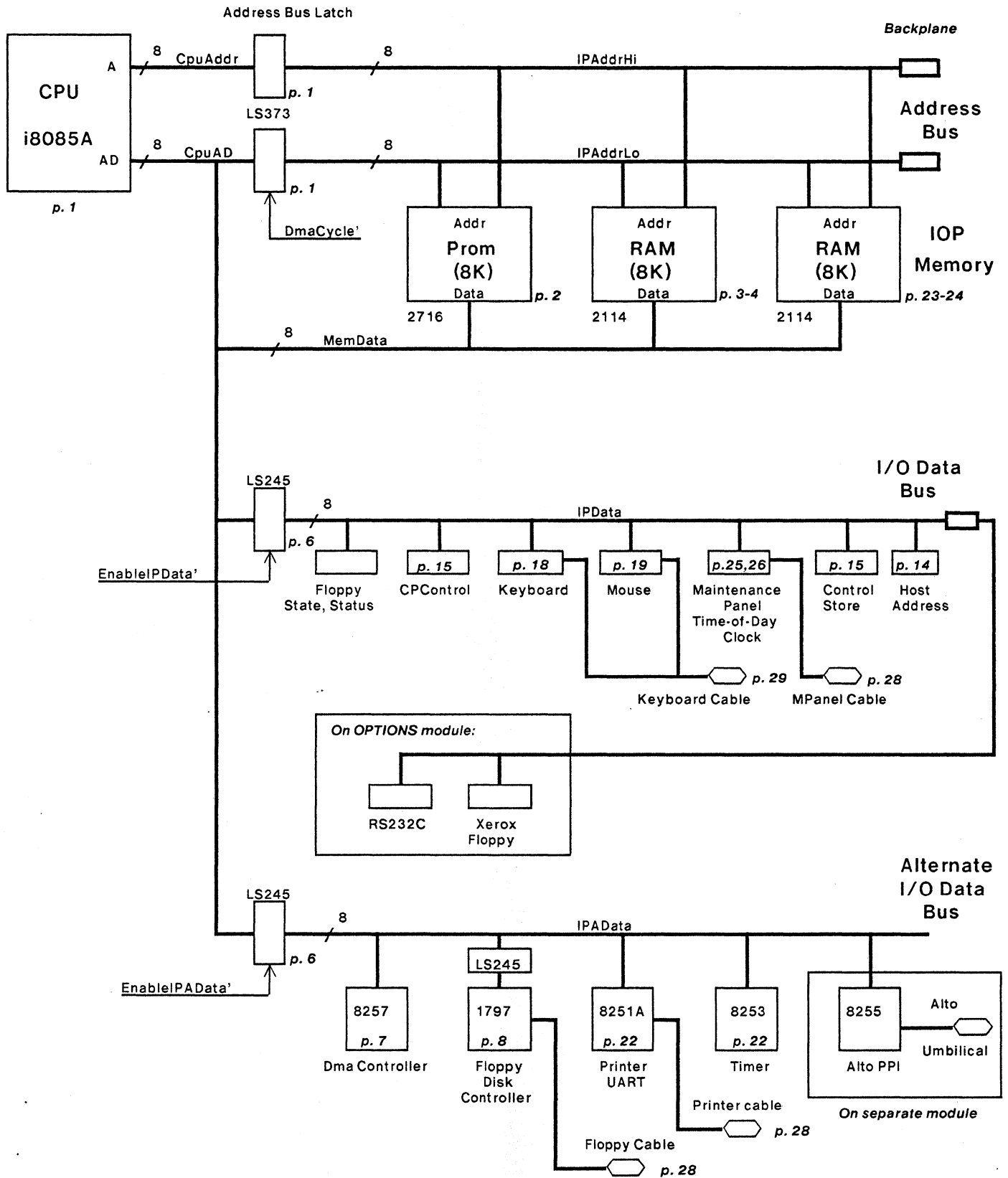
## TABLE OF CONTENTS

### SHEET

0.4	BLOCK DIAGRAM: I/O PROCESSOR DATA PATHS
0.5	BLOCK DIAGRAM: I/O PROCESSOR CONTROL ORGANIZATION
0.6	BLOCK DIAGRAM: FLOPPY CONTROLLER
0.7	BLOCK DIAGRAM: CP - IOP PORT
1	CPU
2	PROM, MEMORY CONTROL
3	4K RAM MEMORY BANKS 0 - 3
4	4K RAM MEMORY BANKS 4 - 7
5	I/O CONTROL, MEMORY CONTROL
6	I/O DATA BUS CONTROL
7	DMA CONTROLLER
8	FLOPPY DISK CONTROLLER
9	FLOPPY DISK CONTROLLER WRITE COMPARE
10	FLOPPY DISK CONTROLLER MISCELLANEOUS
11	FLOPPY DISK RECEIVERS/DRIVERS
12	FLOPPY DISK CONTROLLER DATA SEPARATOR
13	DMA TEST REGISTER
14	INTERRUPT REQUEST REGISTER
15	CP CONTROL, CONTROL STORE
16	CP - IOP PORT - 1
17	CP - IOP PORT - 2
18	KEYBOARD INTERFACE
19	MOUSE INTERFACE
20	TIME-OF-DAY/MAINTENANCE PANEL INTERFACE
21	MISCELLANEOUS CPU CONTROL
22	PRINTER INTERFACE
23	4K RAM MEMORY BANKS 8 - 11
24	4K RAM MEMORY BANKS 12 - 15
25	TIME-OF-DAY CLOCK - 1
26	TIME-OF-DAY CLOCK - 2
27	DISCRETES, I/O CONNECTORS - 1
28	I/O CONNECTORS - 2
29	FUSES, POWER SUPPLY, CONNECTORS
30	POWER UP/DOWN CIRCUIT
31	FILTER CAPACITORS
32	TEST POINT LISTING, CONNECTOR LISTING, SIGNAL LISTING

IOP-0.3B.SIL

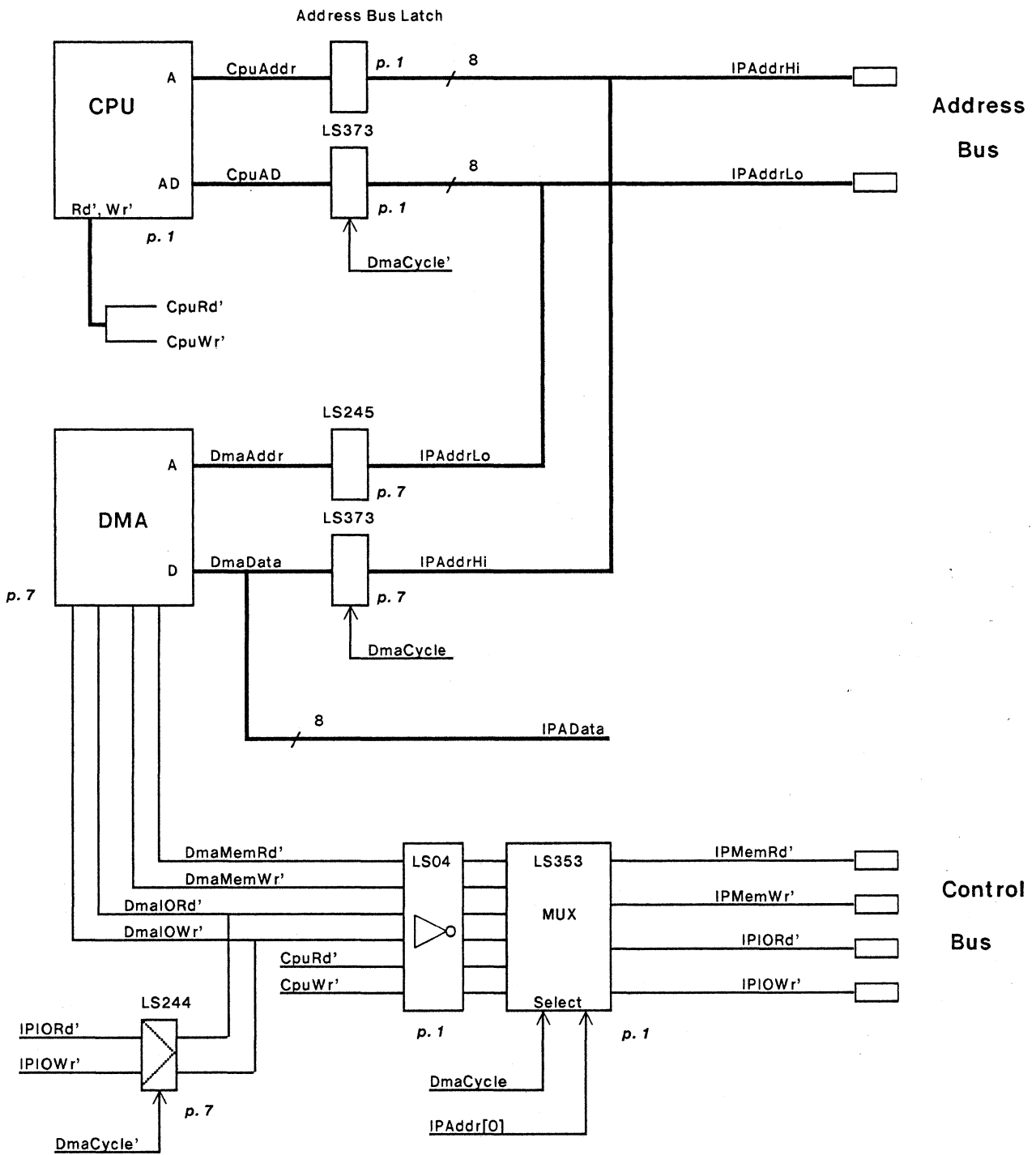
<b>XEROX</b>	PROPRIETARY NOTE ON COVER SHEET APPLIES TO ALL SHEETS		DWG SIZE <b>A4</b>	DWG NO. <b>156P11446</b>	SHEET REV. <b>B</b>
	TITLE	<b>SCHEMATIC, IOP</b>	SHEET	<b>0.3</b> OF	<b>B</b>



**BLOCK DIAGRAM: I/O PROCESSOR DATA PATHS**

IOP-0.4B.SIL

XEROX	PROPRIETARY NOTE ON COVER SHEET APPLIES TO ALL SHEETS	DWG SIZE A4	DWG NO. 156P11446	SHEET REV. B
	TITLE SCHEMATIC, IOP		SHEET 0.4 OF	



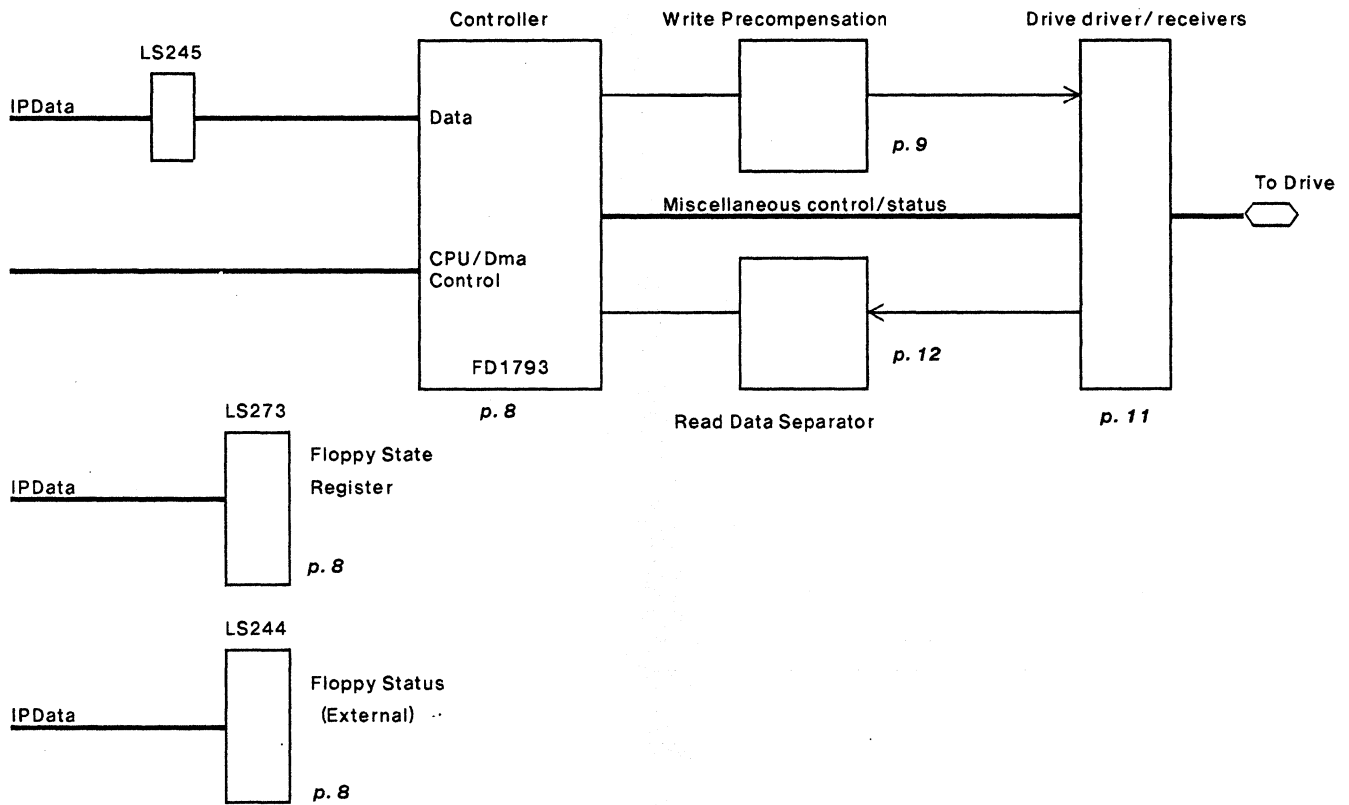
BLOCK DIAGRAM: I/O PROCESSOR CONTROL ORGANIZATION

IOP-0.5B.SIL

XEROX	PROPRIETARY NOTE ON COVER SHEET APPLIES TO ALL SHEETS	DWG SIZE A4	DWG NO. 156P11446	SHEET REV. B
	TITLE SCHEMATIC, IOP		SHEET 0.5 OF	

# I/O Processor Floppy Disk Controller

## Block Diagram



**BLOCK DIAGRAM: FLOPPY CONTROLLER**

IOP-0.6B.SIL

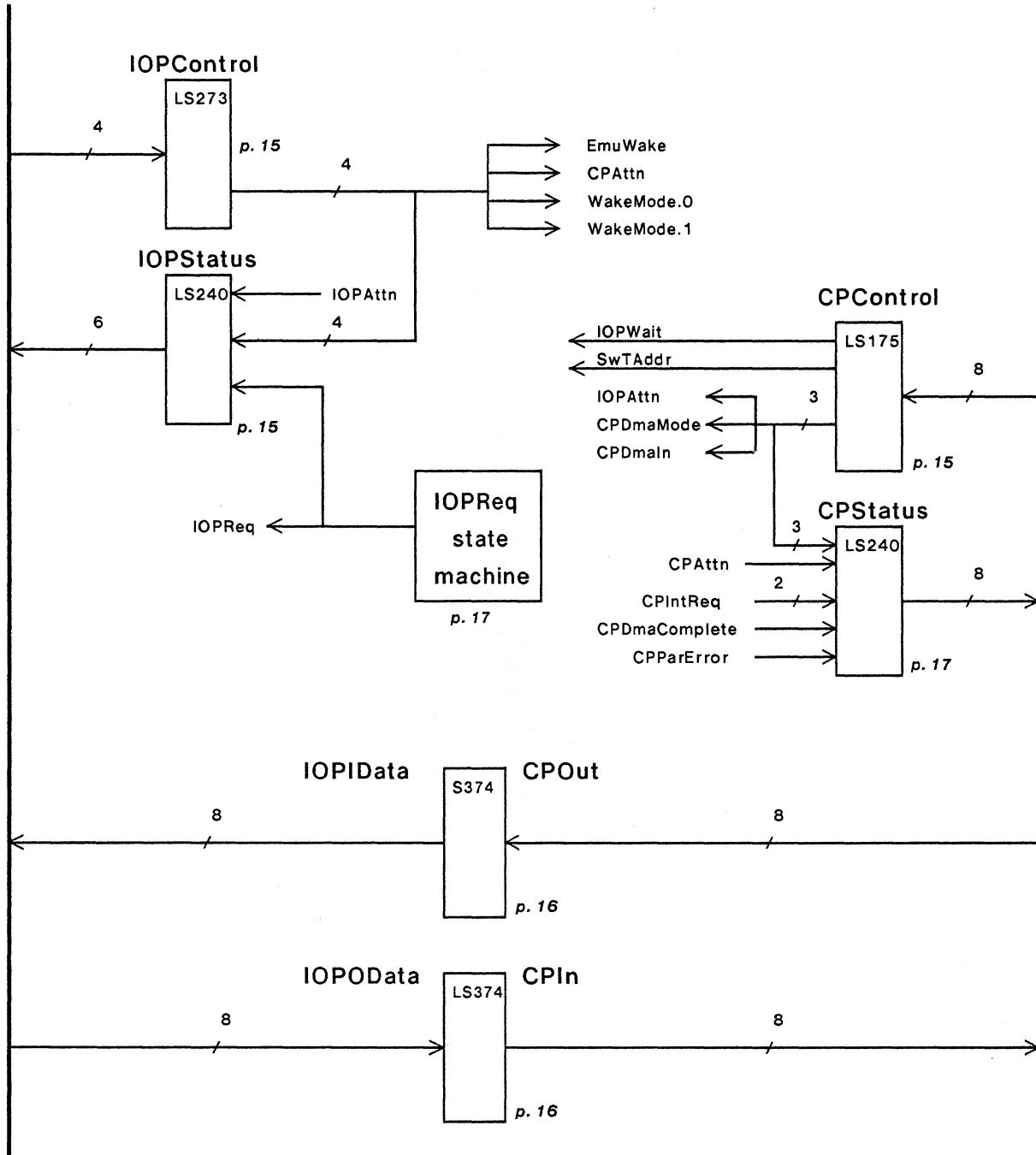
<b>XEROX</b>	PROPRIETARY NOTE ON COVER SHEET APPLIES TO ALL SHEETS		DWG SIZE <b>A4</b>	DWG NO. 156P11446		SHEET REV. <b>B</b>
	TITLE SCHEMATIC, IOP			SHEET <b>0.6</b> OF		

Central Processor

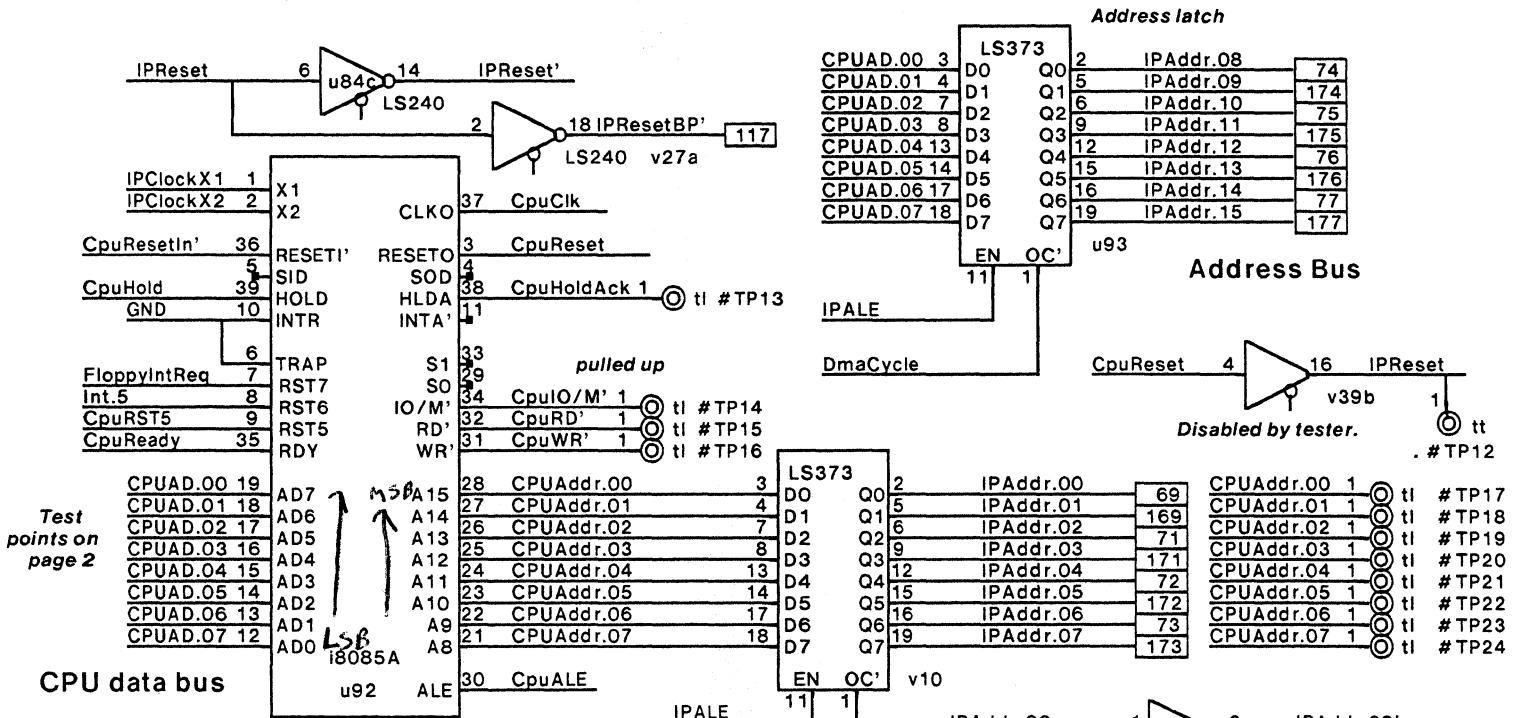
I/O Processor

X-bus

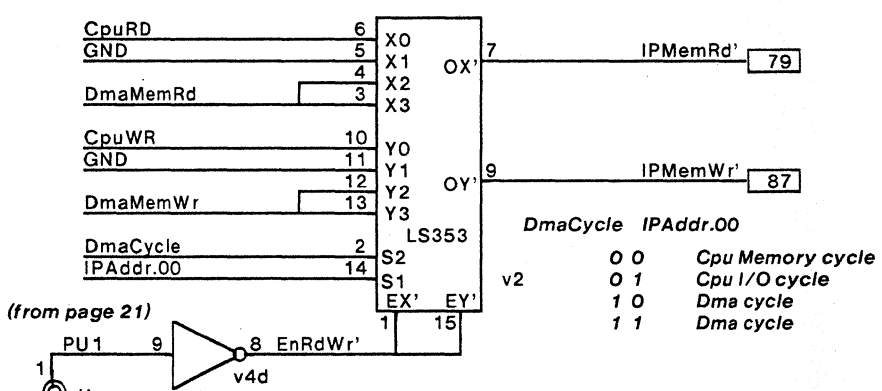
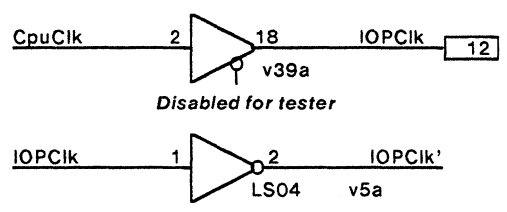
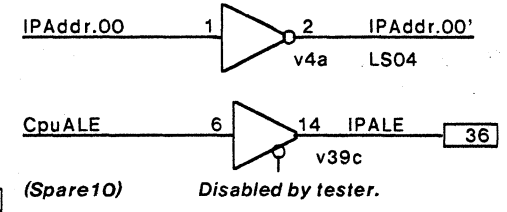
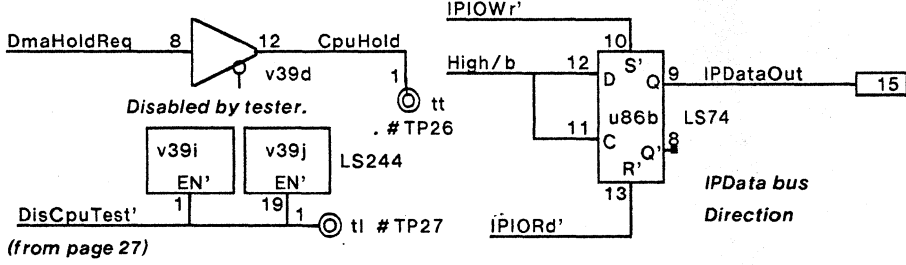
IOPData bus



XEROX	PROPRIETARY NOTE ON COVER SHEET APPLIES TO ALL SHEETS	DWG SIZE A4	DWG NO. 156P11446	SHEET REV. B
	TITLE SCHEMATIC, IOP		SHEET 0.7 OF	

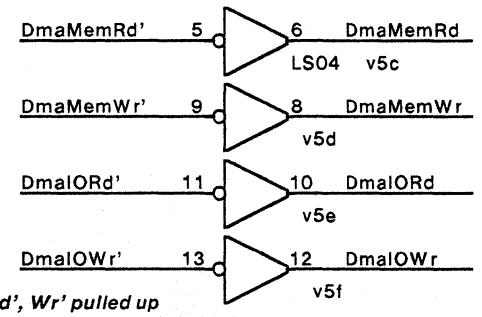
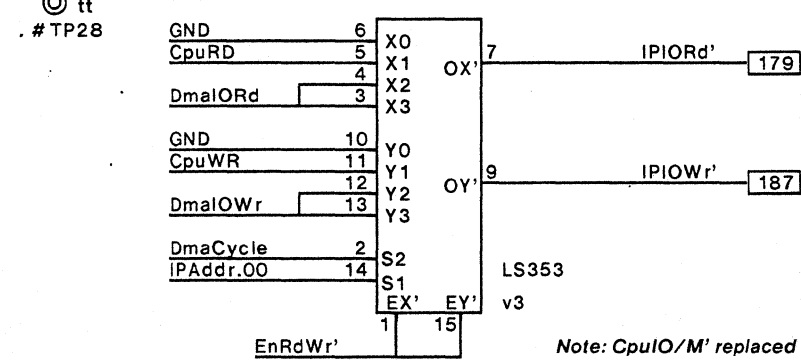
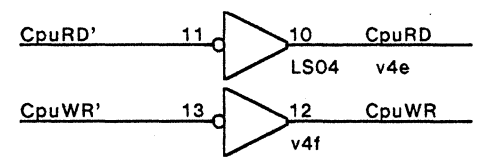


Note: RST 5 is MouseHalt OR CPAtnn (page 14)  
RST 6 is for RS232C Interrupt (Int.5, page 14)  
RST 7 is for Floppy Interrupt



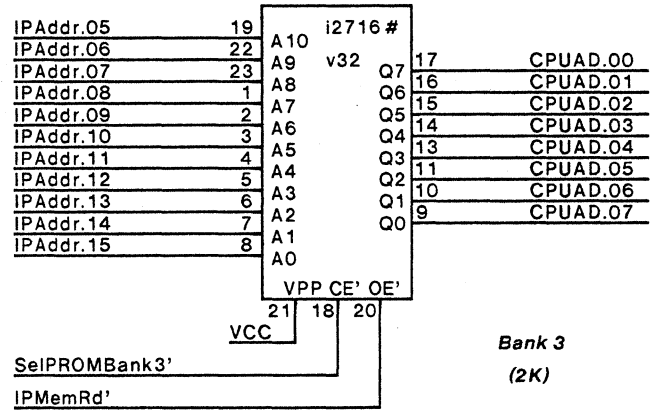
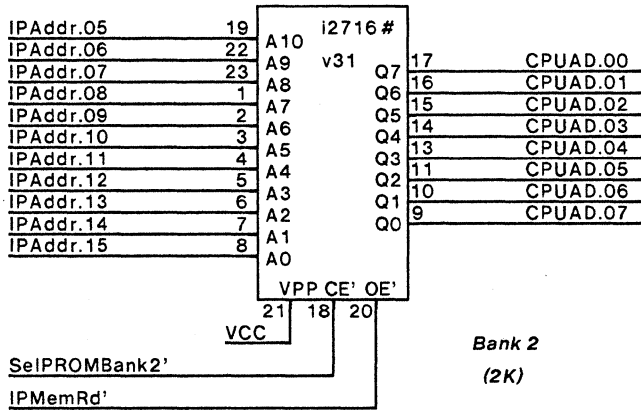
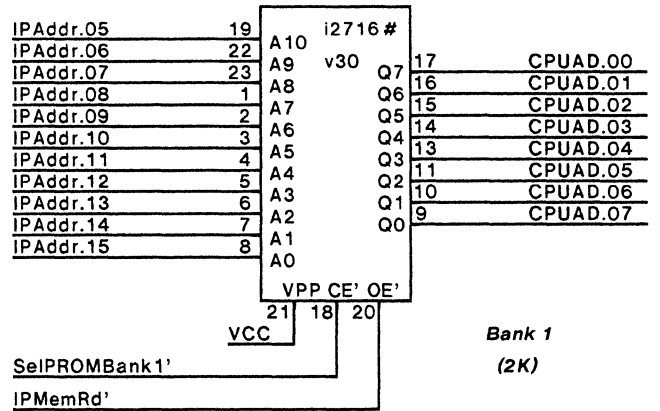
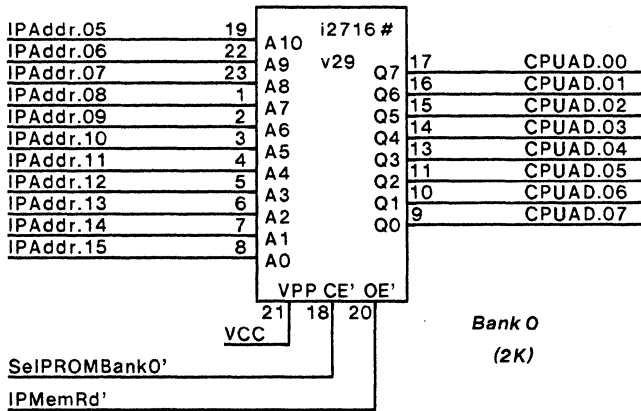
**DmaCycle IPAddr.00**

0 0	Cpu Memory cycle
0 1	Cpu I/O cycle
1 0	Dma cycle
1 1	Dma cycle



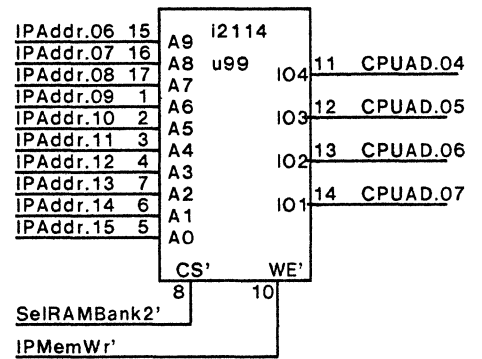
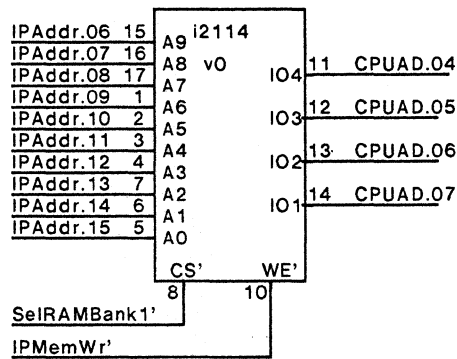
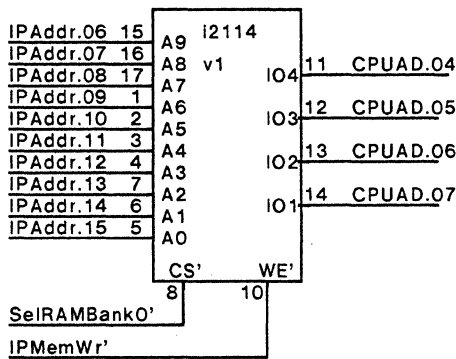
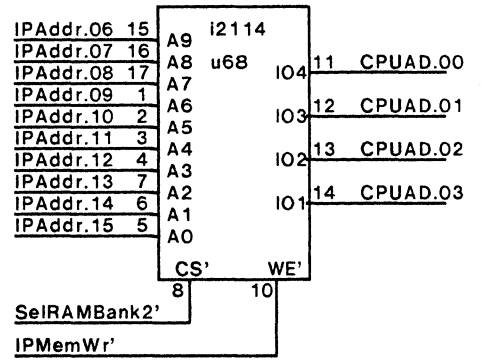
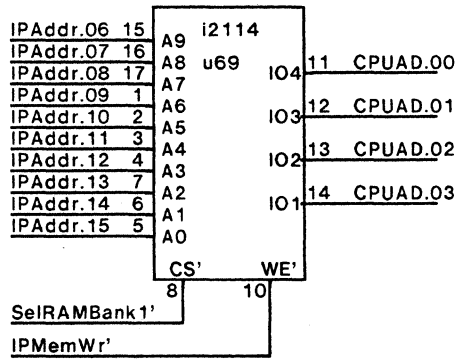
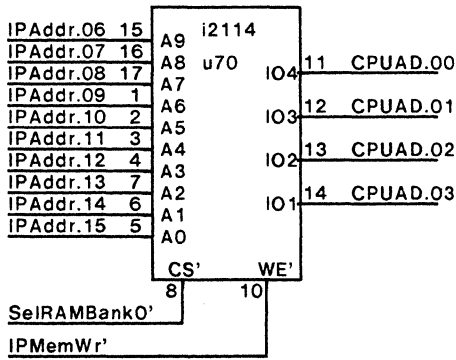
Note: CpuIO/M' replaced by IPAddr.00.  
See note on Address space.

Prom



CPUAD.00	1	tl	# TP3
CPUAD.01	1	tl	# TP4
CPUAD.02	1	tl	# TP5
CPUAD.03	1	tl	# TP6
CPUAD.04	1	tl	# TP7
CPUAD.05	1	tl	# TP8
CPUAD.06	1	tl	# TP9
CPUAD.07	1	tl	# TP10

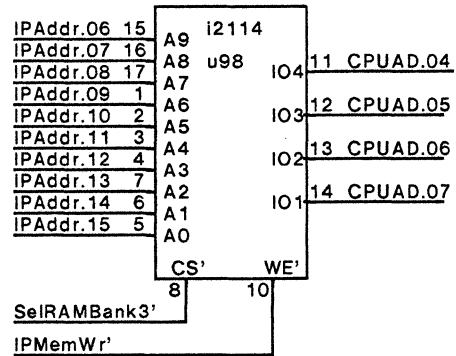
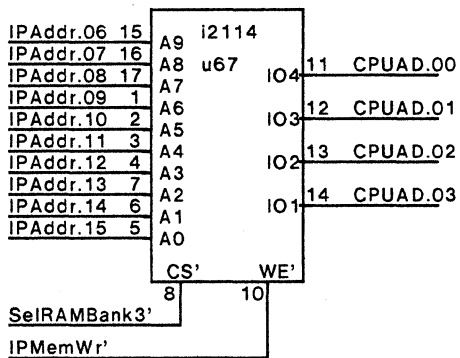
# RAM - Banks 0 - 3



Bank 0

Bank 1

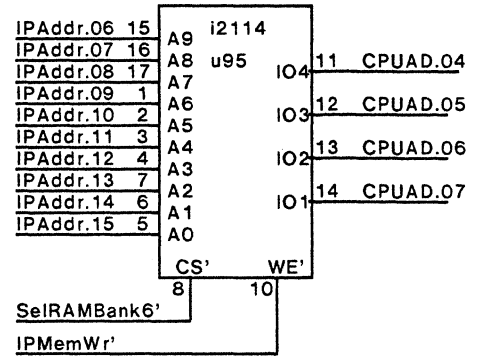
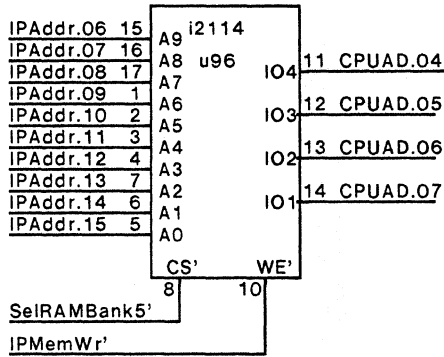
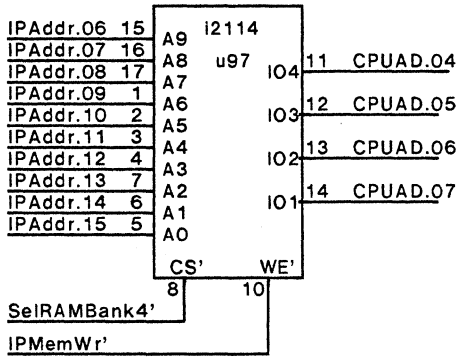
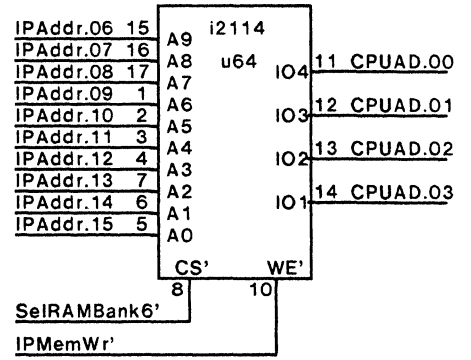
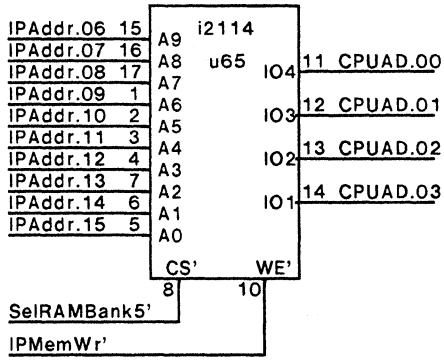
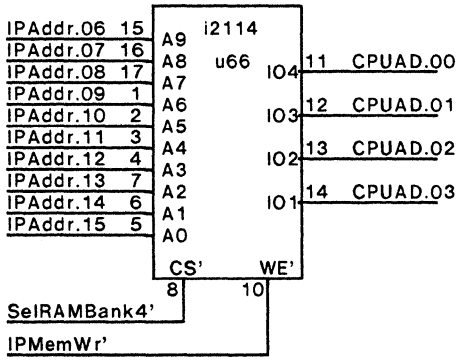
Bank 2



Bank 3



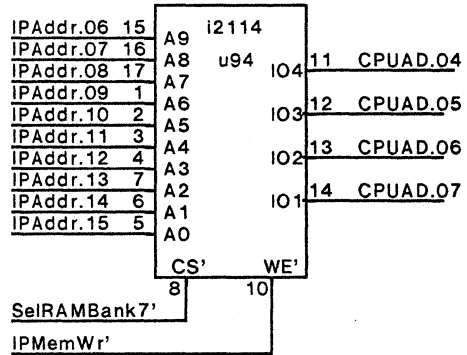
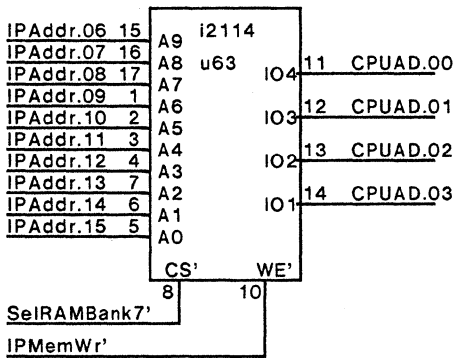
RAM - Banks 4 - 7



Bank 4

Bank 5

Bank 6

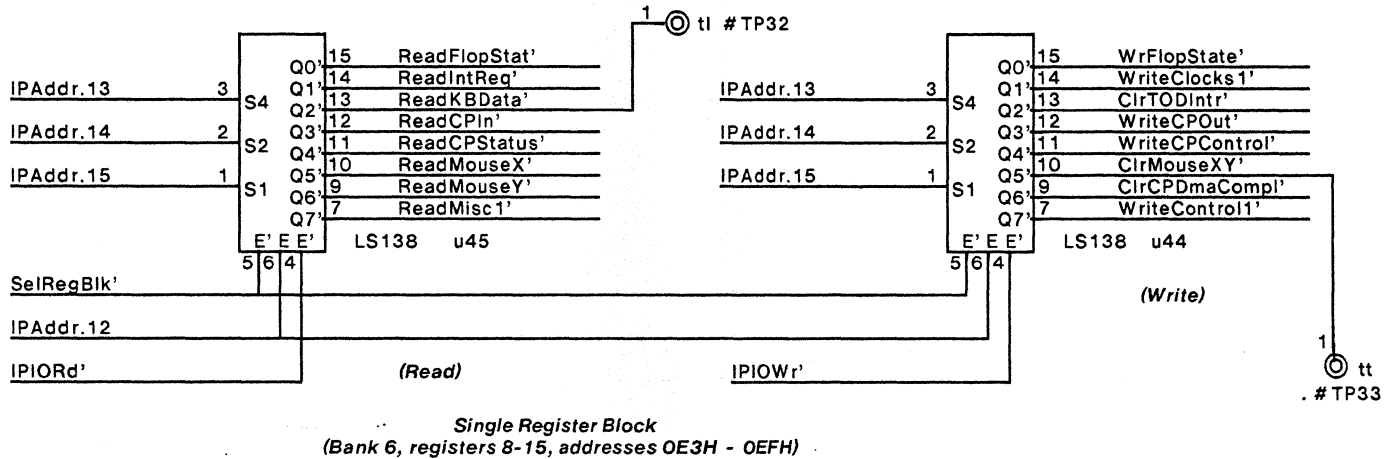
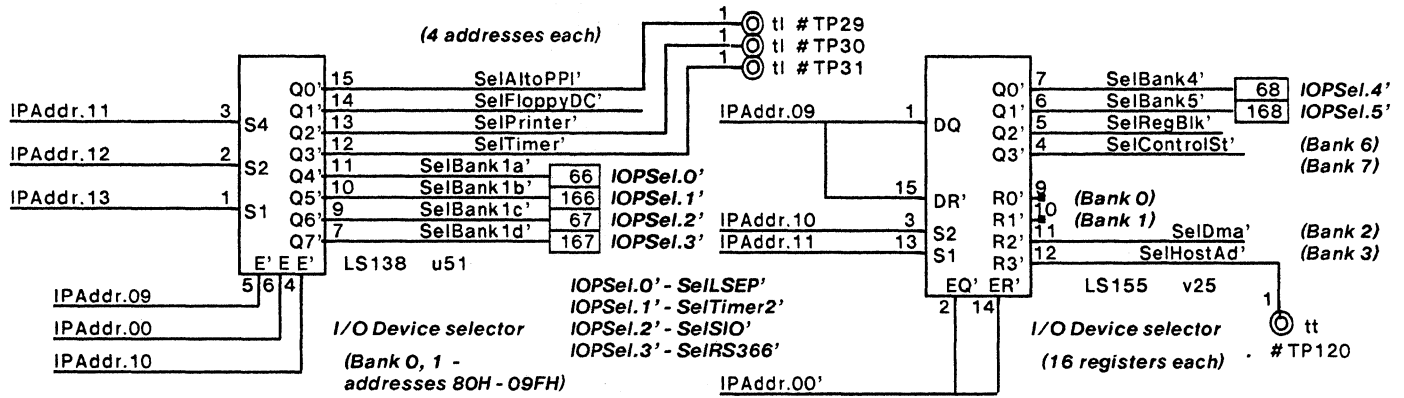


Bank 7

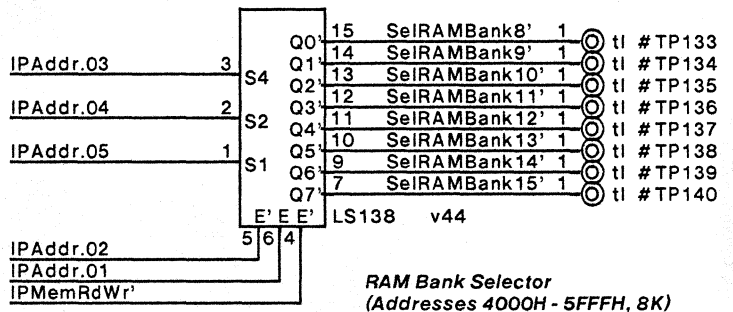
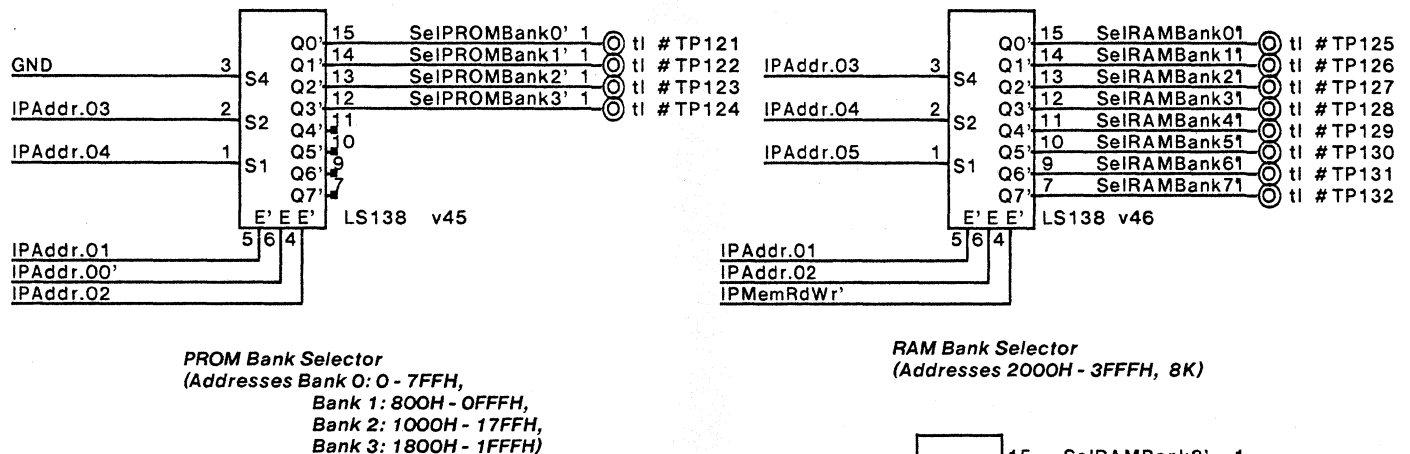
Note: RAM Banks 8 - 15 on pages 23 and 24.

## I/O Control

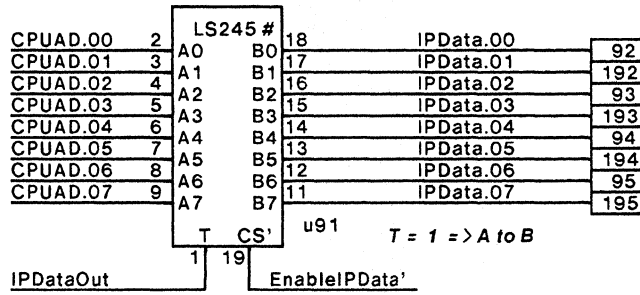
(Depends on Addr[0] = Addr[8], etc. for I/O. IPAddr[0] = 1 for I/O addresses.)



## Memory Control

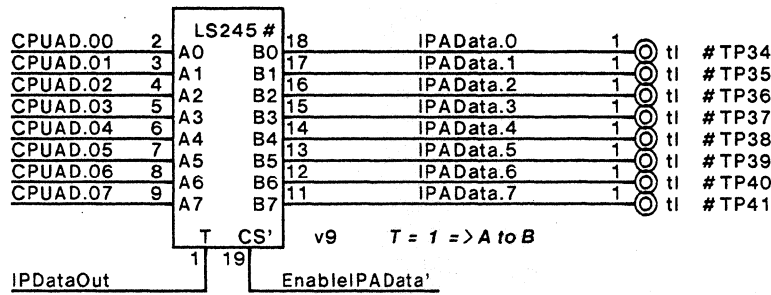


Bidirectional bus-driver



I/O  
Data Bus  
(IOPData.xx on backplane)

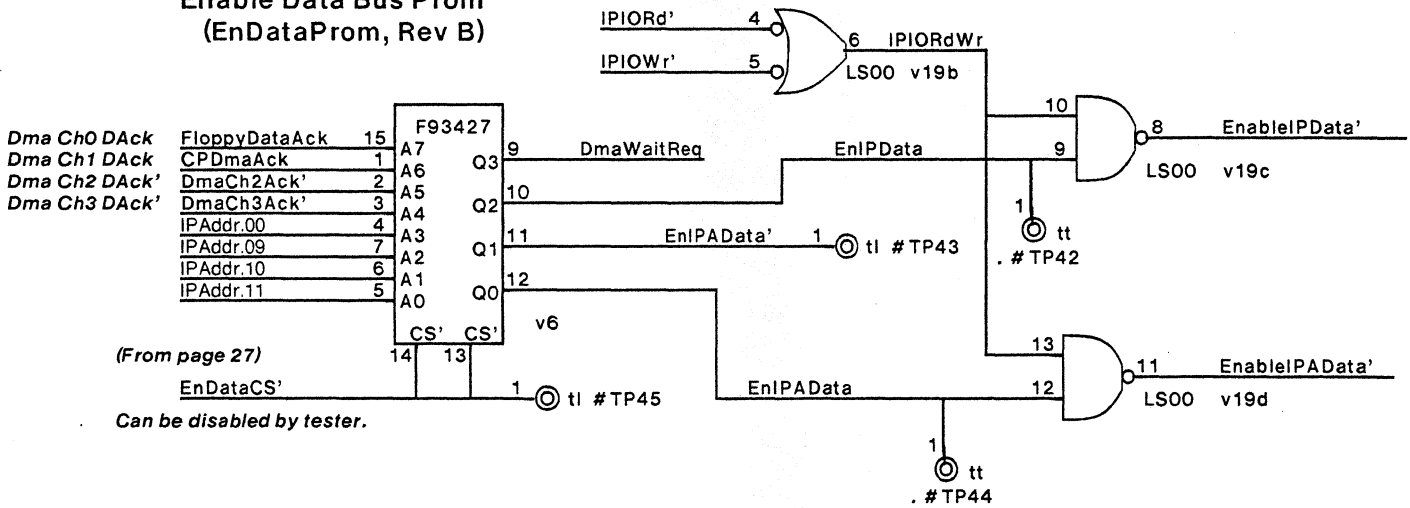
Bidirectional bus-driver



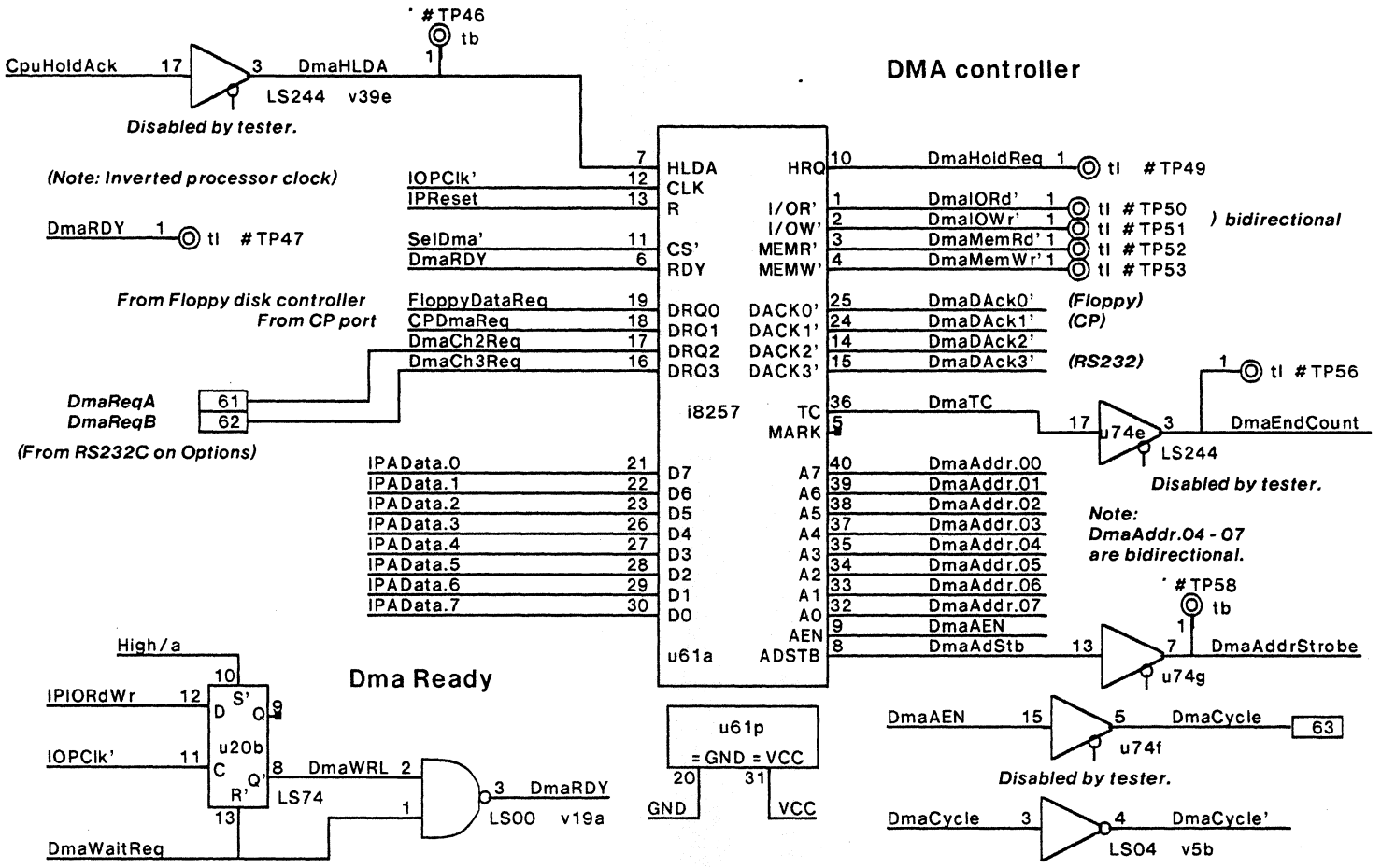
Alternate  
I/O  
Data Bus

DmaWaitReq 1 tl # TP141

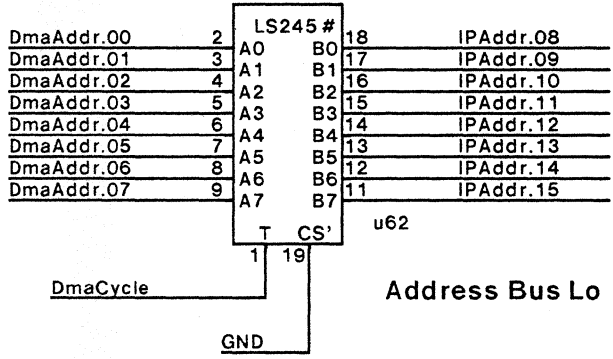
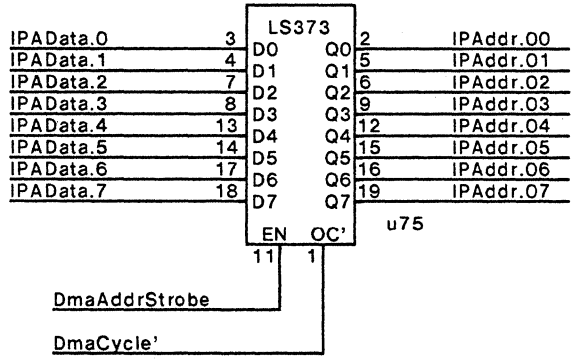
Enable Data Bus Prom  
(EnDataProm, Rev B)



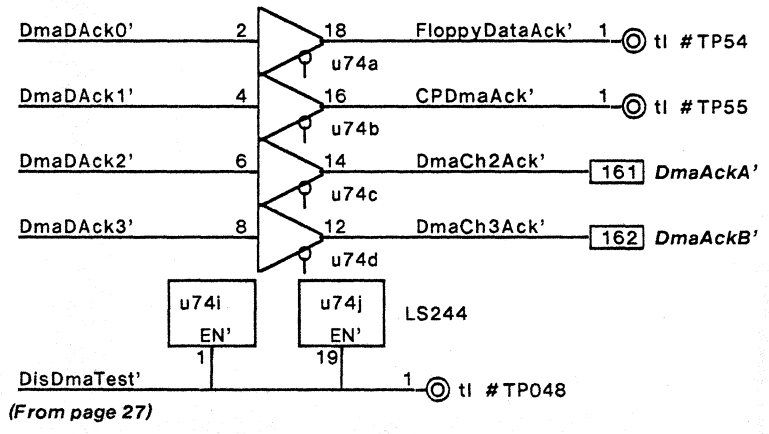
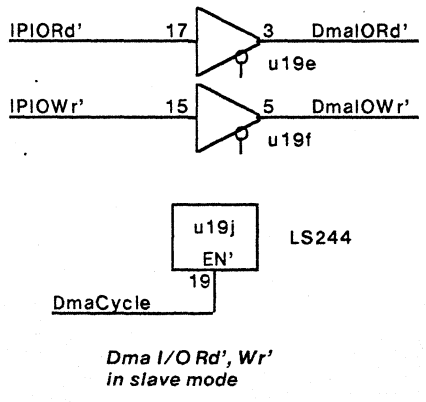
Note: Do not use DMA memory addresses in the I/O Address space.



**Address Bus Hi**

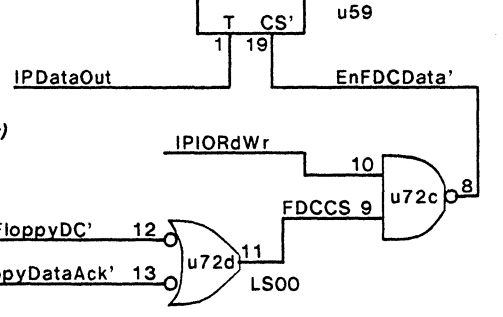
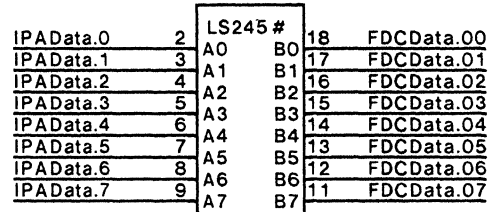
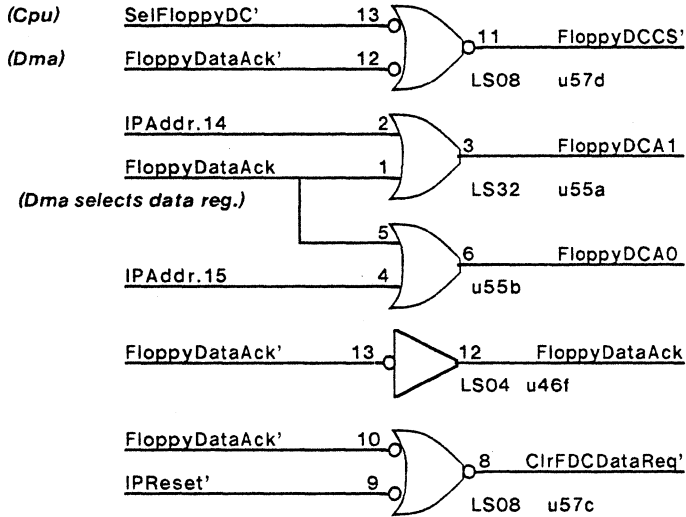
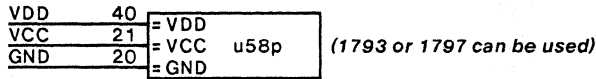
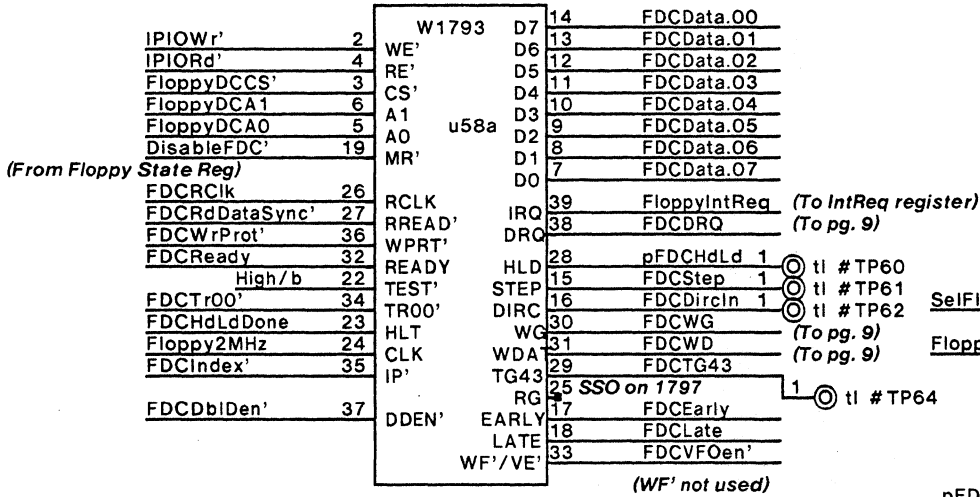


This logic disabled by tester.

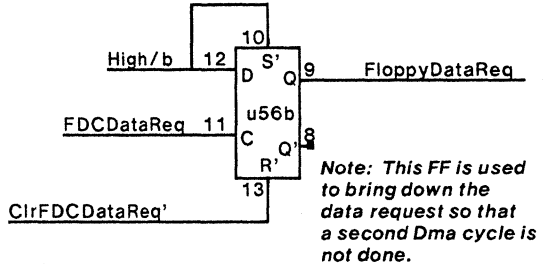
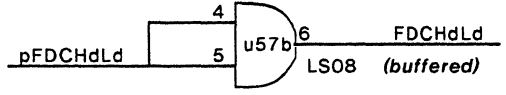


# Floppy Controller

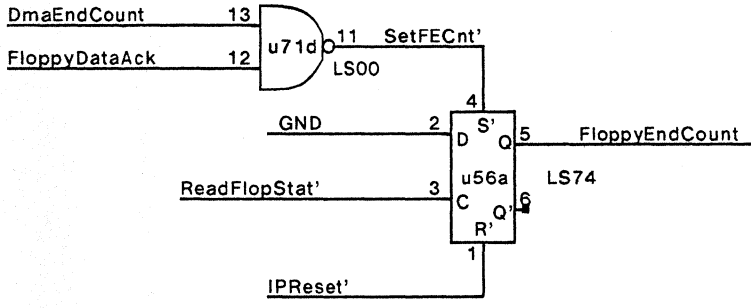
FloppyDataReq  
FloppyIntReq ) are pulled up  
FDCVFOen'



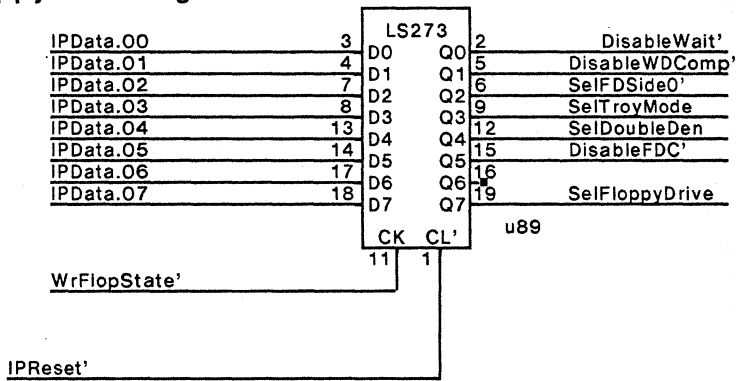
Note: If EnDataProm not used, then use inverted FloppyDCCS' here



Note: This FF is used to bring down the data request so that a second Dma cycle is not done.



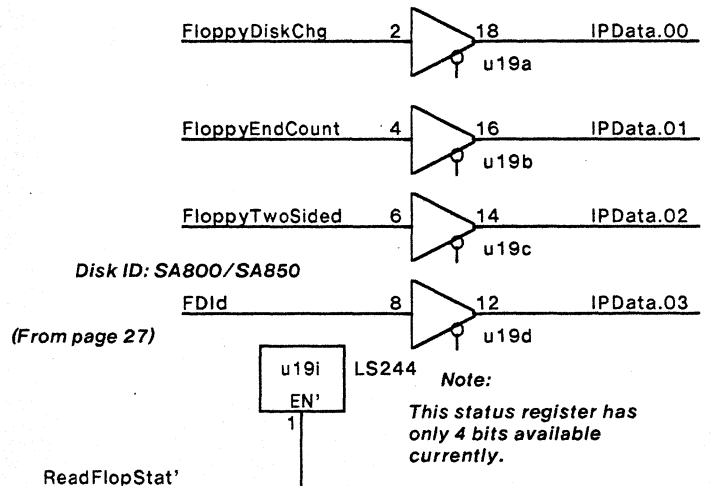
## Floppy State Register

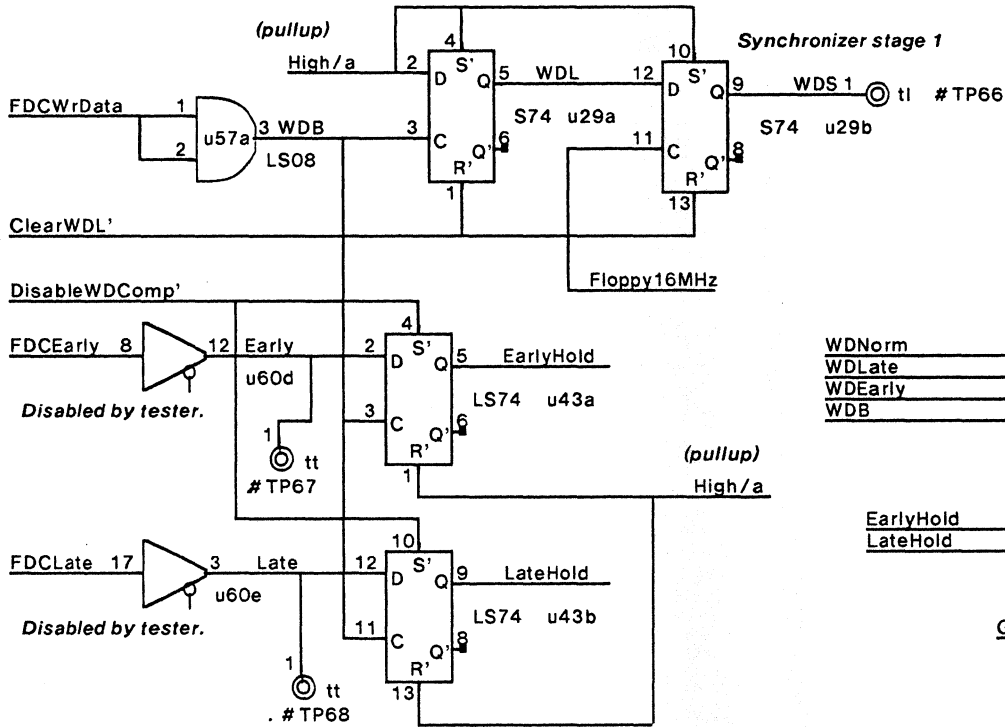


State register format:

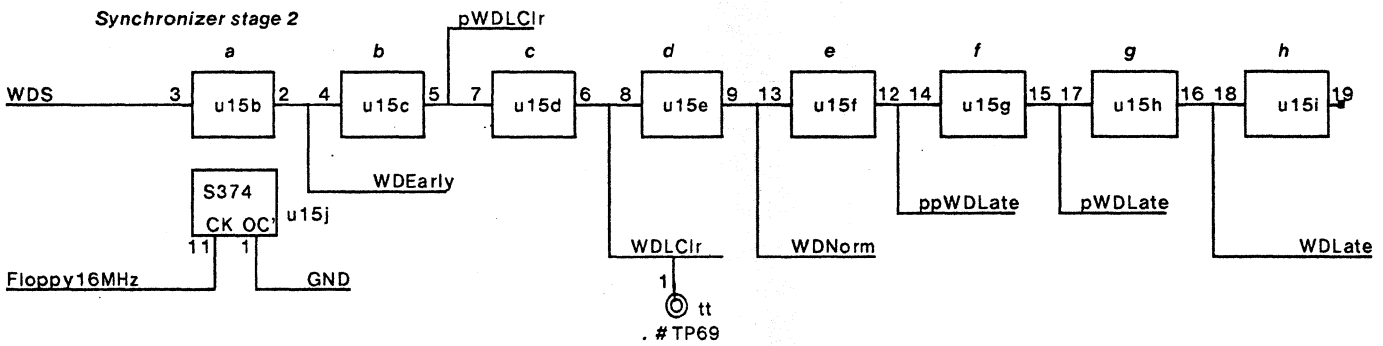
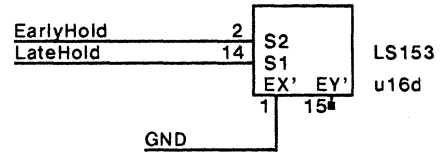
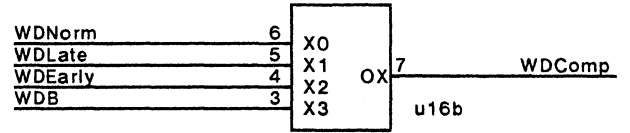
- Bit 0 - Enable Cpu Waits
- Bit 1 - Enable Write PreComp
- Bit 2 - Select Side 1
- Bit 3 - Select Troy mode
- Bit 4 - Select Double Density
- Bit 5 - Enable Floppy Controller
- Bit 6 - (unused)
- Bit 7 - Enable Floppy Drive

## Floppy Status

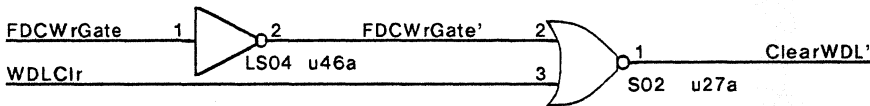




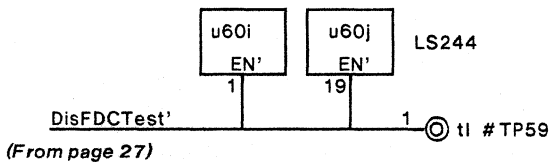
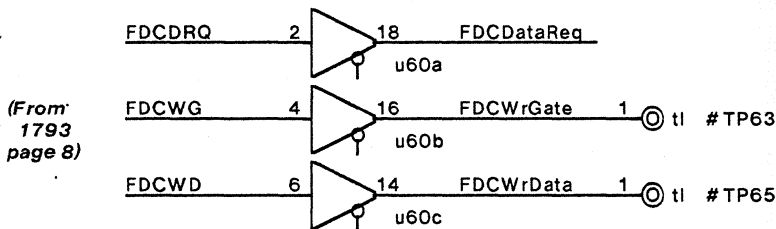
Phase Jitter = 62.5 ns (+/- 31.25 ns)  
 Precompensation = 187.5 ns +/- jitter  
 WriteData Pulse width = 187.5 ns  
 (See DandIOP39.silx for timing)



Note: e4 (S74), f3 (S374), i3 (S02)  
 must be Schottky.

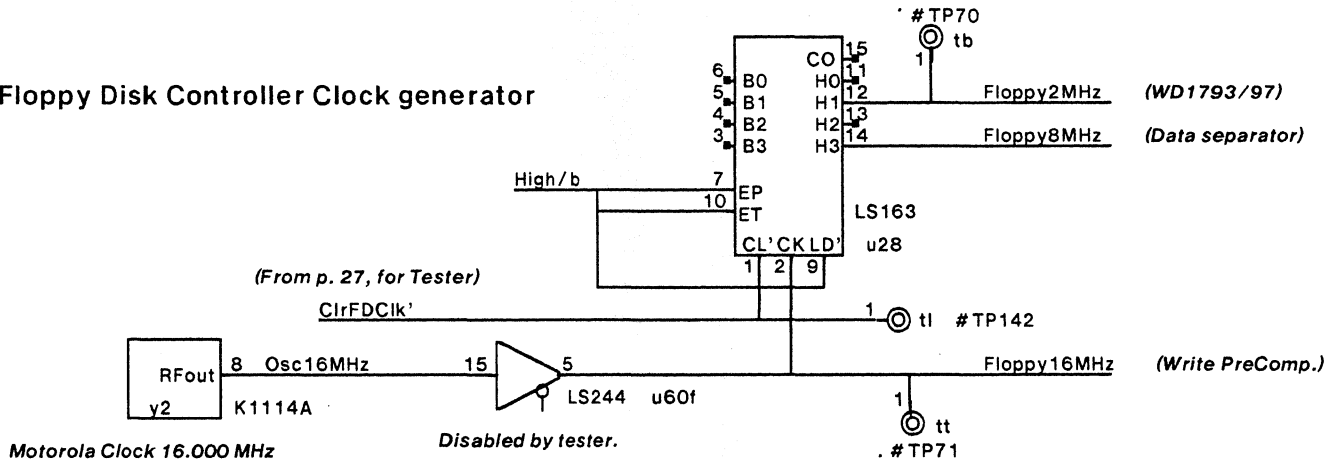


Disabled by tester.

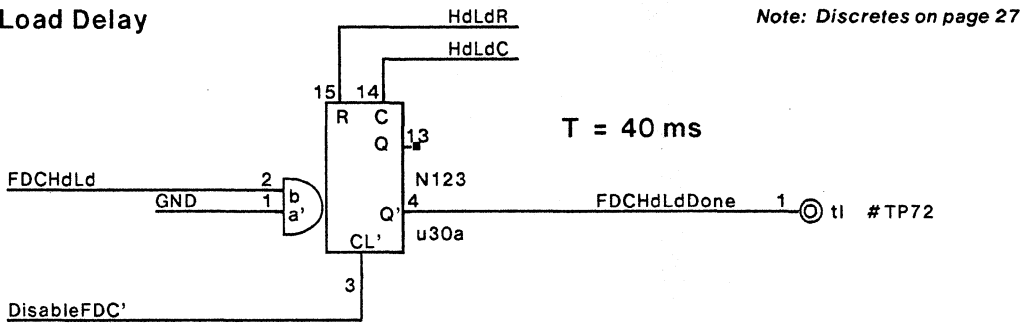


(From page 27)

### Floppy Disk Controller Clock generator



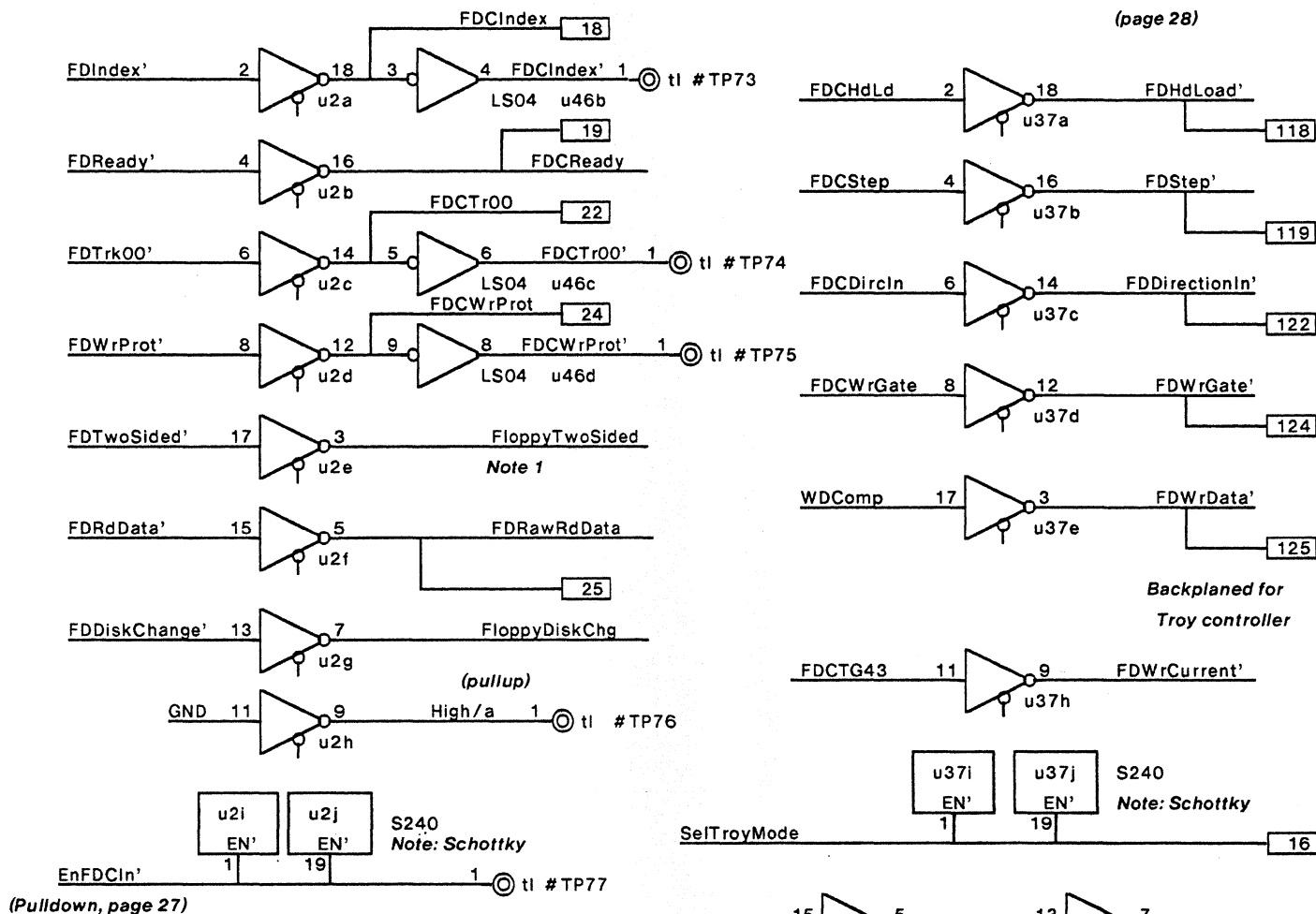
### Head Load Delay



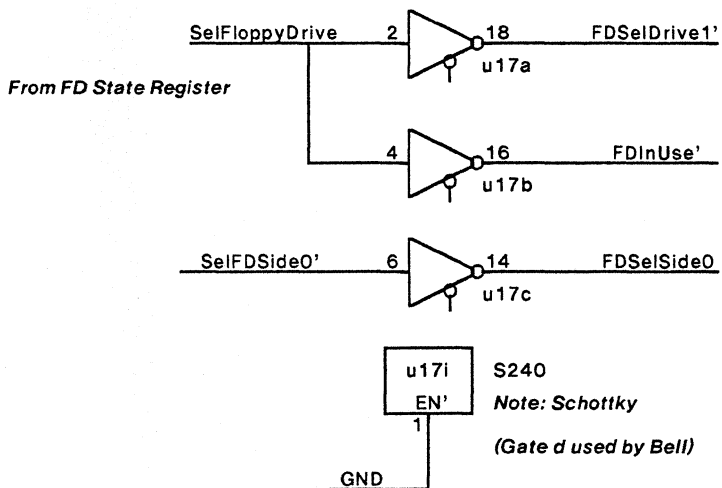
From FD cable (page 28)  
 FD Pull-ups = 150 Ohms, 1/4 Watt

Backplaned for  
 Troy controller

To FD cable  
 (page 28)

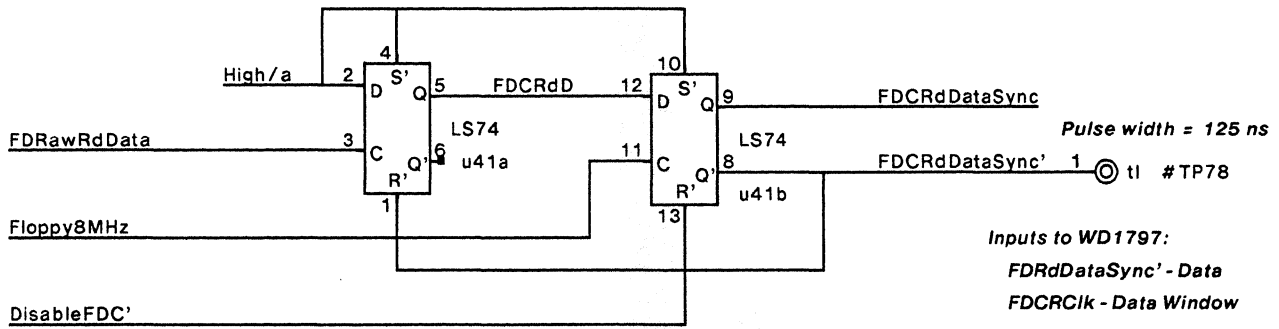


- Note 1: Active high if two sided diskette is installed on SA 850
- Note 2: I/O Connector description on page 28.
- Note 3: For cable documentation see SA 850 OEM manual
- Note 4: TP 076 above is to be used to provide High/a signal when EnFDCIn' is inactive.

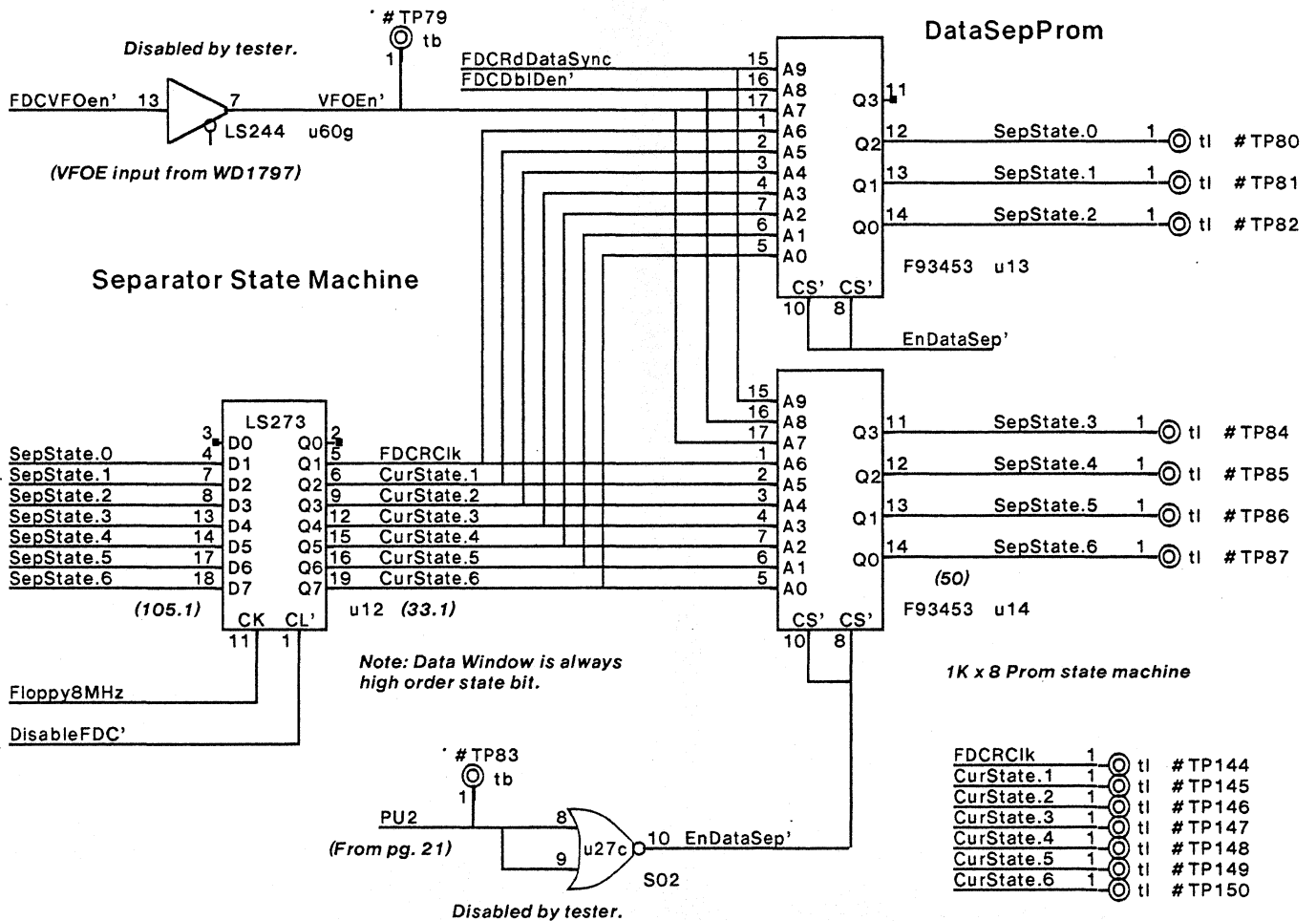
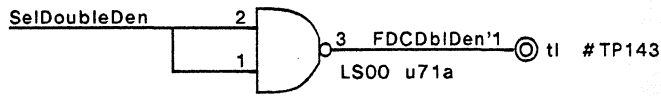




### Raw Read Data synchronizer and pulse shaper



### Double density selection

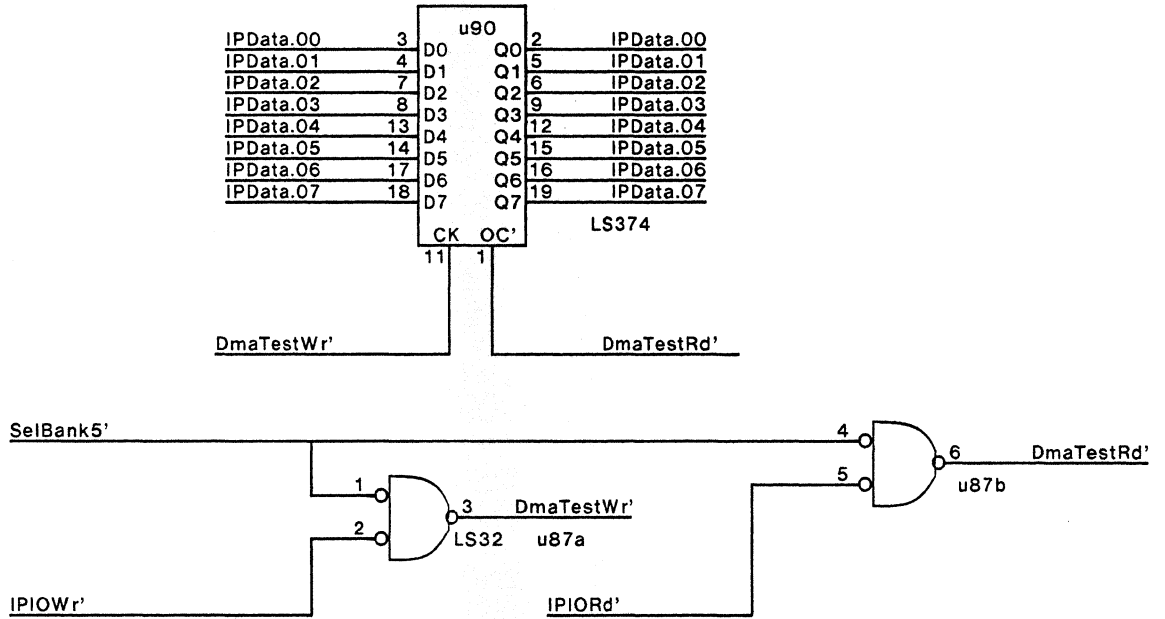


# IPData bus Test

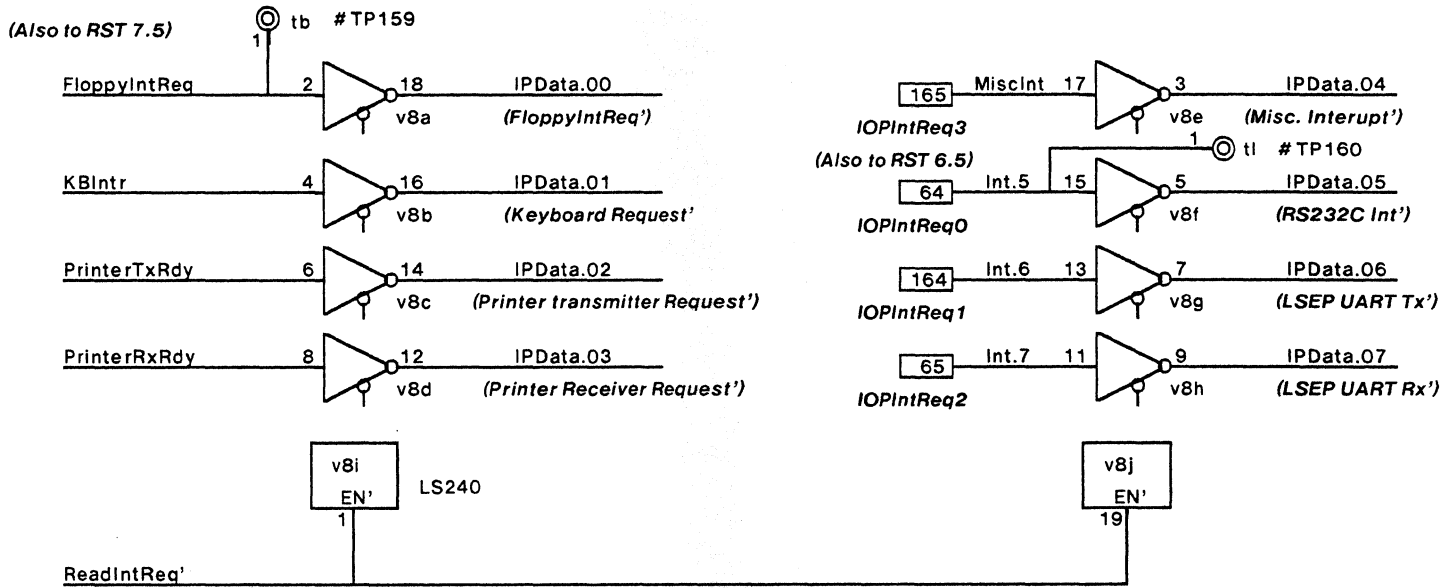
*This register can be used to test the integrity of the IPData bus, which is the external data bus.*

*The register can be written and then read back to determine the soundness of the bus.*

*This register uses SelBank5' to enabled it.*



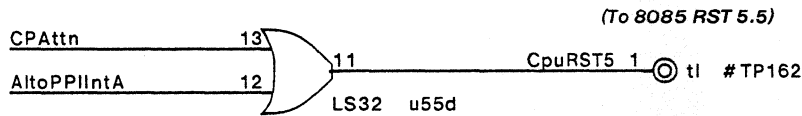
## Interrupt Request Register



This register contains the Interrupt requests of various devices.

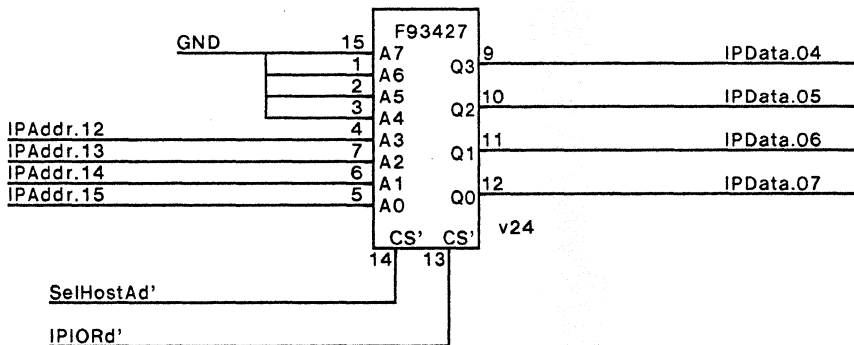
To read true value of the requests, XOR with XOR vector = 1111 1111 (OFFH)

IOIntReq0 - RS232C - Options  
 IOIntReq1 - LSEP UART Tx - Options  
 IOIntReq2 - LSEP UART Rx - Options  
 IOIntReq3 - Miscellaneous interrupt  
 (from Timer)



RST 5.5 interrupt is for MouseHalt or CPAttn

## Host Address Prom (HostAddrProm)



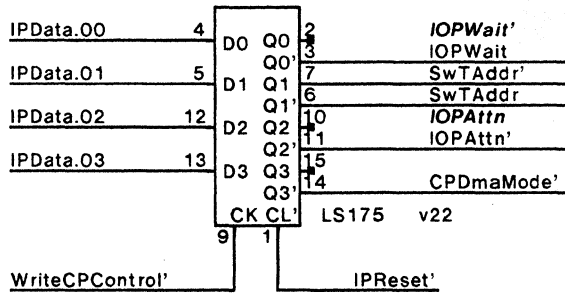
Host address is 48 bits long, stored in addresses 0 - 11 of the Prom.

Address 12 should contain a 4-bit checksum of the address.

Host address Prom has the 16 I/O Bank 3 addresses.

I/O address	Host address bits
OBOH	0:3
OBCH	44:47
OBDH	checksum

### Central processor control CP port control

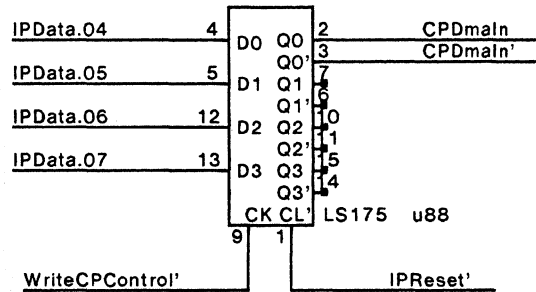


(IOPWait and SwTAddr should be true after booting)

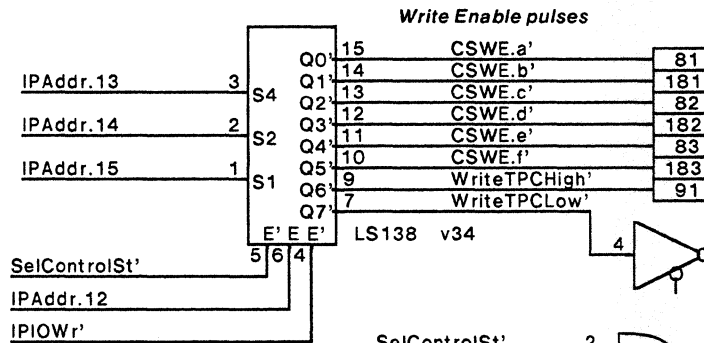
### Data:

- Bit 0 - Enable CP (remove Wait)
- Bit 1 - Switch TPC address from NIAX
- Bit 2 - Set CP Attention
- Bit 3 - Set Dma Mode for CP port
- Bit 4 - Set Dma Mode for CPport as input/output'

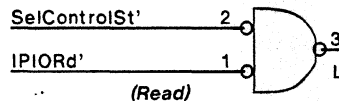
Normal state of register is 1100 0xxx



### Control Store handling



(Write)

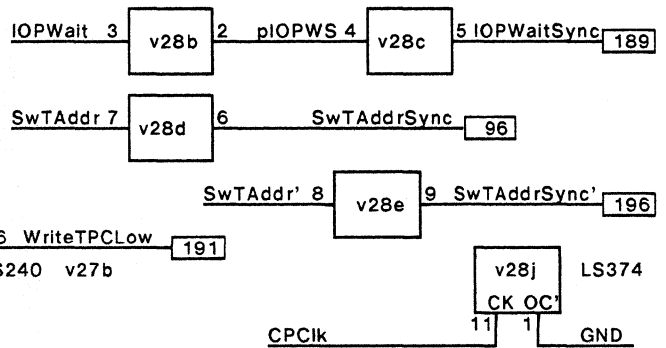


(Read)

Note: All 16 addresses will respond for Read, use 8-15.

(Bank 7, registers 8-15, addresses 0F8 - 0FFH)

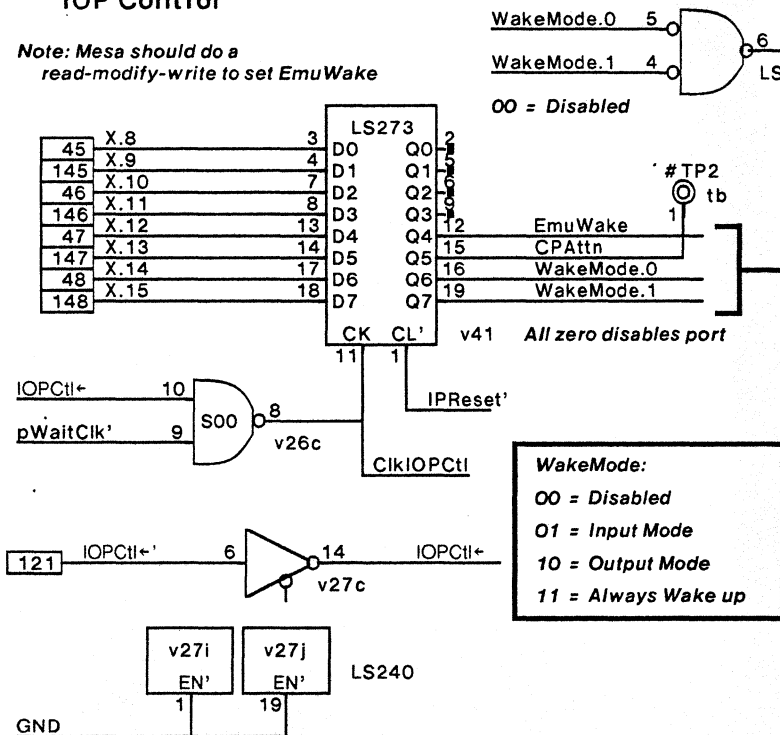
### CP synchronization



### Central Processor registers

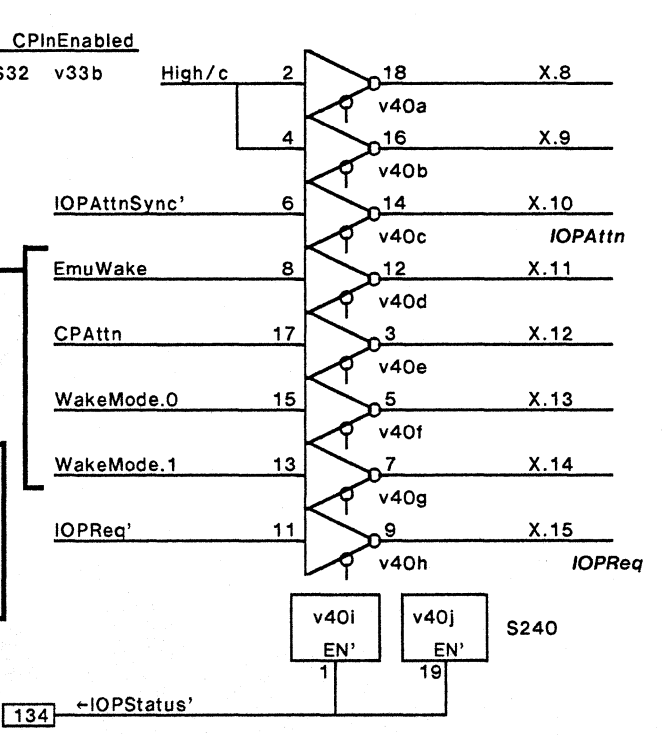
#### IOP Control

Note: Mesa should do a read-modify-write to set EmuWake

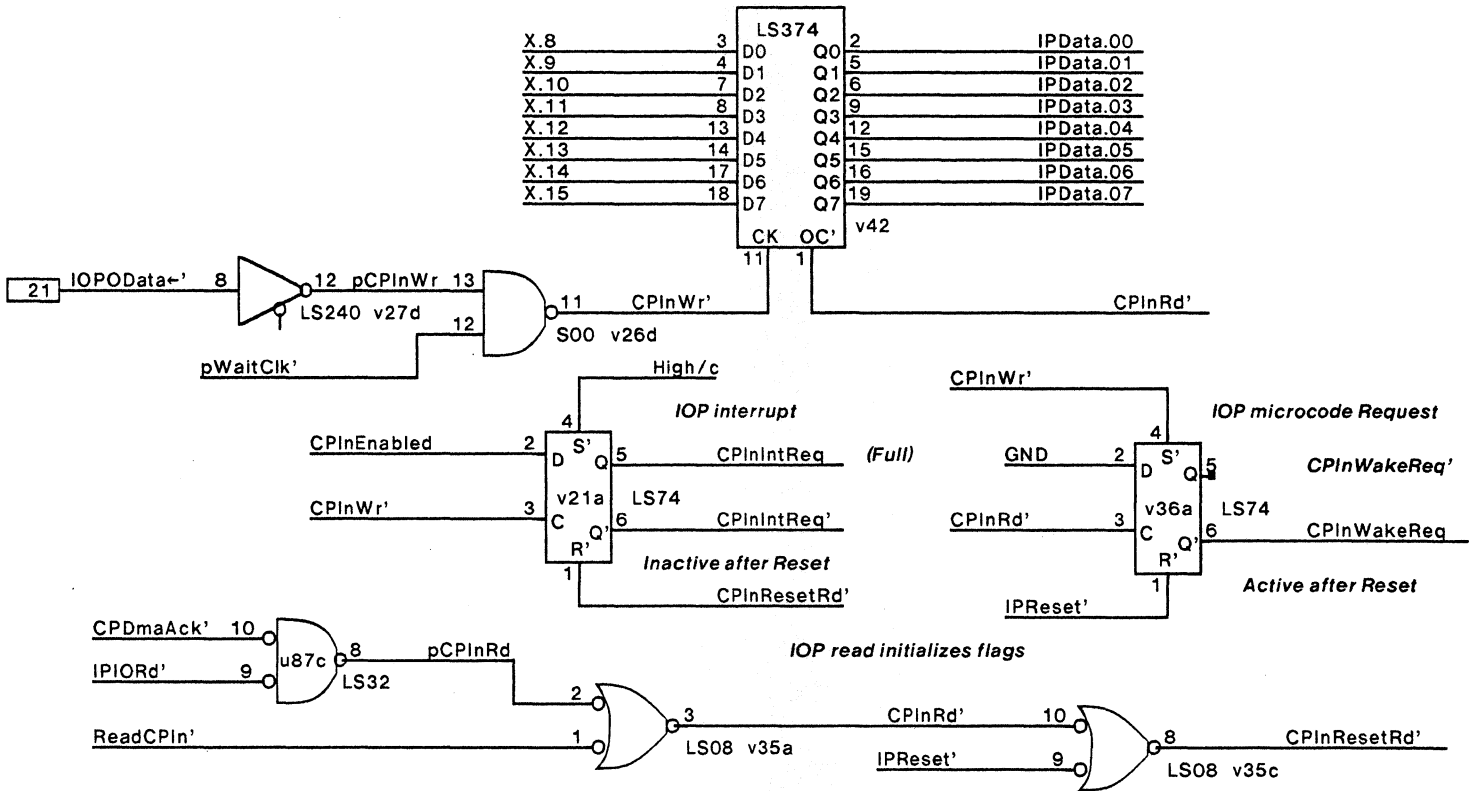


**WakeMode:**  
00 = Disabled  
01 = Input Mode  
10 = Output Mode  
11 = Always Wake up

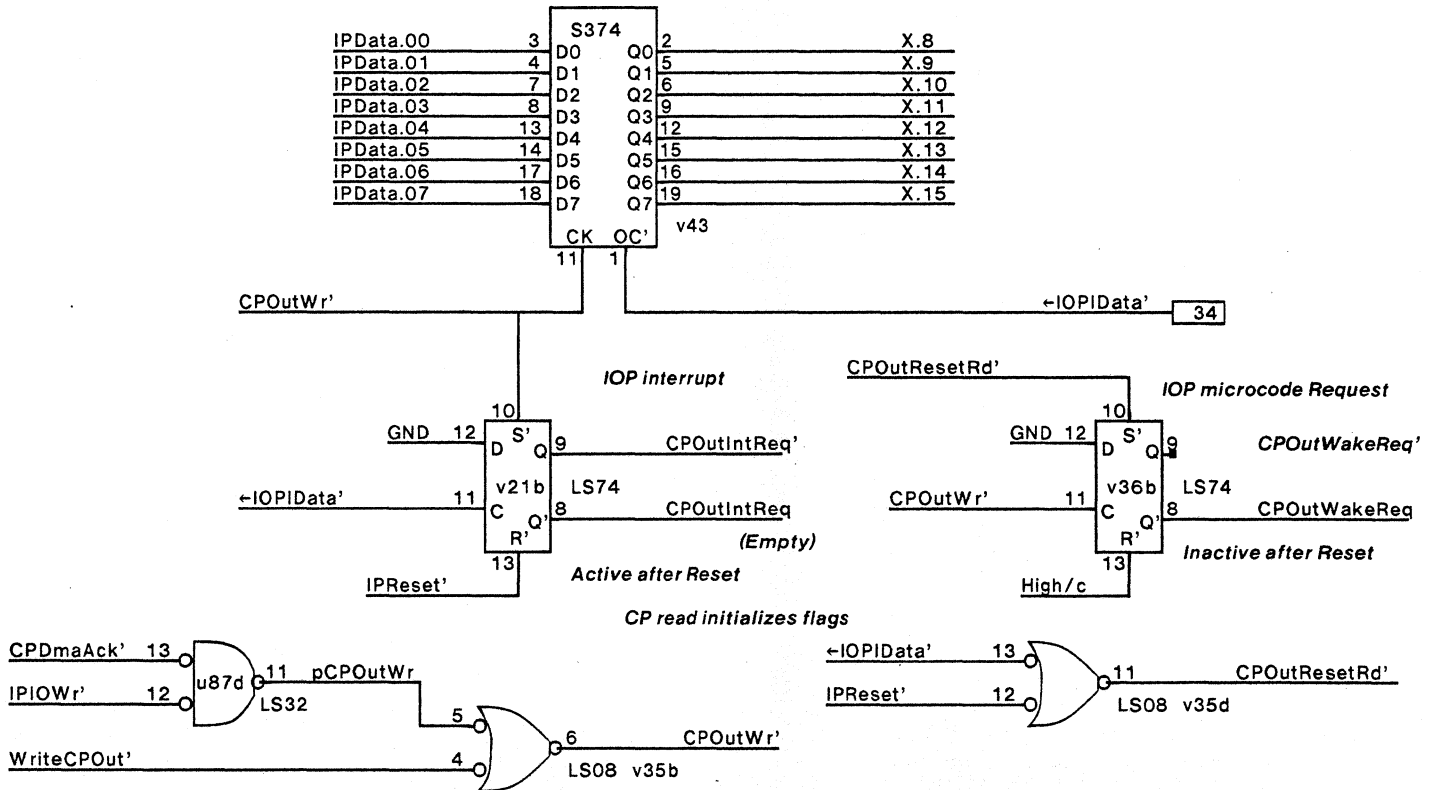
#### IOP Status



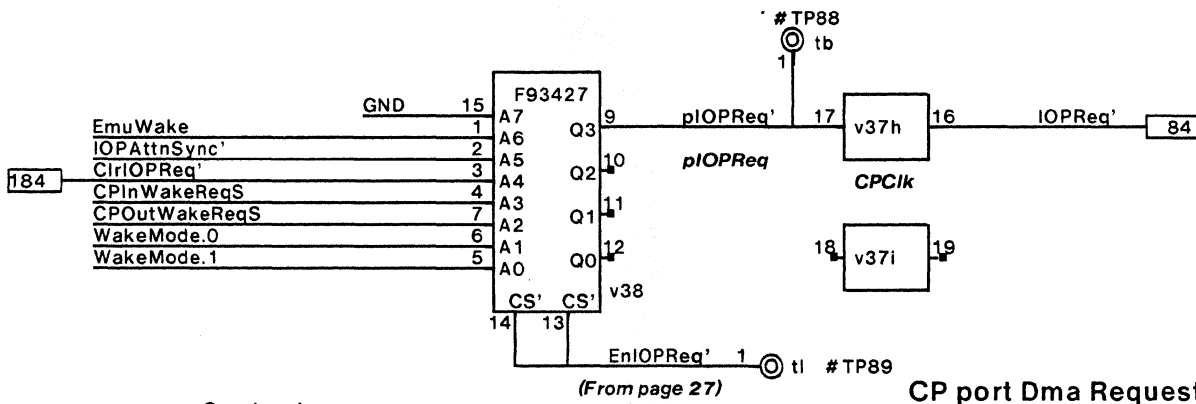
**CPIn (From CP)**



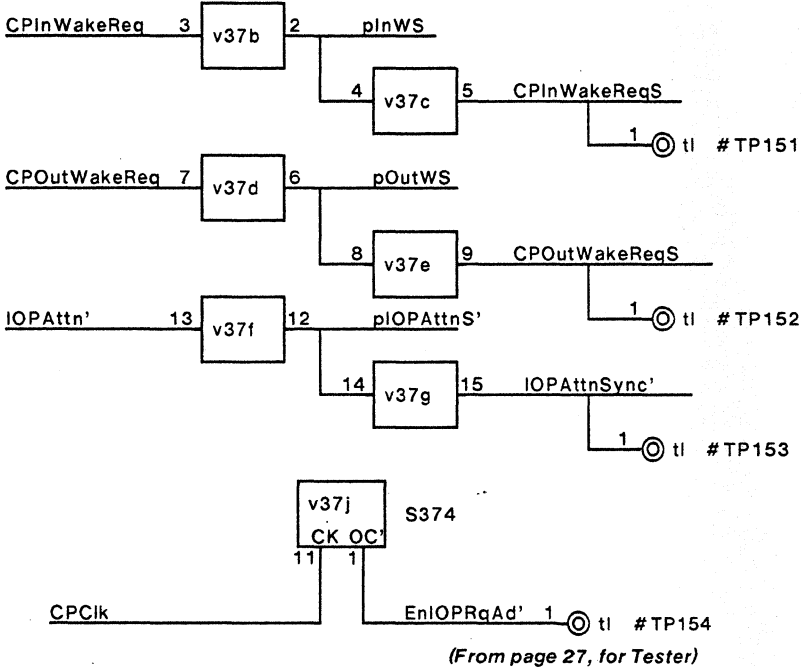
**CPOut (To CP)**



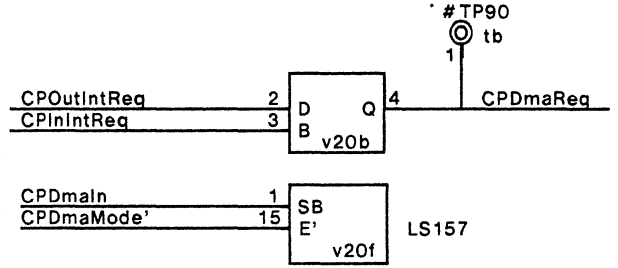
# IOPReq



## Synchronizers

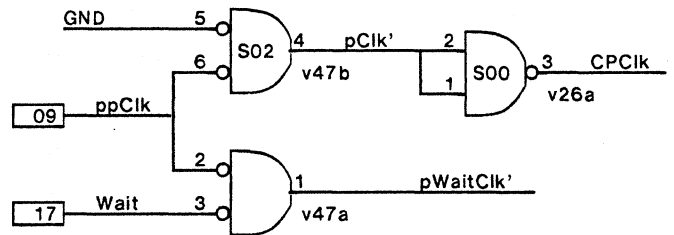


## CP port Dma Request



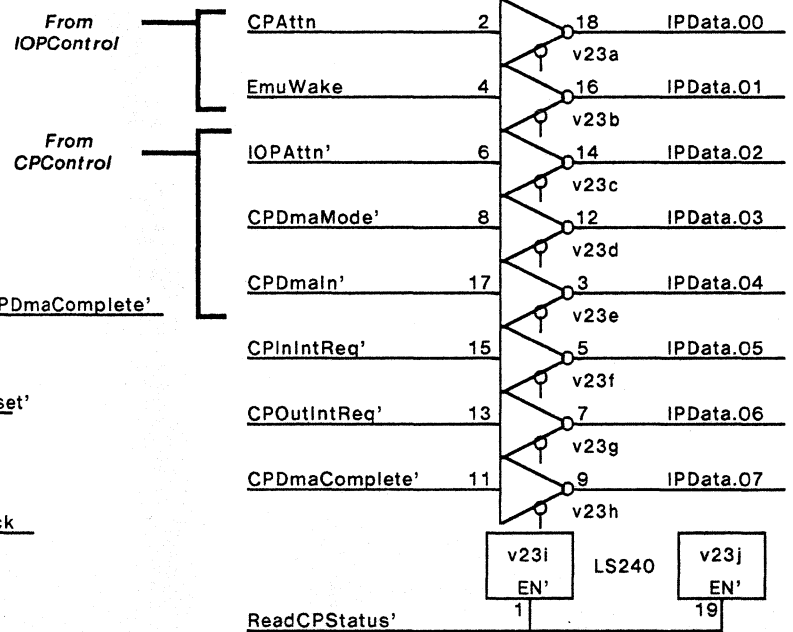
Note: First set up CPDmain, then CPDmaMode

## CP Clock

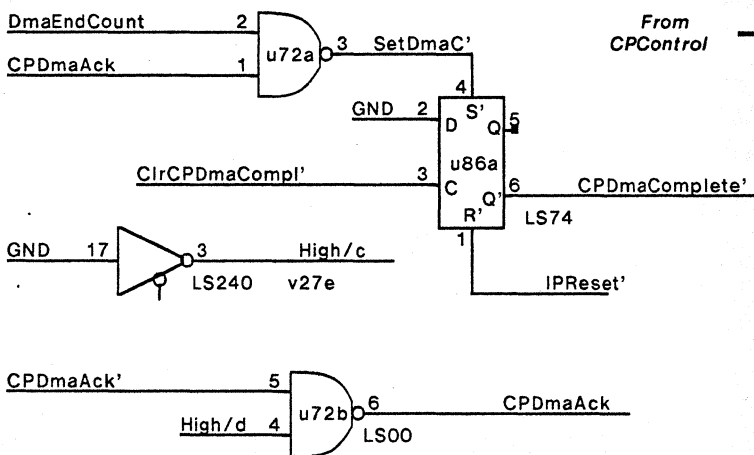


## CP port status

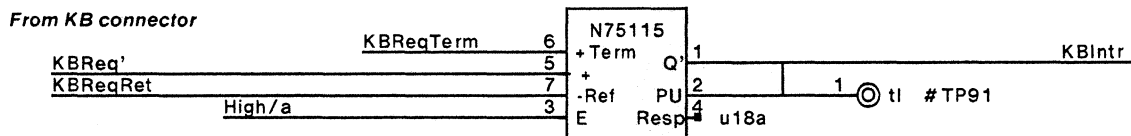
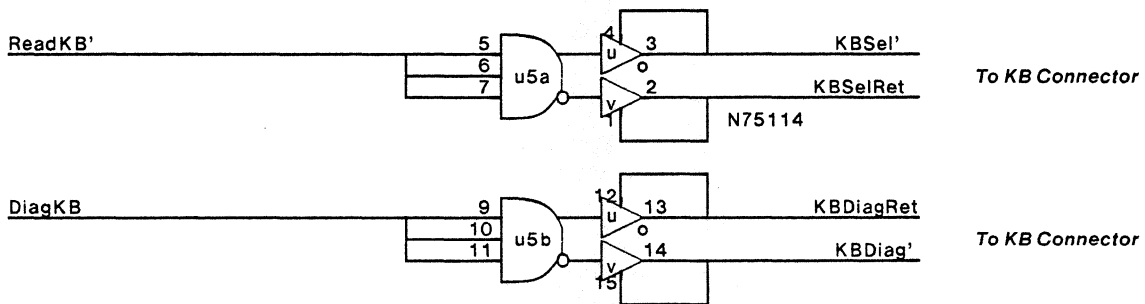
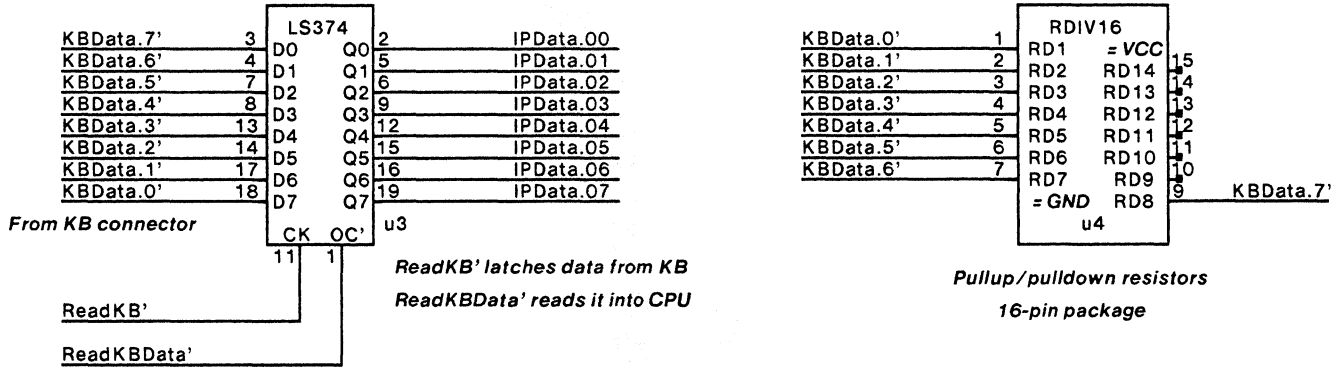
Also to i8085 RST 5 (with MouseHalt)



## CP port Dma Complete



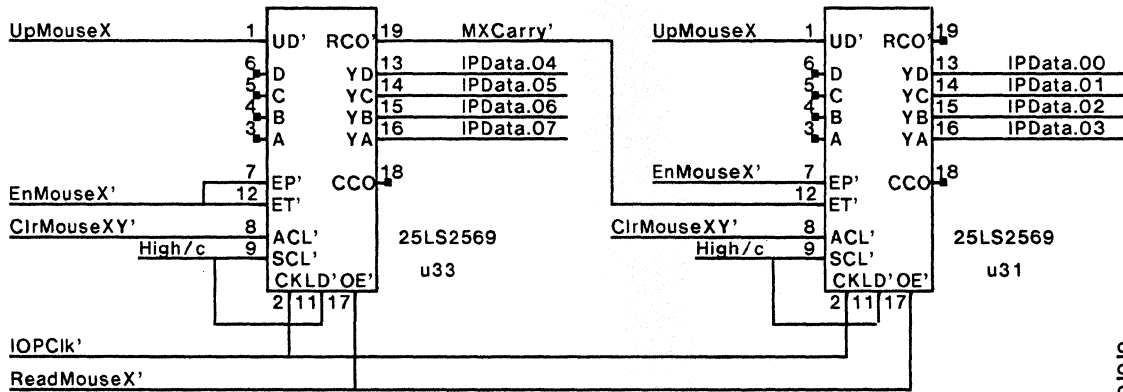
## Keyboard Data



KBReqTerm connected through 1000 pF capacitor to KBReqRet  
(see page 27)

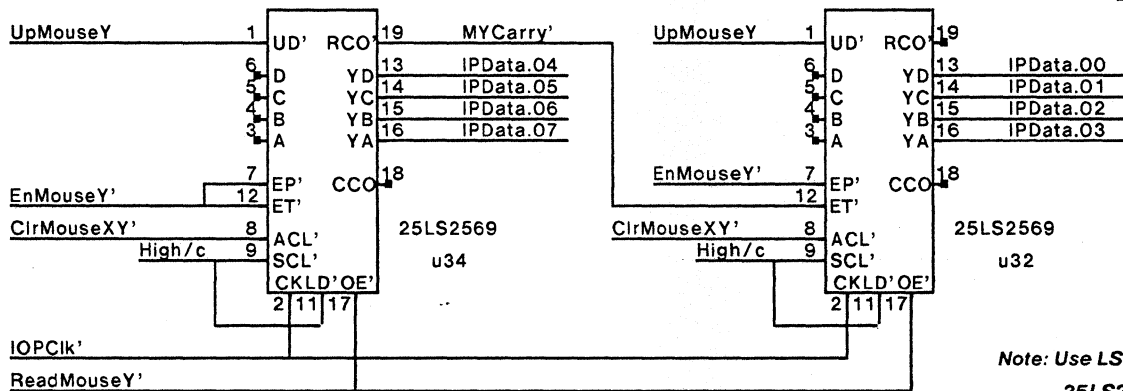
Bell circuit on page 27.

### X-coordinate



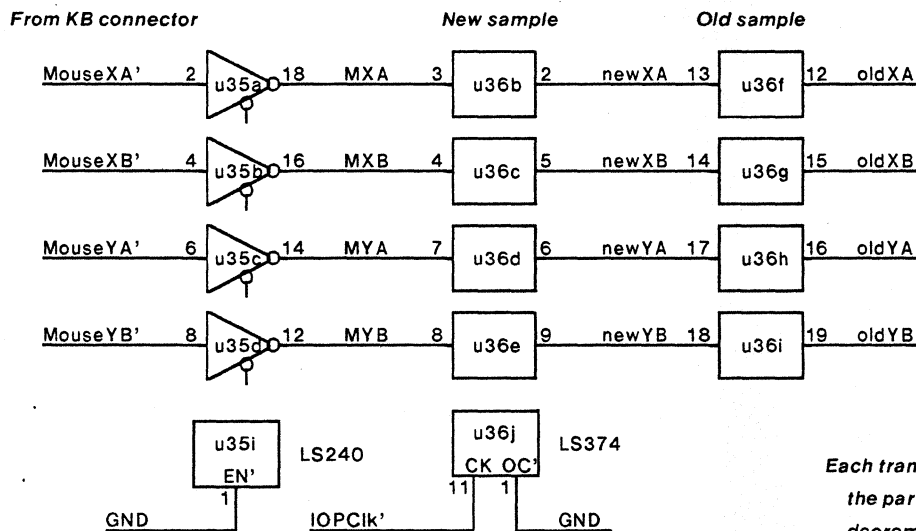
- oldXA 1 ○ t1 # TP92
- oldXB 1 ○ t1 # TP93
- oldYA 1 ○ t1 # TP94
- oldYB 1 ○ t1 # TP95

### Y-coordinate

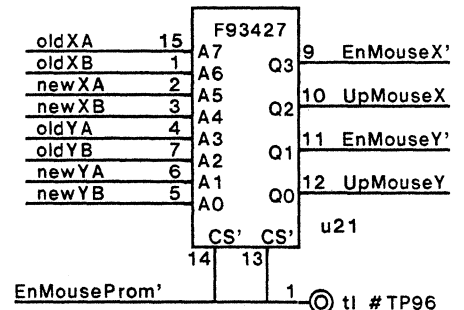


- EnMouseX' 1 ○ t1 # TP97
- UpMouseX 1 ○ t1 # TP98
- EnMouseY' 1 ○ t1 # TP99
- UpMouseY 1 ○ t1 # TP100

Note: Use LS191 + LS244 if not 25LS2569



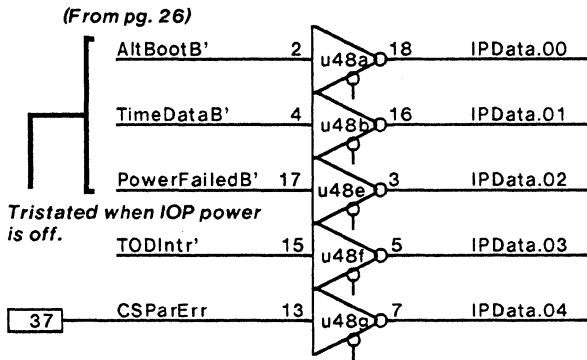
### MouseProm



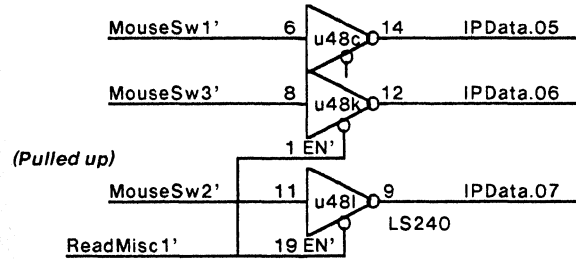
Each transition of XA or XB (YA or YB) causes the particular counter to be incremented or decremented. The samples are made with the inverted processor clock to ensure that the counters are stable when a read or clear of the counters is done.



### Miscellaneous Input 1

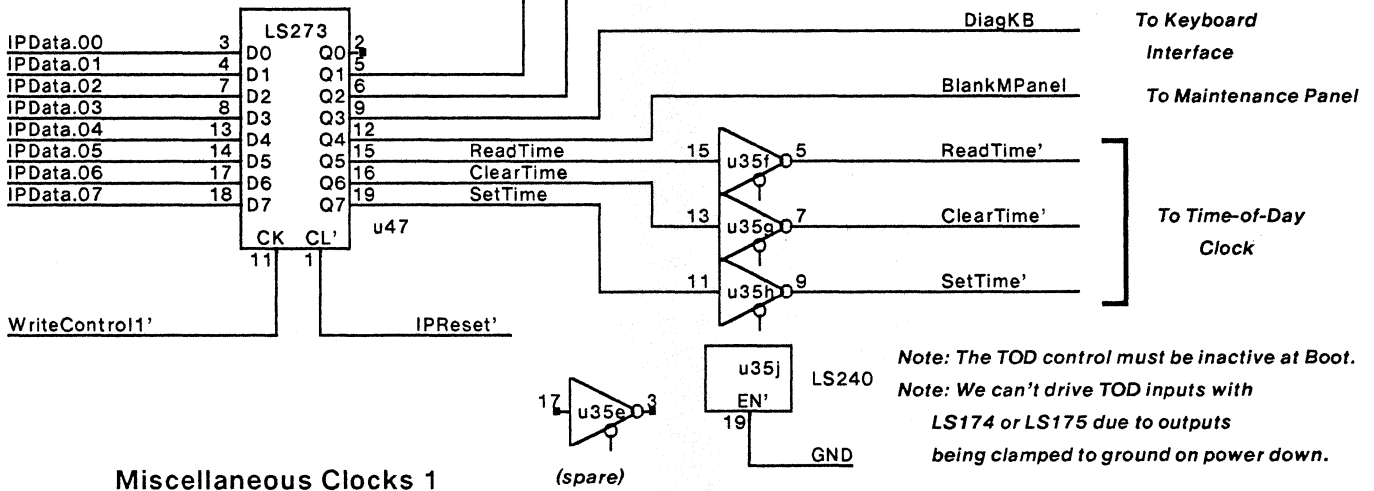


### From KB connector

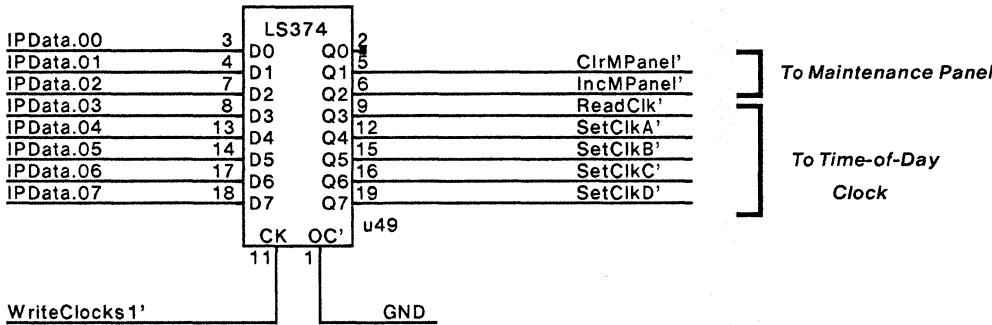


Note: For two-button mice, MouseSw2 does not exist.

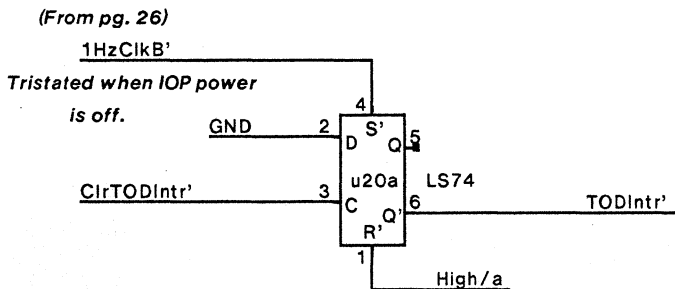
### Miscellaneous control 1



### Miscellaneous Clocks 1



### Time-of-Day 1 second interrupt



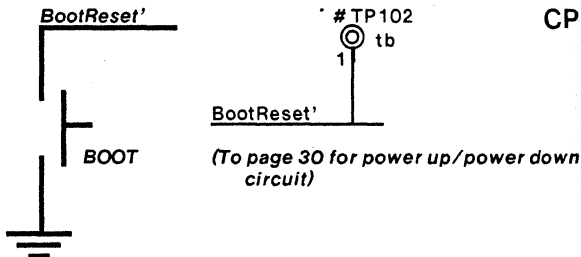
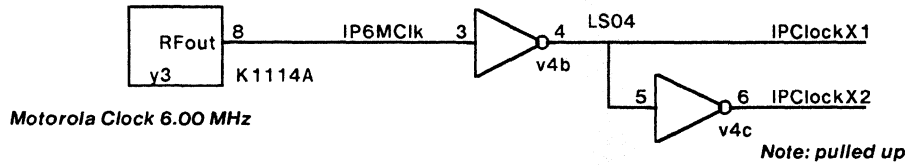
#### Procedure to read time:

- Wait for TODIntr to be set
- When set, clear and wait for it to be set again
- read time (maximum delay = 2 seconds)

#### Alternative:

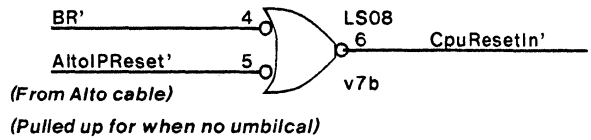
- clear TODIntr (if being set will not be cleared)
- wait until set
- read time (maximum delay = 1 second)

# IOP CPU Clock generator



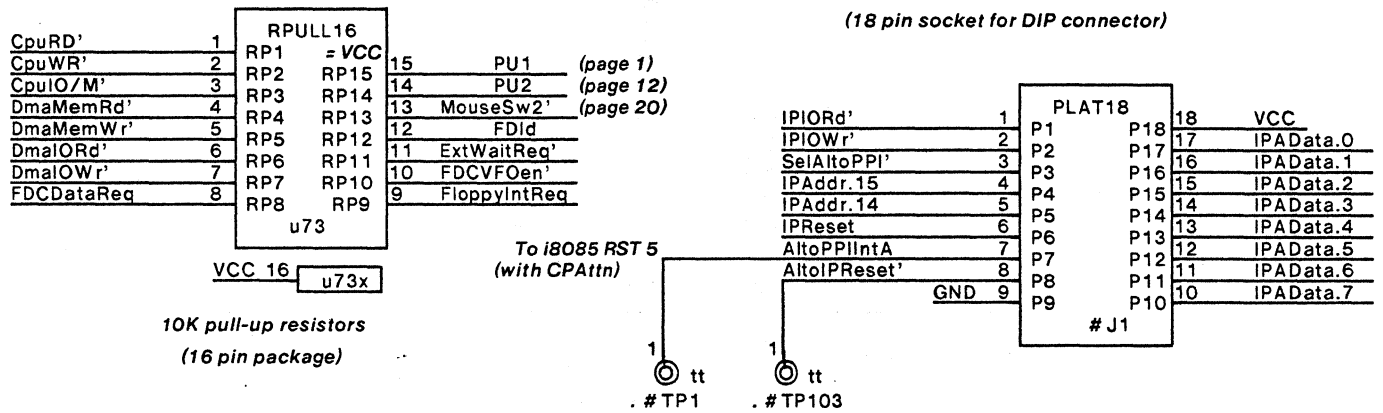
## CPU boot

(From page 30)

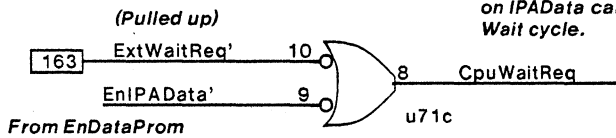


## Dip Cable to Alto-IOP interface

(18 pin socket for DIP connector)

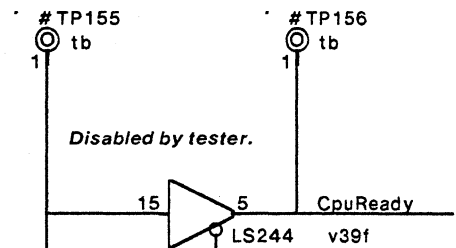


External Waits can be requested from the Options card. The RS232 will require a Wait cycle.

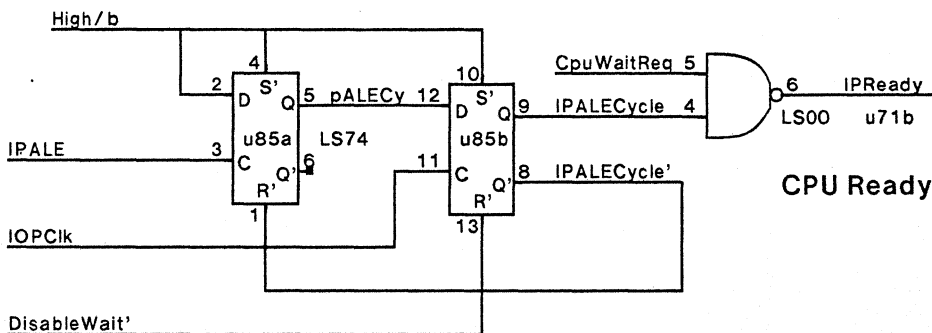
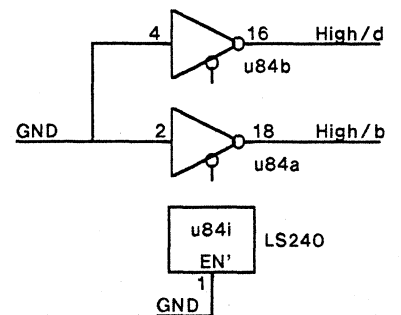


Currently, all devices on IPADData cause a Wait cycle.

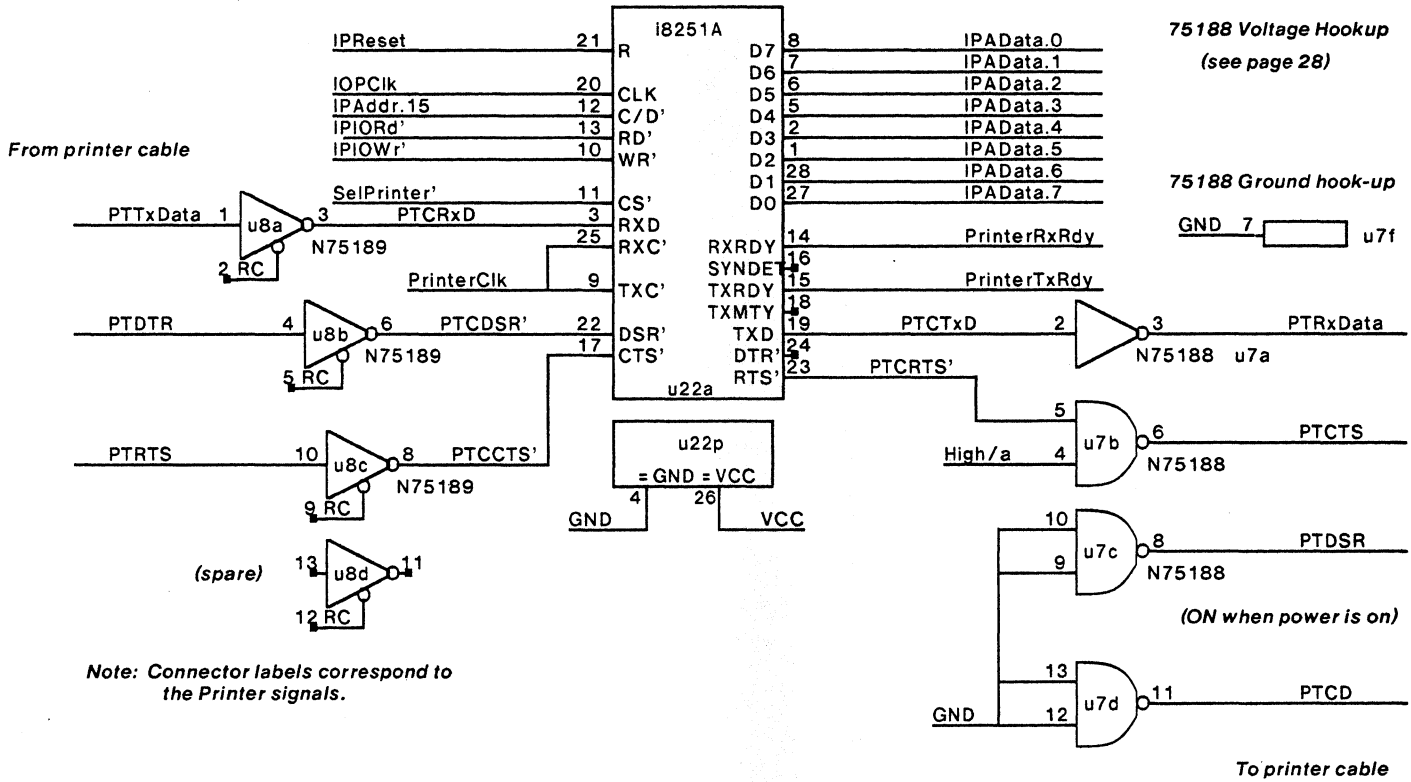
Disabled by tester.



## Pullups

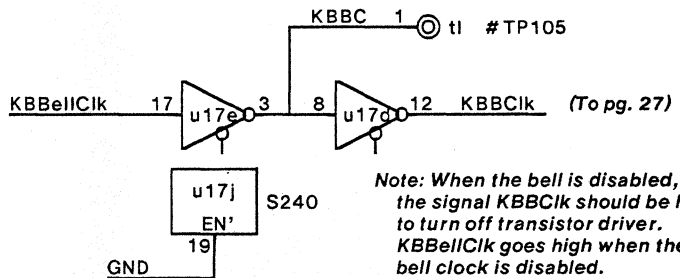
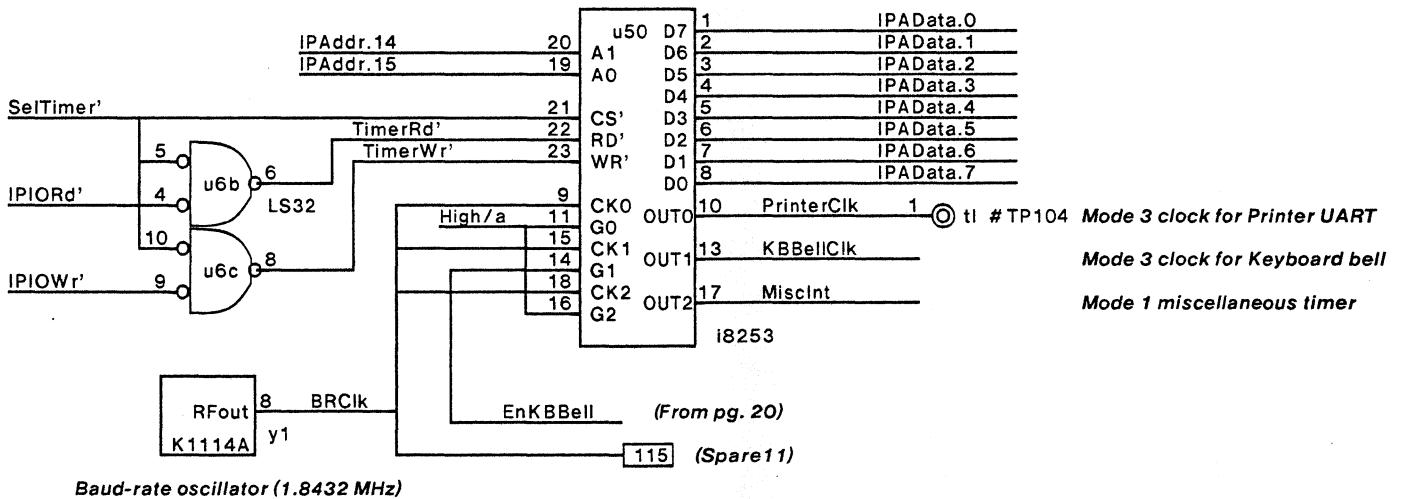


### Printer UART

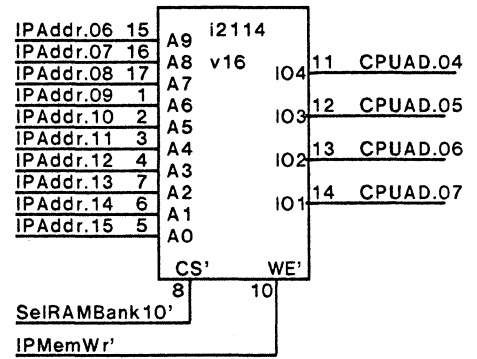
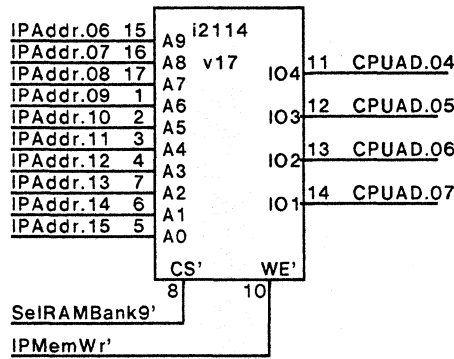
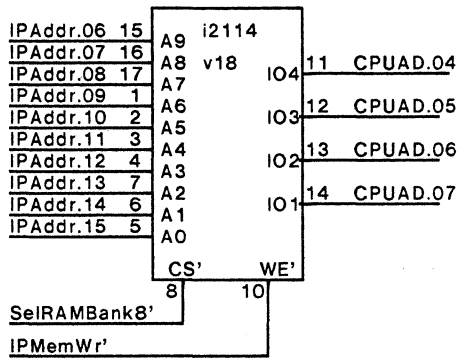
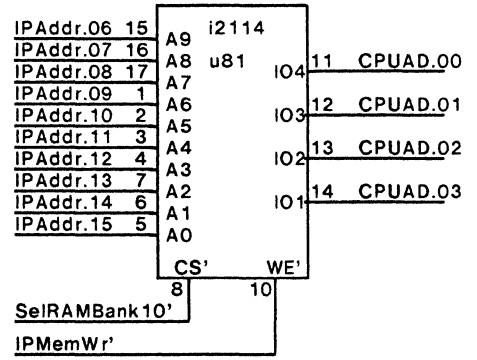
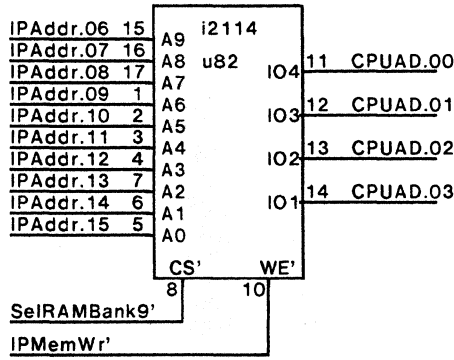
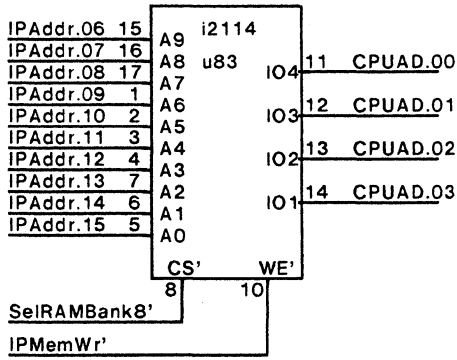


Note: Due to a design shortcoming the RD' and WR' lines of the 8253-5 must be externally qualified.

### Baud-rate generator



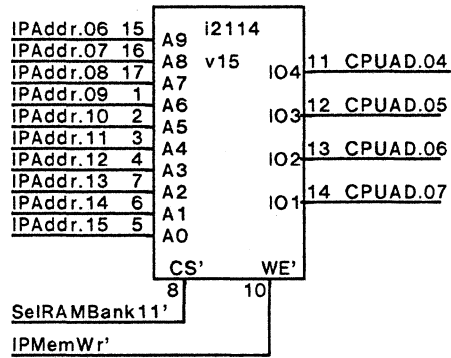
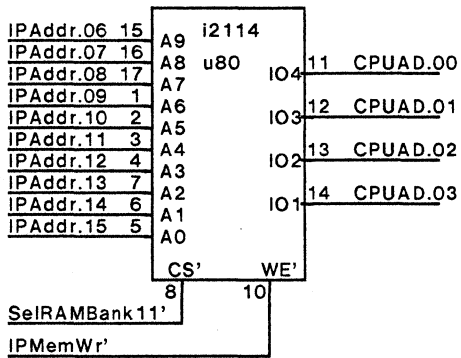
# RAM - Banks 8 - 11



Bank 8

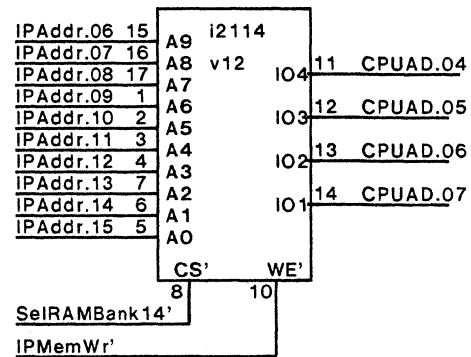
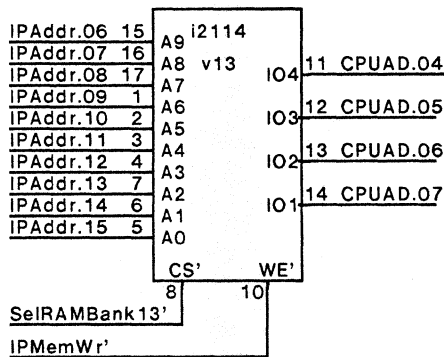
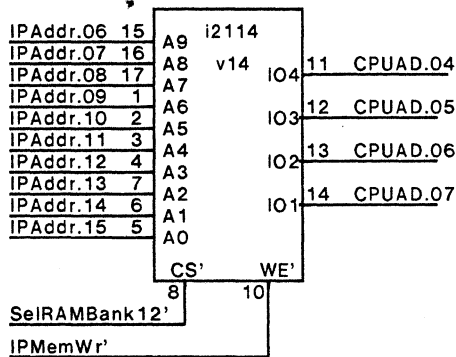
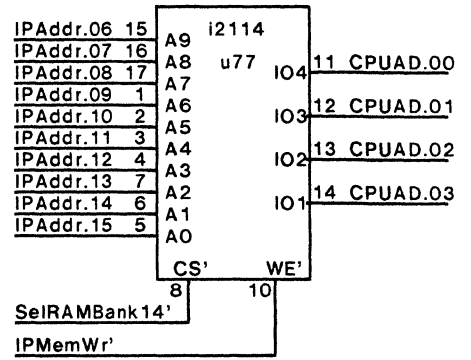
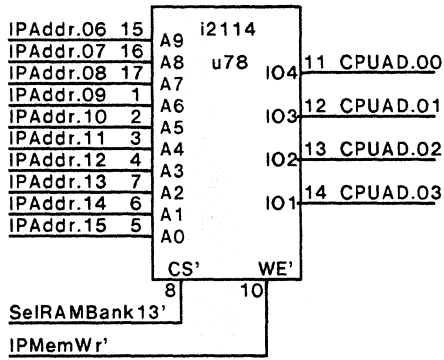
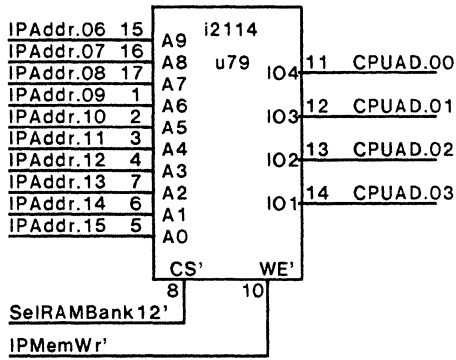
Bank 9

Bank 10



Bank 11

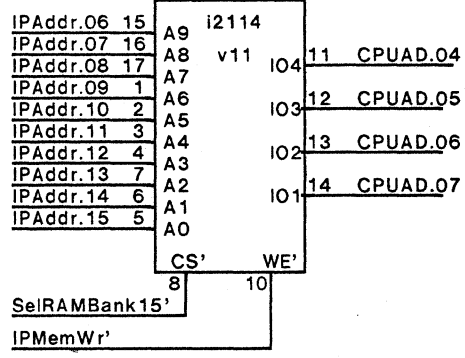
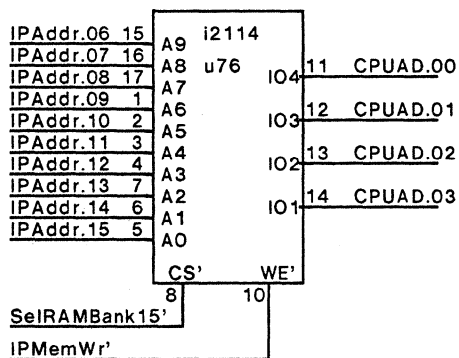
RAM - Banks 12 - 15



Bank 12

Bank 13

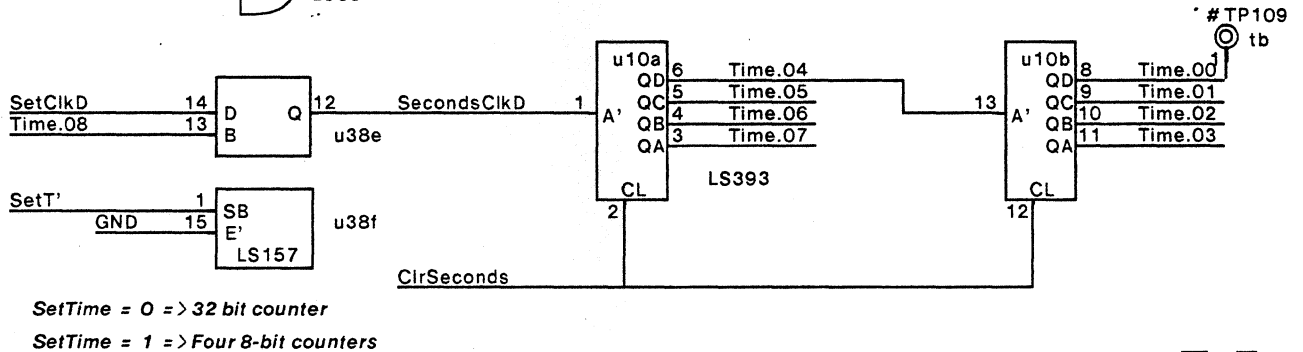
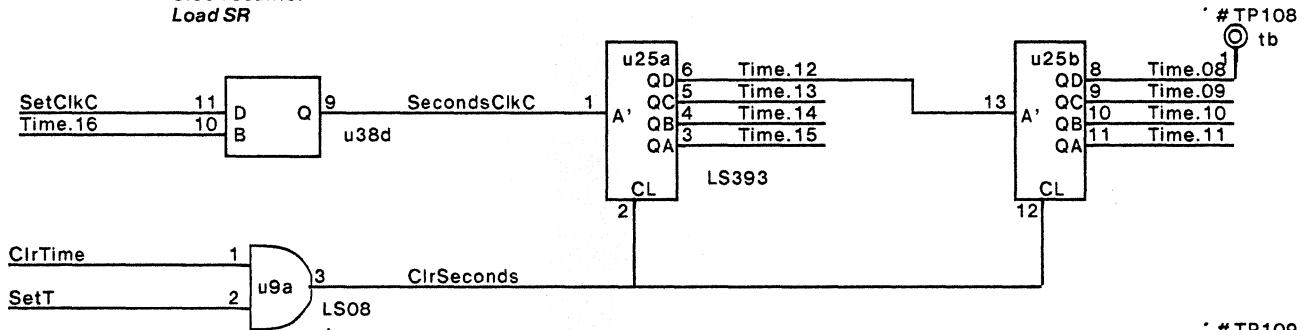
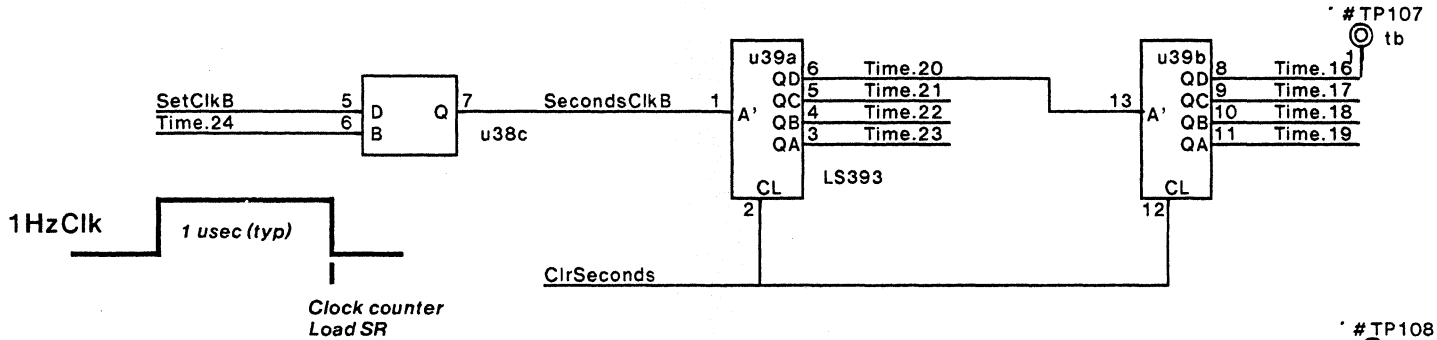
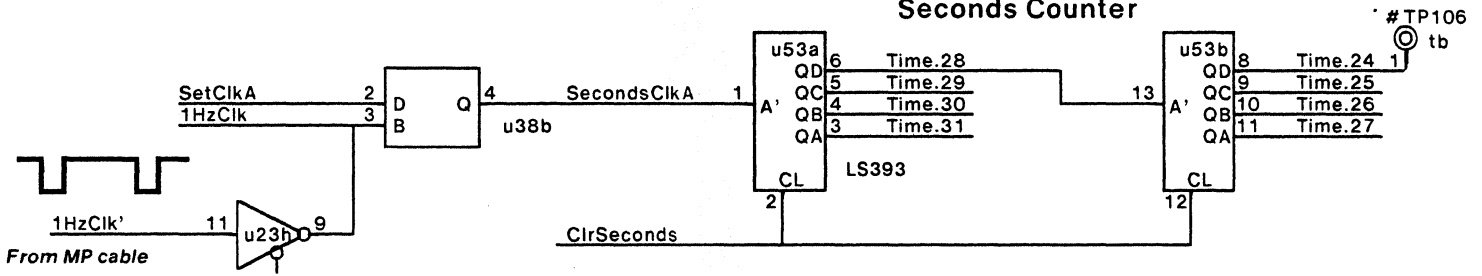
Bank 14



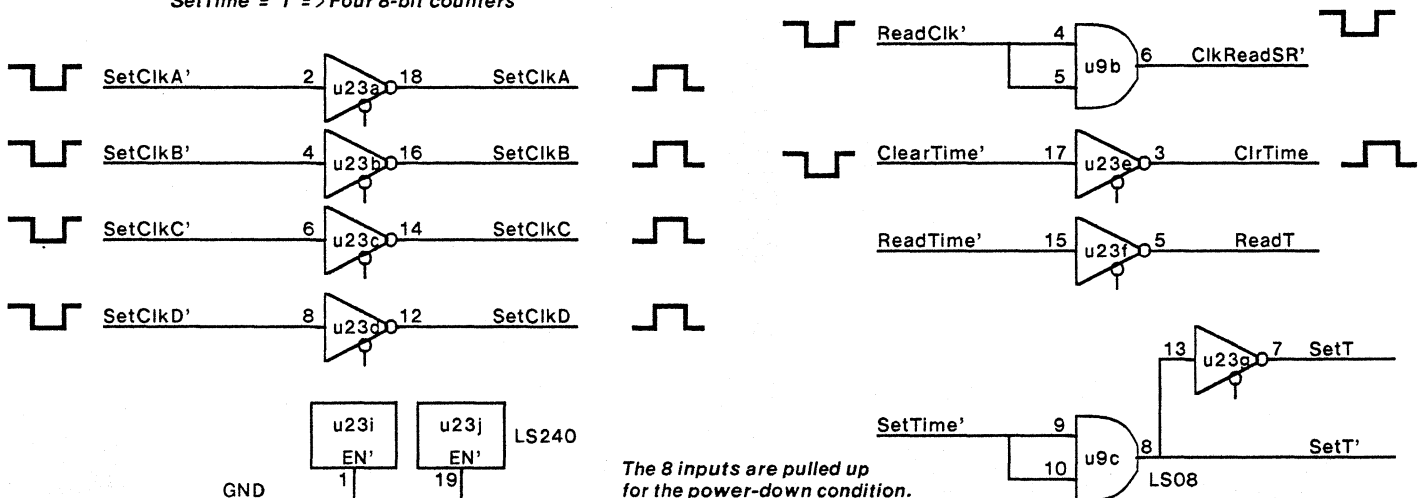
Bank 15

Note: All the logic on this page is powered from the Maintenance panel +5V supply, MVcc (see page 26)

### Seconds Counter



SetTime = 0 => 32 bit counter  
SetTime = 1 => Four 8-bit counters



The 8 inputs are pulled up for the power-down condition.

Note: All the logic on this page is powered from the Maintenance panel +5V supply, MVcc

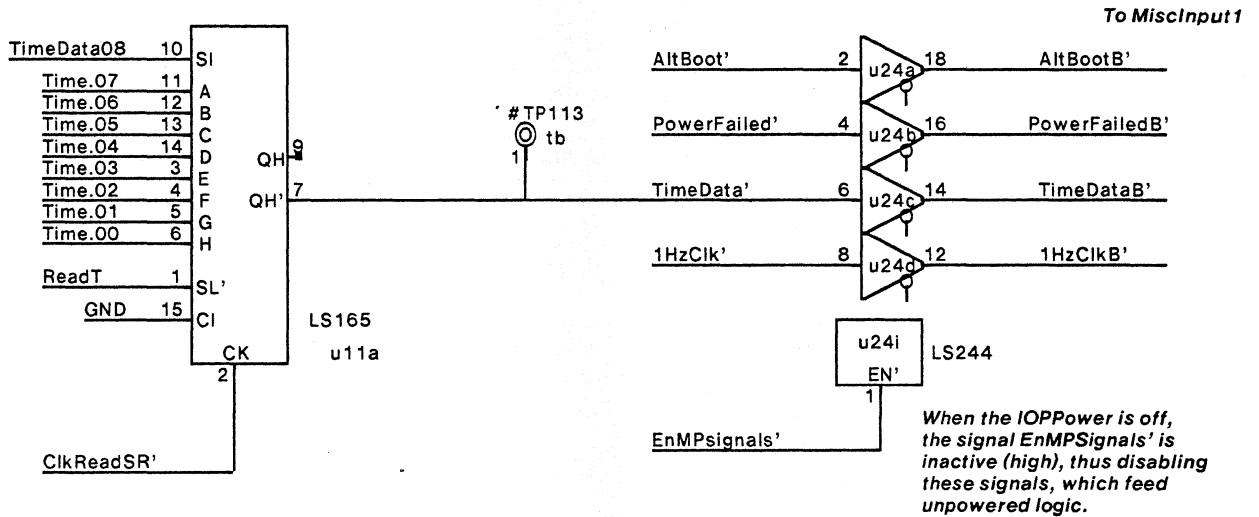
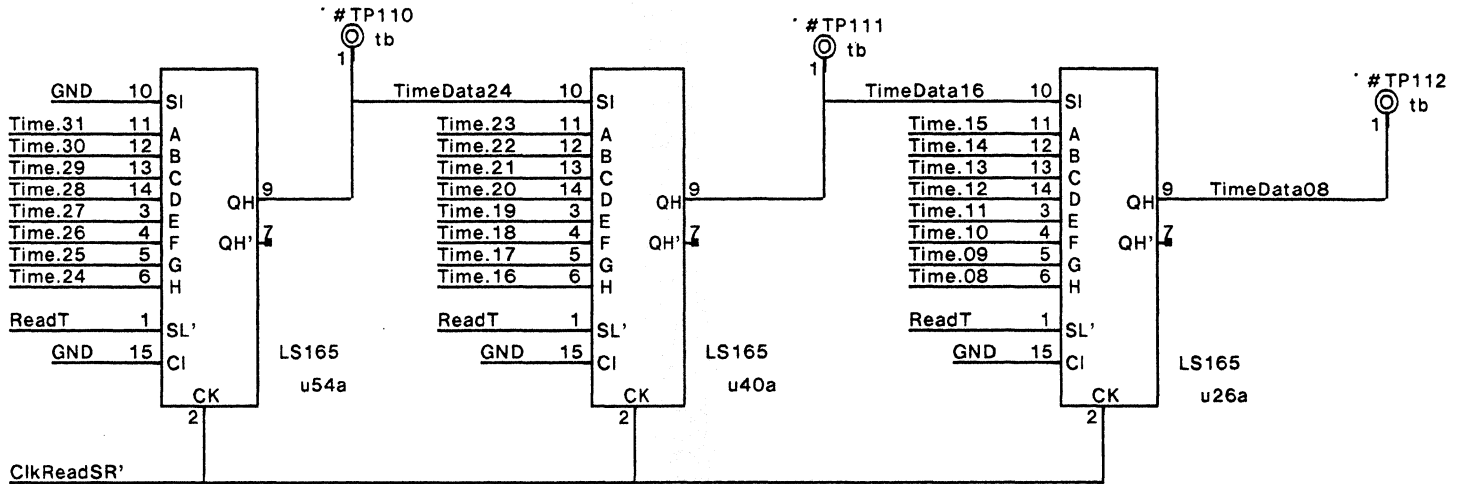
### Time Read Shift Register

ReadTime = 0 =>

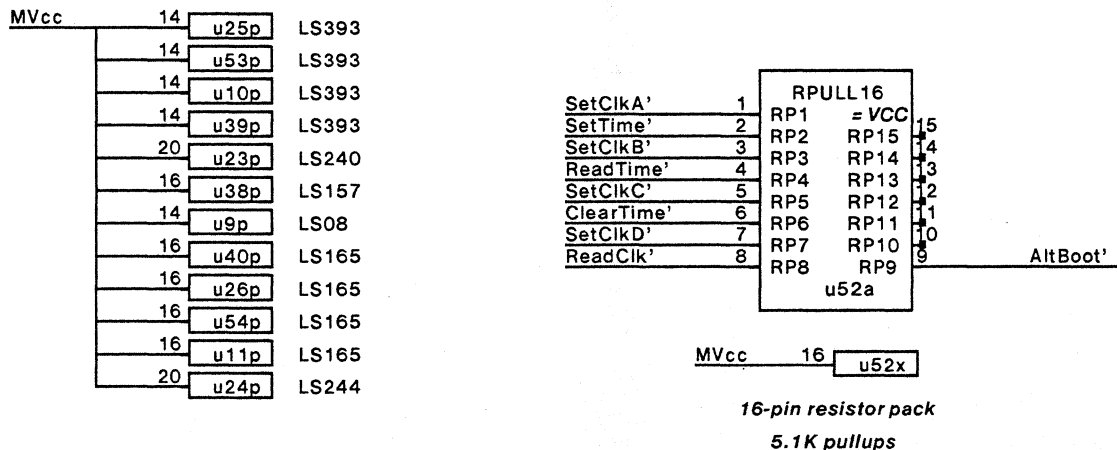
Shift register parallel loads Seconds Counter

ReadTime = 1 =>

Shift register in serial shift mode, ClkReadSR' clock

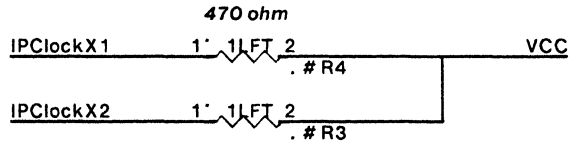
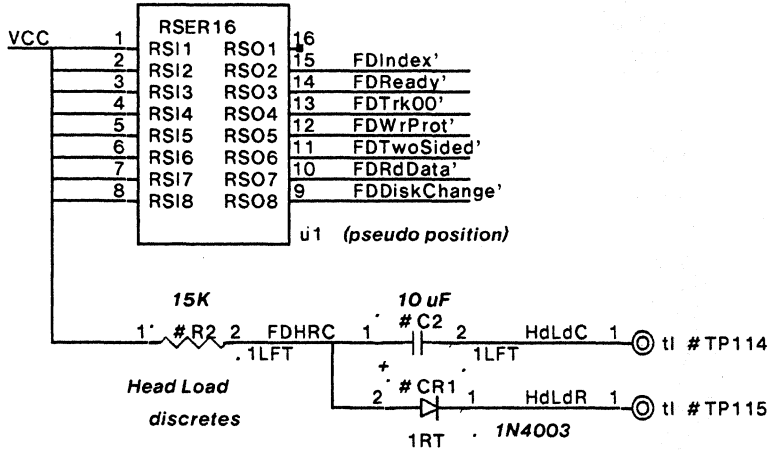


### Power for Time-of-day clock from Maintenance Panel

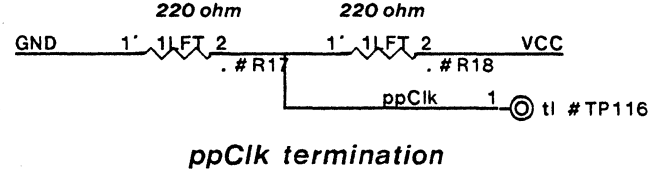


DISCRETE COMPONENTS (see also page 30)

Pullups, 150 ohm, 1/4 watt  
Allen-Bradley 316B151

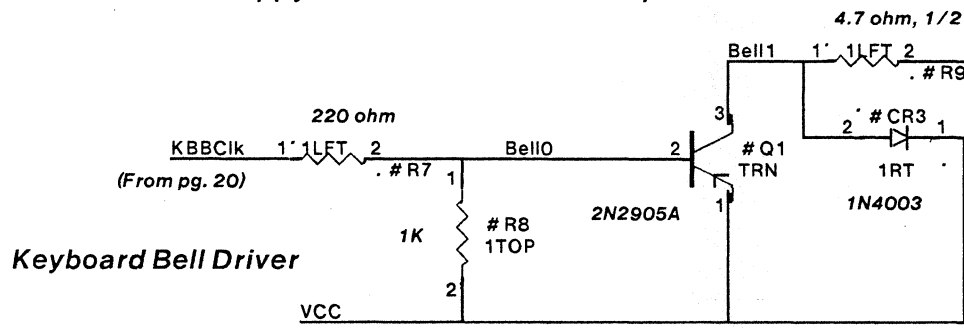


CPU clock driver pull-ups

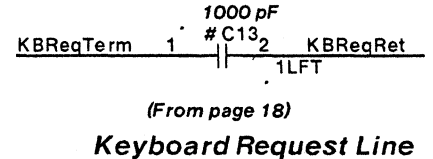


ppCik termination

Miscellaneous Floppy Controller discrete components

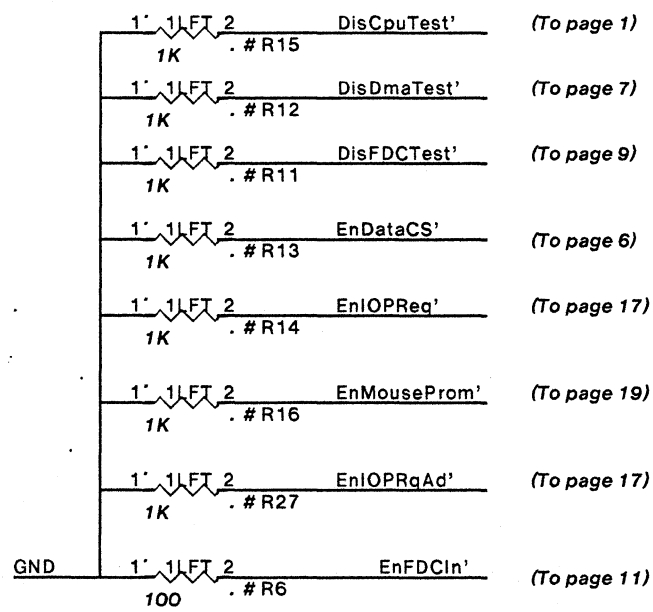


Keyboard Bell Driver

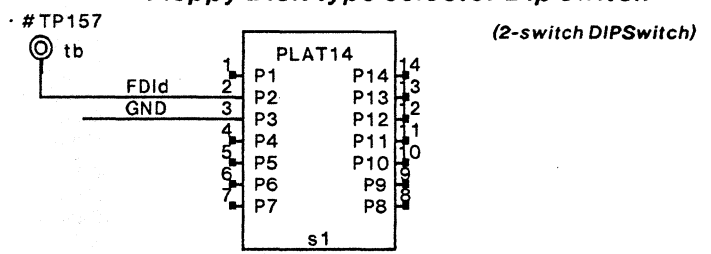


Keyboard Request Line

Discretes for Testability



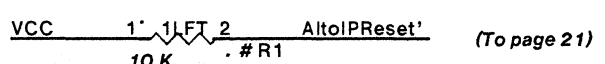
Floppy Disk type selector Dip switch



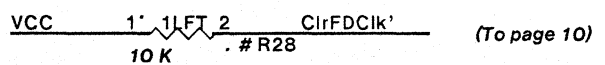
This is the switch to select the type of Floppy Disk drive in the system. Only 1 switch is needed, and should be the rightmost switch of a DIP switch package. The smallest package should be used. The package should be inserted in the spare position between u50 and u51, or below u74, whichever is easier, right adjusted. Shown here as position x51.

Examples: 4-switch DIP package: Grayhill 76RSB04 or 76SB04.  
Xerox P/N 710W00004  
2-switch DIP package: Grayhill 76RSB02 or 76SB02.

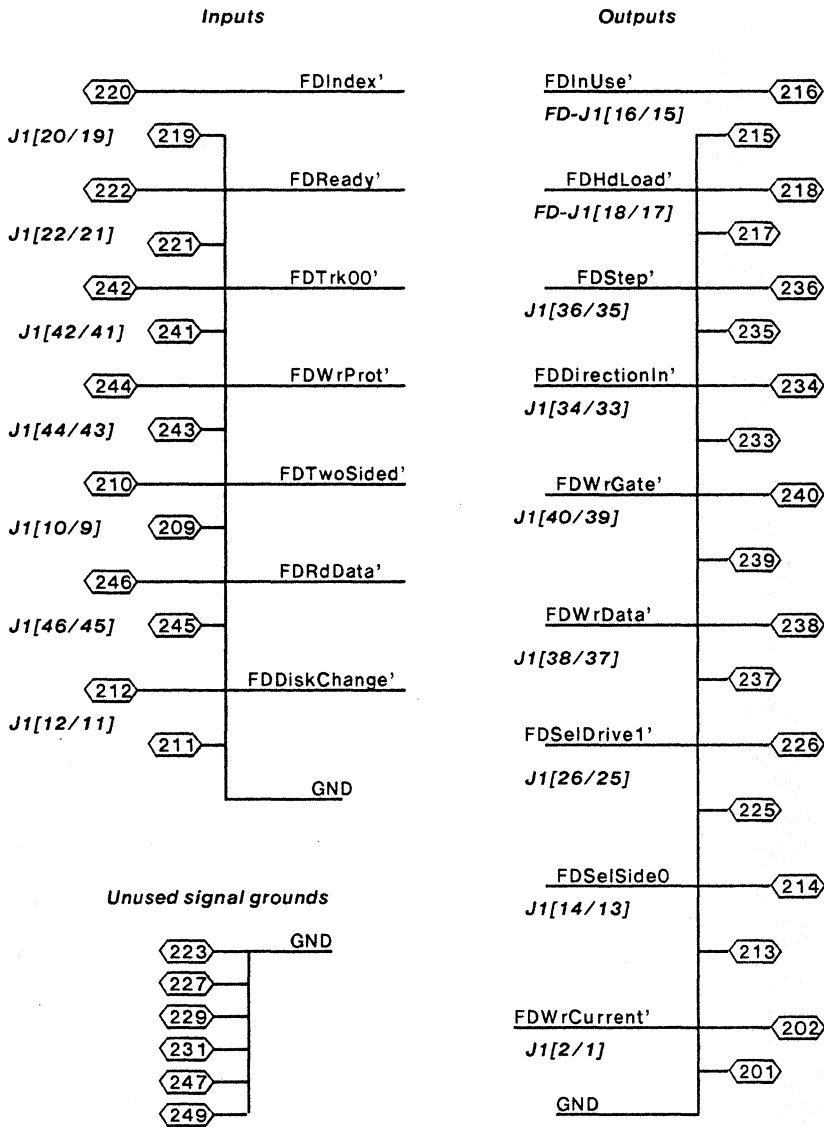
Pullups



(Needed for when no Alto is connected)



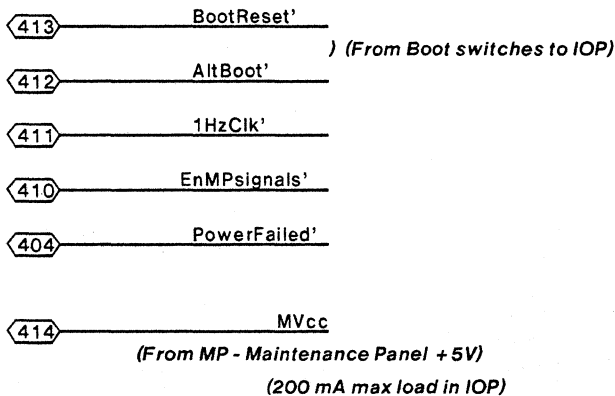




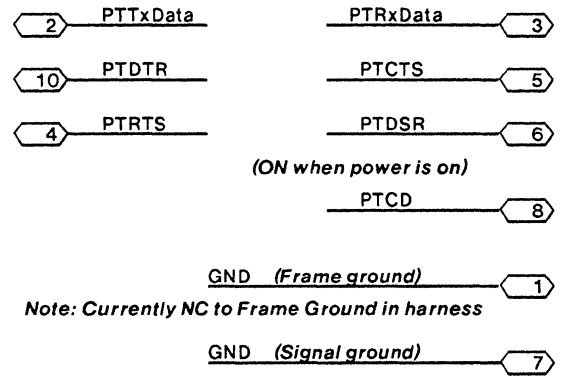
### Floppy Disk Cable Connector

50-pin male connector  
Xerox 713W14820

(Subtract 200 from above pin numbers to get physical pin number)



### (Printer)



Note: Currently NC to Frame Ground in harness

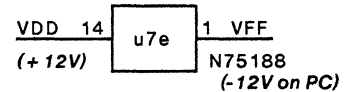
Note: Connector labels correspond to the Printer signals.

### RS-232-C DCE port Cable Connector

10-pin male connector  
Xerox 713W12220

(Above pin numbers are same as physical pin numbers)

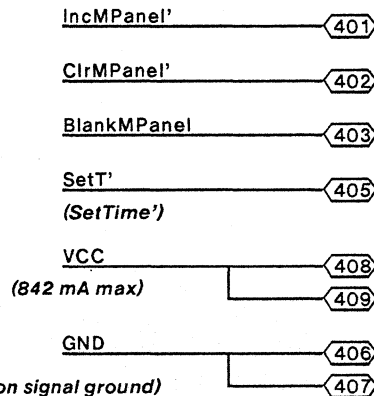
### 75188 Voltage hookup

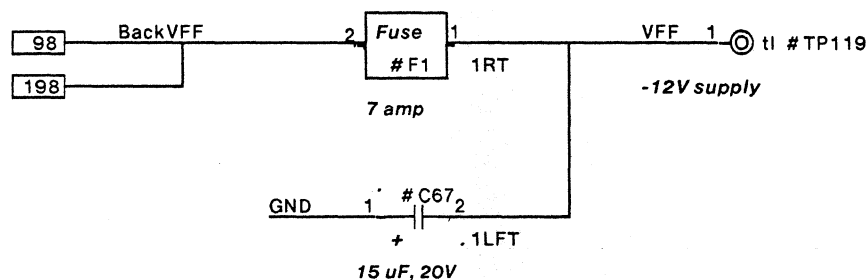
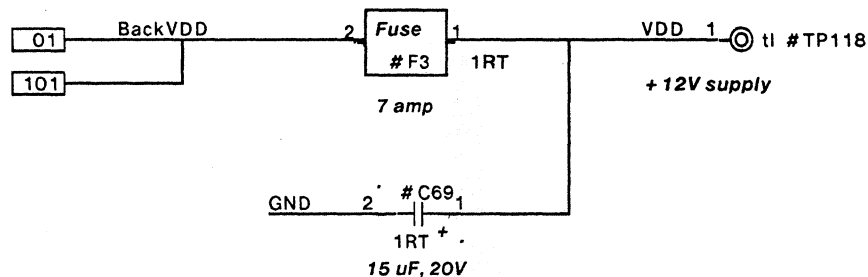
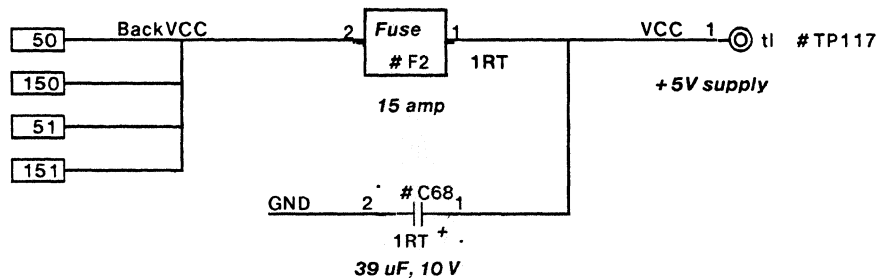


### Maintenance Panel Cable Connector

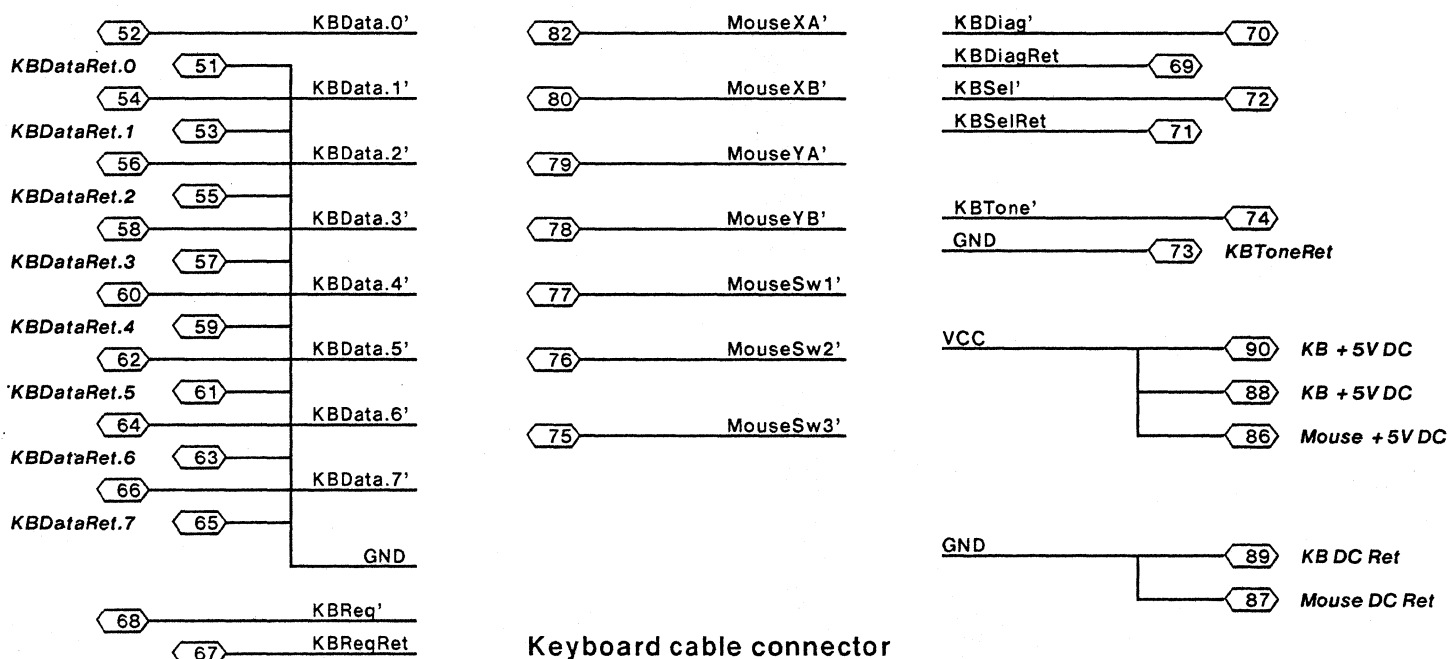
14-pin male connector  
Xerox 713W13320

(Subtract 400 from pin numbers to get physical pin number)





### CABLES



### Keyboard cable connector

40-pin male connector

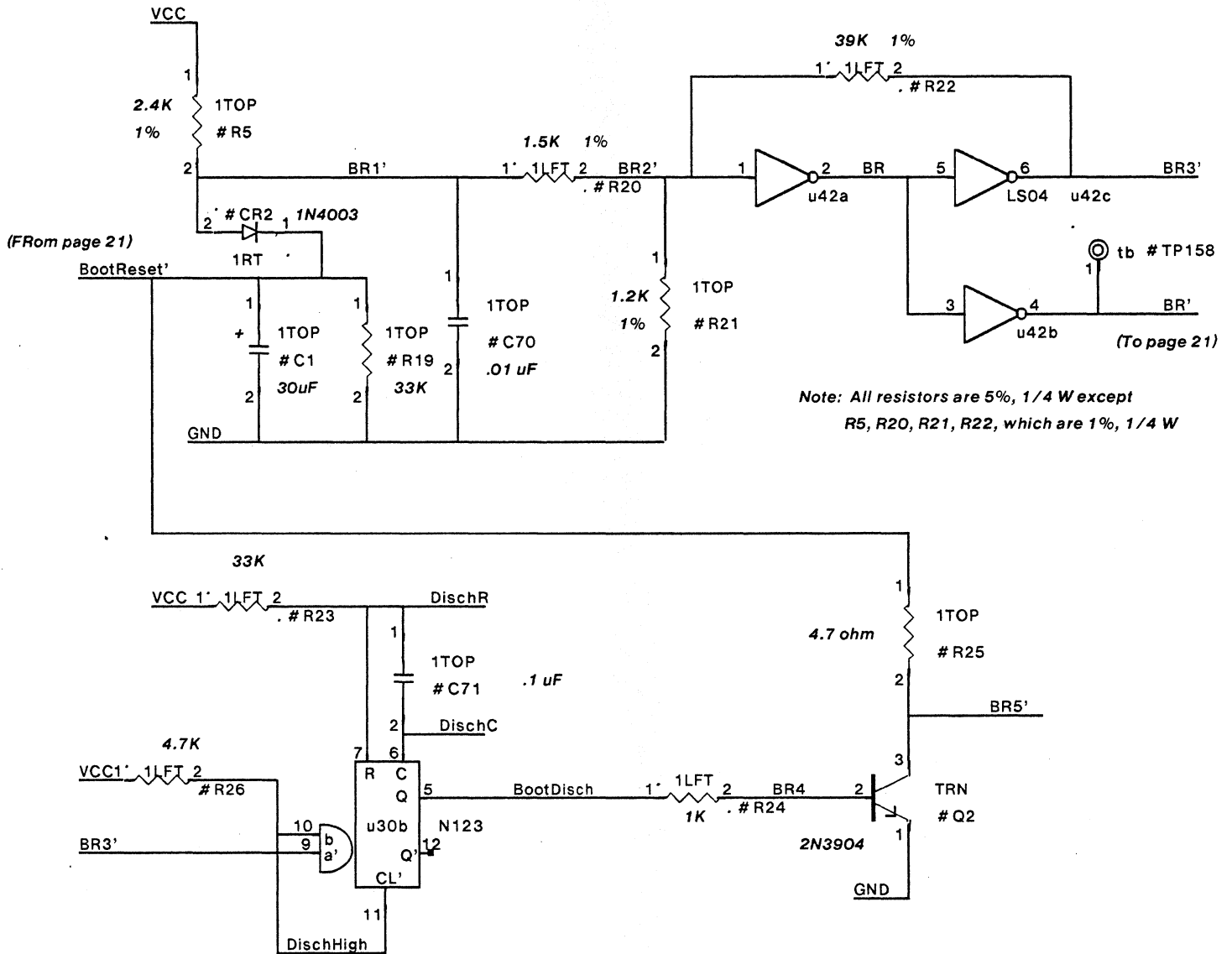
Xerox 713W12720

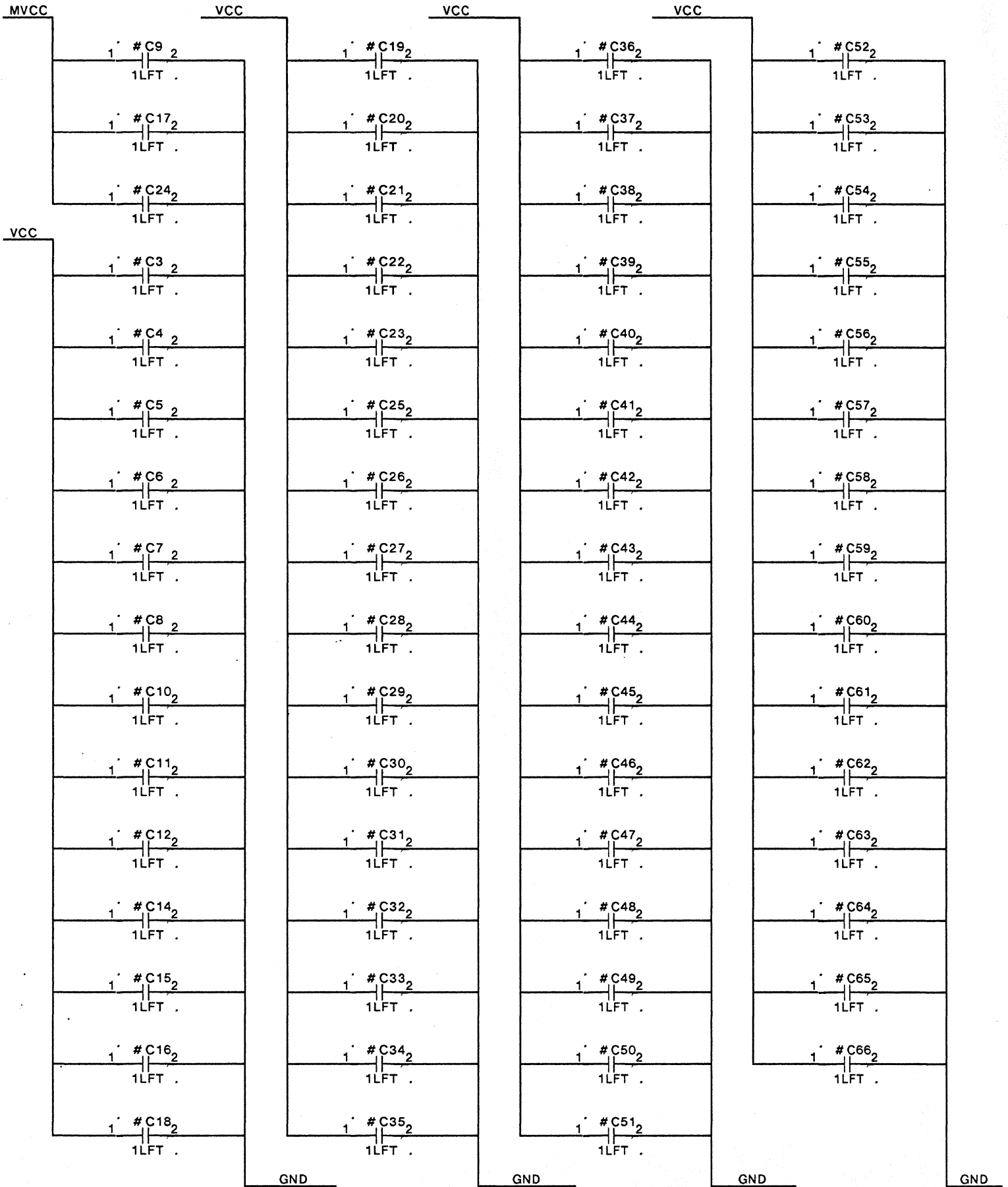
(Subtract 50 from above pin numbers to get physical pin number)

XEROX	PROPRIETARY NOTE ON COVER SHEET APPLIES TO ALL SHEETS		DWG SIZE A4	DWG NO. 156P11446		SHEET REV. B
	TITLE	SCHMATIC, IOP		SHEET	29 OF	

## Power up and Power down circuit

This circuit take care of the power up and power down requirements. On power up, IOPReset' should be active for at least 50 msec after the power (Vcc) has stabilized. On power down, IOPReset' should be made active when the Vcc has dropped to about 4V. This is so the disks will not be allowed to have WriteGate active when the power is disappearing. A Schmitt trigger causes the BR' signal (which becomes IOPReset'), to go active quickly when Vcc drops to about 4V. This causes the Discharge one-shot to fire which quickly discharges the capacitor C1. This causes the normal power up sequence to occur even if the power glitches momentarily.





NOTE: All capacitors on this page are ceramic capacitors, 50V, 0.10 uf, part number 702W05218

XEROX	PROPRIETARY NOTE ON COVER SHEET APPLIES TO ALL SHEETS		DWG SIZE A4	DWG NO. 156P11446		SHEET REV. B
	TITLE SCHEMATIC, IOP			SHEET 31 OF		

Comments:

- 1) Designator notation notes: u1-99 = U1-99, v0-99 = U100-199, w0-99 = U200-299
- 2) The last item on lines below, preceded by a semicolon (;), is the schematic page number on which the test point, connector or signal information originates.
- 3) Line with no page number was a continuation of the previous line.

#TP1	.1i	AltoPPIIntA	;21	#TP145	.1i	CurState.1	;12
#TP10	.1i	CPUAD.07	;02	#TP146	.1i	CurState.2	;12
#TP100	.1i	UpMouseY	;19	#TP147	.1i	CurState.3	;12
#TP101	.1i	EnKBBell	;20	#TP148	.1i	CurState.4	;12
#TP102	.1i	BootReset'	;21	#TP149	.1i	CurState.5	;12
#TP103	.1i	AltoIPReset'	;21	#TP15	.1i	CpuRD'	;01
#TP104	.1i	PrinterClk	;22	#TP150	.1i	CurState.6	;12
#TP105	.1i	KBBC	;22	#TP151	.1i	CPInWakeReqS	;17
#TP106	.1i	Time.24	;25	#TP152	.1i	CPOutWakeReqS	;17
#TP107	.1i	Time.16	;25	#TP153	.1i	IOPAttnSync'	;17
#TP108	.1i	Time.08	;25	#TP154	.1i	EnIOPRqAd'	;17
#TP109	.1i	Time.00	;25	#TP156	.1i	CpuReady	;21
#TP110	.1i	TimeData24	;26	#TP157	.1i	FDId	;27
#TP111	.1i	TimeData16	;26	#TP158	.1i	BR'	;30
#TP112	.1i	TimeData08	;26	#TP159	.1i	FloppyIntReq	;14
#TP113	.1i	TimeData'	;26	#TP16	.1i	CpuWR'	;01
#TP114	.1i	HdLdC	;27	#TP160	.1i	Int.5	;14
#TP115	.1i	HdLdR	;27	#TP162	.1i	CpuRST5	;14
#TP116	.1i	ppClk	;27	#TP17	.1i	CPUAddr.00	;01
#TP117	.1i	VCC	;29	#TP18	.1i	CPUAddr.01	;01
#TP118	.1i	VDD	;29	#TP19	.1i	CPUAddr.02	;01
#TP119	.1i	VFF	;29	#TP2	.1i	CPAttn	;15
#TP12	.1i	IPReset	;01	#TP20	.1i	CPUAddr.03	;01
#TP120	.1i	SelHostAd'	;05	#TP21	.1i	CPUAddr.04	;01
#TP121	.1i	SelPROMBank0'	;05	#TP22	.1i	CPUAddr.05	;01
#TP122	.1i	SelPROMBank1'	;05	#TP23	.1i	CPUAddr.06	;01
#TP123	.1i	SelPROMBank2'	;05	#TP24	.1i	CPUAddr.07	;01
#TP124	.1i	SelPROMBank3'	;05	#TP26	.1i	CpuHold	;01
#TP125	.1i	SelRAMBank0'	;05	#TP28	.1i	PU1	;01
#TP126	.1i	SelRAMBank1'	;05	#TP29	.1i	SelAltoPPI'	;05
#TP127	.1i	SelRAMBank2'	;05	#TP3	.1i	CPUAD.00	;02
#TP128	.1i	SelRAMBank3'	;05	#TP30	.1i	SelPrinter'	;05
#TP129	.1i	SelRAMBank4'	;05	#TP31	.1i	SelTimer'	;05
#TP13	.1i	CpuHoldAck	;01	#TP32	.1i	ReadKBData'	;05
#TP130	.1i	SelRAMBank5'	;05	#TP33	.1i	ClrMouseXY'	;05
#TP131	.1i	SelRAMBank6'	;05	#TP34	.1i	IPADData.0	;06
#TP132	.1i	SelRAMBank7'	;05	#TP35	.1i	IPADData.1	;06
#TP133	.1i	SelRAMBank8'	;05	#TP36	.1i	IPADData.2	;06
#TP134	.1i	SelRAMBank9'	;05	#TP37	.1i	IPADData.3	;06
#TP135	.1i	SelRAMBank10'	;05	#TP38	.1i	IPADData.4	;06
#TP136	.1i	SelRAMBank11'	;05	#TP39	.1i	IPADData.5	;06
#TP137	.1i	SelRAMBank12'	;05	#TP4	.1i	CPUAD.01	;02
#TP138	.1i	SelRAMBank13'	;05	#TP40	.1i	IPADData.6	;06
#TP139	.1i	SelRAMBank14'	;05	#TP41	.1i	IPADData.7	;06
#TP14	.1i	CpuIO/M'	;01	#TP42	.1i	EnIPData	;06
#TP140	.1i	SelRAMBank15'	;05	#TP43	.1i	EnIPADData'	;06
#TP141	.1i	DmaWaitReq	;06	#TP44	.1i	EnIPADData	;06
#TP142	.1i	ClrFDC1k'	;10	#TP46	.1i	DmaHLDA	;07
#TP143	.1i	FDCDb1Den'	;12	#TP47	.1i	DmaRDY	;07
#TP144	.1i	FDCRC1k	;12	#TP49	.1i	DmaHoldReq	;07
				#TP5	.1i	CPUAD.02	;02
				#TP50	.1i	DmaIORd'	;07

#TP51	.1i	DmaIOWr'	;07	C053	GND	;29
#TP52	.1i	DmaMemRd'	;07	C054	KBData.1'	;29
#TP53	.1i	DmaMemWr'	;07	C055	GND	;29
#TP54	.1i	FloppyDataAck'	;07	C056	KBData.2'	;29
#TP55	.1i	CPDmaAck'	;07	C057	GND	;29
#TP56	.1i	DmaEndCount	;07	C058	KBData.3'	;29
#TP58	.1i	DmaAddrStrobe	;07	C059	GND	;29
#TP6	.1i	CPUAD.03	;02	C060	KBData.4'	;29
#TP60	.1i	pFDChdLd	;08	C061	GND	;29
#TP61	.1i	FDCStep	;08	C062	KBData.5'	;29
#TP62	.1i	FDCDircIn	;08	C063	GND	;29
#TP63	.1i	FDCWrGate	;09	C064	KBData.6'	;29
#TP64	.1i	FDCTG43	;08	C065	GND	;29
#TP65	.1i	FDCWrData	;09	C066	KBData.7'	;29
#TP66	.1i	WDS	;09	C067	KBReqRet	;29
#TP67	.1i	Early	;09	C068	KBReq'	;29
#TP68	.1i	Late	;09	C069	KBdiagRet	;29
#TP69	.1i	WDLC1r	;09	C070	KBdiag'	;29
#TP7	.1i	CPUAD.04	;02	C071	KBSe1Ret	;29
#TP70	.1i	Floppy2MHz	;10	C072	KBSe1'	;29
#TP71	.1i	Floppy16MHz	;10	C073	GND	;29
#TP72	.1i	FDChdLdDone	;10	C074	KBtone'	;29
#TP73	.1i	FDCIndex'	;11	C075	MouseSw3'	;29
#TP74	.1i	FDCTr00'	;11	C076	MouseSw2'	;29
#TP75	.1i	FDCWrProt'	;11	C077	MouseSw1'	;29
#TP76	.1i	High/a	;11	C078	MouseYB'	;29
#TP79	.1i	VFOEn'	;12	C079	MouseYA'	;29
#TP8	.1i	CPUAD.05	;02	C080	MouseXB'	;29
#TP80	.1i	SepState.0	;12	C082	MouseXA'	;29
#TP81	.1i	SepState.1	;12	C086	VCC	;29
#TP82	.1i	SepState.2	;12	C087	GND	;29
#TP83	.1i	PU2	;12	C088	VCC	;29
#TP84	.1i	SepState.3	;12	C089	GND	;29
#TP85	.1i	SepState.4	;12	C090	VCC	;29
#TP86	.1i	SepState.5	;12	C201	GND	;28
#TP87	.1i	SepState.6	;12	C202	FDWrCurrent'	;28
#TP88	.1i	pIOPReq'	;17	C209	GND	;28
#TP9	.1i	CPUAD.06	;02	C210	FDTwoSided'	;28
#TP90	.1i	CPDmaReq	;17	C211	GND	;28
#TP91	.1i	KBIntr	;18	C212	FDDiskChange'	;28
#TP92	.1i	oldXA	;19	C213	GND	;28
#TP93	.1i	oldXB	;19	C214	FDSe1Side0	;28
#TP94	.1i	oldYA	;19	C215	GND	;28
#TP95	.1i	oldYB	;19	C216	FDInUse'	;28
#TP97	.1i	EnMouseX'	;19	C217	GND	;28
#TP98	.1i	UpMouseX	;19	C218	FDHdLoad'	;28
#TP99	.1i	EnMouseY'	;19	C219	GND	;28
				C220	FDIndex'	;28
C001		GND	;28	C221	GND	;28
C002		PTTxData	;28	C222	FDReady'	;28
C003		PTRxData	;28	C223	GND	;28
C004		PTRTS	;28	C225	GND	;28
C005		PTCTS	;28	C226	FDSe1Drive1'	;28
C006		PTDSR	;28	C227	GND	;28
C007		GND	;28	C229	GND	;28
C008		PTCD	;28	C231	GND	;28
C010		PTDTR	;28	C233	GND	;28
C051		GND	;29	C234	FDDirectionIn'	;28
C052		KBData.0'	;29	C235	GND	;28

C236	FDStep'	:28	E072	IPAddr.04	:01
C237	GND	:28	E073	IPAddr.06	:01
C238	FDWrData'	:28	E074	IPAddr.08	:01
C239	GND	:28	E075	IPAddr.10	:01
C240	FDWrGate'	:28	E076	IPAddr.12	:01
C241	GND	:28	E077	IPAddr.14	:01
C242	FDTrk00'	:28	E079	IPMemRd'	:01
C243	GND	:28	E081	CSWE.a'	:15
C244	FDWrProt'	:28	E082	CSWE.c'	:15
C245	GND	:28	E083	CSWE.e'	:15
C246	FDRdData'	:28	E084	IOPReq'	:17
C247	GND	:28	E087	IPMemWr'	:01
C249	GND	:28	E091	WriteTPCHigh'	:15
C401	IncMPanel'	:28	E092	IPData.00	:06
C402	ClrMPanel'	:28	E093	IPData.02	:06
C403	BlankMPanel	:28	E094	IPData.04	:06
C404	PowerFailed'	:28	E095	IPData.06	:06
C405	SetT'	:28	E096	SwTAddrSync	:15
C406	GND	:28	E098	BackVFF	:29
C407	GND	:28	E101	BackVDD	:29
C408	VCC	:28	E115	BRClk	:22
C409	VCC	:28	E117	IPResetBP'	:01
C410	EnMPsignals'	:28	E118	FDHdLoad'	:11
C411	1HzClk'	:28	E119	FDStep'	:11
C412	AltBoot'	:28	E121	IOPCtl←'	:15
C413	BootReset'	:28	E122	FDDirectionIn'	:11
C414	MVcc	:28	E124	FDWrGate'	:11
			E125	FDWrData'	:11
E001	BackVDD	:29	E134	←IOPStatus'	:15
E009	ppClk	:17	E145	X.9	:15
E012	IOPClk	:01	E146	X.11	:15
E015	IPDataOut	:01	E147	X.13	:15
E016	SelTroyMode	:11	E148	X.15	:15
E017	Wait	:17	E150	BackVCC	:29
E018	FDCIndex	:11	E151	BackVCC	:29
E019	FDCReady	:11	E161	DmaCh2Ack'	:07
E021	IOPOData←'	:16	E162	DmaCh3Ack'	:07
E022	FDCTr00	:11	E163	ExtWaitReq'	:21
E024	FDCWrProt	:11	E164	Int.6	:14
E025	FDRawRdData	:11	E165	MiscInt	:14
E034	←IOPIData'	:16	E166	SelBank1b'	:05
E036	IPALE	:01	E167	SelBank1d'	:05
E037	CSParErr	:20	E168	SelBank5'	:05
E045	X.8	:15	E169	IPAddr.01	:01
E046	X.10	:15	E171	IPAddr.03	:01
E047	X.12	:15	E172	IPAddr.05	:01
E048	X.14	:15	E173	IPAddr.07	:01
E050	BackVCC	:29	E174	IPAddr.09	:01
E051	BackVCC	:29	E175	IPAddr.11	:01
E061	DmaCh2Req	:07	E176	IPAddr.13	:01
E062	DmaCh3Req	:07	E177	IPAddr.15	:01
E063	DmaCycle	:07	E179	IPIORD'	:01
E064	Int.5	:14	E181	CSWE.b'	:15
E065	Int.7	:14	E182	CSWE.d'	:15
E066	SelBank1a'	:05	E183	CSWE.f'	:15
E067	SelBank1c'	:05	E184	ClrIOPReq'	:17
E068	SelBank4'	:05	E187	IPIOWr'	:01
E069	IPAddr.00	:01	E188	ReadCSEn'	:15
E071	IPAddr.02	:01	E189	IOPWaitSync	:15

XEROX	PROPRIETARY NOTE ON COVER SHEET APPLIES TO ALL SHEETS		DWG SIZE	DWG NO. 156P11446	SHEET REV.
	TITLE SCHEMATIC, IOP		A4	SHEET 34 OF	B

E191 WriteTPCLow ;15  
 E192 IPData.01 ;06  
 E193 IPData.03 ;06  
 E194 IPData.05 ;06  
 E195 IPData.07 ;06  
 E196 SwtAddrSync' ;15  
 E198 BackVFF ;29  
  
 1HzClk': u23.11i ;25  
 1HzClk': u24.8i ;26  
 1HzClk': C411 ;28  
  
 1HzClk: u23.9o, u38.3i ;25  
  
 1HzClkB': u20.4i ;20  
 1HzClkB': u24.12o ;26  
  
 AltBoot': u52.9o ;26  
 AltBoot': u24.2i ;26  
 AltBoot': C412 ;28  
  
 AltBootB': u48.2i ;20  
 AltBootB': u24.18o ;26  
  
 AltoIPReset': v07.5i ;21  
 AltoIPReset': #TP103.1i, #J1.8i ;21  
 AltoIPReset': #R1.2o ;27  
  
 AltoPPIIntA: u55.12i ;14  
 AltoPPIIntA: #TP1.1i, #J1.7i ;21  
  
 BackVCC: E150, E51, E151, E50 ;29  
 BackVCC: #F2.2i  
  
 BackVDD: E101, E1, #F3.2i ;29  
  
 BackVFF: E198, E98, #F1.2i ;29  
  
 Bell0: #R8.1i, #R7.2o, #Q1.2i ;27  
  
 Bell1: #CR3.2i, #Q1.3i, #R9.1i ;27  
  
 BlankMPanel: u47.12o ;20  
 BlankMPanel: C403 ;28  
  
 BootDisch: u30.5o, #R24.1i ;30  
  
 BootReset': #TP102.1i ;21  
 BootReset': C413 ;28  
 BootReset': #CR2.1o, #C1.1i ;30  
 BootReset': #R25.1i, #R19.1i  
  
 BR': v07.4i ;21  
 BR': #TP158.1i, u42.4o ;30  
  
 BR1': #C70.1i, #CR2.2i, #R5.2i ;30  
 BR1': #R20.1i

BR2': #R22.1i, #R21.1i, #R20.2o ;30  
 BR2': u42.1i  
  
 BR3': #R22.2o, u42.6o ;30  
 BR3': u30.9i ;30  
  
 BR4: #R24.2o, #Q2.2i ;30  
  
 BR5': #Q2.3i, #R25.2i ;30  
  
 BR: u42.3i, u42.2o, u42.5i ;30  
  
 BRC1k: y01.8o, E115, u50.18i ;22  
 BRC1k: u50.15i, u50.9i  
  
 ClearTime': u35.7o ;20  
 ClearTime': u23.17i ;25  
 ClearTime': u52.6i ;26  
  
 ClearTime: u47.16o, u35.13i ;20  
  
 ClearWDL': u29.1i, u29.13i ;09  
 ClearWDL': u27.1o ;09  
  
 ClkIOPCt1: v26.8o, v41.11i ;15  
  
 ClkReadSR': u09.6o ;25  
 ClkReadSR': u40.2i, u54.2i, u26.2i ;26  
 ClkReadSR': u11.2i ;26  
  
 ClrCPDmaCompl': u44.9o ;05  
 ClrCPDmaCompl': u86.3i ;17  
  
 ClrFDCDataReq': u57.8o ;08  
 ClrFDCDataReq': u56.13i ;08  
  
 ClrFDC1k': u28.1i, #TP142.1i ;10  
 ClrFDC1k': #R28.2o ;27  
  
 ClrIOPReq': E184, v38.3i ;17  
  
 ClrMouseXY': u44.10o, #TP33.1i ;05  
 ClrMouseXY': u33.8i ;19  
 ClrMouseXY': u31.8i ;19  
 ClrMouseXY': u32.8i ;19  
 ClrMouseXY': u34.8i ;19  
  
 ClrMPanel': u49.5o ;20  
 ClrMPanel': C402 ;28  
  
 ClrSeconds: u53.2i, u53.12i ;25  
 ClrSeconds: u39.2i, u39.12i ;25  
 ClrSeconds: u25.2i, u09.3o ;25  
 ClrSeconds: u25.12i  
 ClrSeconds: u10.2i, u10.12i ;25  
  
 ClrTime: u23.3o ;25  
 ClrTime: u09.1i ;25



ClrTODIntr': u44.13o ;05  
 ClrTODIntr': u20.3i ;20  
  
 CPAttn: u55.13i ;14  
 CPAttn: v41.15o, #TP2.1i ;15  
 CPAttn: v40.17i ;15  
 CPAttn: v23.2i ;17  
  
 CPClk: v28.11i ;15  
 CPClk: v37.11i ;17  
 CPClk: v26.3o ;17  
  
 CPDmaAck': u74.16o, #TP55.1i ;07  
 CPDmaAck': u87.10i ;16  
 CPDmaAck': u87.13i ;16  
 CPDmaAck': u72.5i ;17  
  
 CPDmaAck: v06.1i ;06  
 CPDmaAck: u72.1i ;17  
 CPDmaAck: u72.6o ;17  
  
 CPDmaComplete': u86.6o ;17  
 CPDmaComplete': v23.11i ;17  
  
 CPDmaIn': u88.3o ;15  
 CPDmaIn': v23.17i ;17  
  
 CPDmaIn: u88.2o ;15  
 CPDmaIn: v20.1i ;17  
  
 CPDmaMode': v22.14o ;15  
 CPDmaMode': v20.15i ;17  
 CPDmaMode': v23.8i ;17  
  
 CPDmaReq: u61.18i ;07  
 CPDmaReq: #TP90.1i, v20.4o ;17  
  
 CPInEnabled: v33.6o ;15  
 CPInEnabled: v21.2i ;16  
  
 CPInIntReq': v21.6o ;16  
 CPInIntReq': v23.15i ;17  
  
 CPInIntReq: v21.5o ;16  
 CPInIntReq: v20.3i ;17  
  
 CPInRd': v42.1i ;16  
 CPInRd': v35.3o, v35.10i ;16  
 CPInRd': v36.3i ;16  
  
 CPInResetRd': v35.8o ;16  
 CPInResetRd': v21.1i ;16  
  
 CPInWakeReq: v36.6o ;16  
 CPInWakeReq: v37.3i ;17  
  
 CPInWakeReqS: #TP151.1i, v37.5o ;17  
 CPInWakeReqS: v38.4i ;17

CPInWr': v26.11o, v42.11i ;16  
 CPInWr': v21.3i ;16  
 CPInWr': v36.4i ;16  
  
 CPOutIntReq': v21.9o ;16  
 CPOutIntReq': v23.13i ;17  
  
 CPOutIntReq: v21.8o ;16  
 CPOutIntReq: v20.2i ;17  
  
 CPOutResetRd': v35.11o ;16  
 CPOutResetRd': v36.10i ;16  
  
 CPOutWakeReq: v36.8o ;16  
 CPOutWakeReq: v37.7i ;17  
  
 CPOutWakeReqS: v38.7i ;17  
 CPOutWakeReqS: #TP152.1i, v37.9o ;17  
  
 CPOutWr': v35.6o ;16  
 CPOutWr': v21.10i, v43.11i ;16  
 CPOutWr': v36.11i ;16  
  
 CPUAD.00: u92.19i ;01  
 CPUAD.00: u93.3i ;01  
 CPUAD.00: v29.17o ;02  
 CPUAD.00: v30.17o ;02  
 CPUAD.00: v31.17o ;02  
 CPUAD.00: v32.17o ;02  
 CPUAD.00: #TP3.1i ;02  
 CPUAD.00: u70.11o ;03  
 CPUAD.00: u69.11o ;03  
 CPUAD.00: u68.11o ;03  
 CPUAD.00: u67.11o ;03  
 CPUAD.00: u66.11o ;04  
 CPUAD.00: u65.11o ;04  
 CPUAD.00: u64.11o ;04  
 CPUAD.00: u63.11o ;04  
 CPUAD.00: u91.2i ;06  
 CPUAD.00: v09.2i ;06  
 CPUAD.00: u83.11o ;23  
 CPUAD.00: u82.11o ;23  
 CPUAD.00: u81.11o ;23  
 CPUAD.00: u80.11o ;23  
 CPUAD.00: u79.11o ;24  
 CPUAD.00: u78.11o ;24  
 CPUAD.00: u77.11o ;24  
 CPUAD.00: u76.11o ;24  
  
 CPUAD.01: u92.18i ;01  
 CPUAD.01: u93.4i ;01  
 CPUAD.01: v29.16o ;02  
 CPUAD.01: v30.16o ;02  
 CPUAD.01: v31.16o ;02  
 CPUAD.01: v32.16o ;02  
 CPUAD.01: #TP4.1i ;02  
 CPUAD.01: u70.12o ;03  
 CPUAD.01: u69.12o ;03  
 CPUAD.01: u68.12o ;03

XEROX	PROPRIETARY NOTE ON COVER SHEET APPLIES TO ALL SHEETS		DWG SIZE A4	DWG NO. 156P11446	SHEET REV. B
	TITLE SCHEMATIC, IOP		SHEET 36	OF	

CPUAD.01:	u67.12o	;03	CPUAD.03:	u83.14o	;23
CPUAD.01:	u66.12o	;04	CPUAD.03:	u82.14o	;23
CPUAD.01:	u65.12o	;04	CPUAD.03:	u81.14o	;23
CPUAD.01:	u64.12o	;04	CPUAD.03:	u80.14o	;23
CPUAD.01:	u63.12o	;04	CPUAD.03:	u79.14o	;24
CPUAD.01:	u91.3i	;06	CPUAD.03:	u78.14o	;24
CPUAD.01:	v09.3i	;06	CPUAD.03:	u77.14o	;24
CPUAD.01:	u83.12o	;23	CPUAD.03:	u76.14o	;24
CPUAD.01:	u82.12o	;23			
CPUAD.01:	u81.12o	;23	CPUAD.04:	u92.15i	;01
CPUAD.01:	u80.12o	;23	CPUAD.04:	u93.13i	;01
CPUAD.01:	u79.12o	;24	CPUAD.04:	v29.13o	;02
CPUAD.01:	u78.12o	;24	CPUAD.04:	v30.13o	;02
CPUAD.01:	u77.12o	;24	CPUAD.04:	v31.13o	;02
CPUAD.01:	u76.12o	;24	CPUAD.04:	v32.13o	;02
			CPUAD.04:	#TP7.1i	;02
CPUAD.02:	u92.17i	;01	CPUAD.04:	v01.11o	;03
CPUAD.02:	u93.7i	;01	CPUAD.04:	v00.11o	;03
CPUAD.02:	v29.15o	;02	CPUAD.04:	u99.11o	;03
CPUAD.02:	v30.15o	;02	CPUAD.04:	u98.11o	;03
CPUAD.02:	v31.15o	;02	CPUAD.04:	u94.11o	;04
CPUAD.02:	v32.15o	;02	CPUAD.04:	u97.11o	;04
CPUAD.02:	#TP5.1i	;02	CPUAD.04:	u96.11o	;04
CPUAD.02:	u70.13o	;03	CPUAD.04:	u95.11o	;04
CPUAD.02:	u69.13o	;03	CPUAD.04:	u91.6i	;06
CPUAD.02:	u68.13o	;03	CPUAD.04:	v09.6i	;06
CPUAD.02:	u67.13o	;03	CPUAD.04:	v18.11o	;23
CPUAD.02:	u66.13o	;04	CPUAD.04:	v17.11o	;23
CPUAD.02:	u65.13o	;04	CPUAD.04:	v16.11o	;23
CPUAD.02:	u64.13o	;04	CPUAD.04:	v15.11o	;23
CPUAD.02:	u63.13o	;04	CPUAD.04:	v11.11o	;24
CPUAD.02:	u91.4i	;06	CPUAD.04:	v14.11o	;24
CPUAD.02:	v09.4i	;06	CPUAD.04:	v13.11o	;24
CPUAD.02:	u83.13o	;23	CPUAD.04:	v12.11o	;24
CPUAD.02:	u82.13o	;23			
CPUAD.02:	u81.13o	;23	CPUAD.05:	u92.14i	;01
CPUAD.02:	u80.13o	;23	CPUAD.05:	u93.14i	;01
CPUAD.02:	u79.13o	;24	CPUAD.05:	v29.11o	;02
CPUAD.02:	u78.13o	;24	CPUAD.05:	v30.11o	;02
CPUAD.02:	u77.13o	;24	CPUAD.05:	v31.11o	;02
CPUAD.02:	u76.13o	;24	CPUAD.05:	v32.11o	;02
			CPUAD.05:	#TP8.1i	;02
CPUAD.03:	u92.16i	;01	CPUAD.05:	v01.12o	;03
CPUAD.03:	u93.8i	;01	CPUAD.05:	v00.12o	;03
CPUAD.03:	v29.14o	;02	CPUAD.05:	u99.12o	;03
CPUAD.03:	v30.14o	;02	CPUAD.05:	u98.12o	;03
CPUAD.03:	v31.14o	;02	CPUAD.05:	u94.12o	;04
CPUAD.03:	v32.14o	;02	CPUAD.05:	u97.12o	;04
CPUAD.03:	#TP6.1i	;02	CPUAD.05:	u96.12o	;04
CPUAD.03:	u70.14o	;03	CPUAD.05:	u95.12o	;04
CPUAD.03:	u69.14o	;03	CPUAD.05:	u91.7i	;06
CPUAD.03:	u68.14o	;03	CPUAD.05:	v09.7i	;06
CPUAD.03:	u67.14o	;03	CPUAD.05:	v18.12o	;23
CPUAD.03:	u66.14o	;04	CPUAD.05:	v17.12o	;23
CPUAD.03:	u65.14o	;04	CPUAD.05:	v16.12o	;23
CPUAD.03:	u64.14o	;04	CPUAD.05:	v15.12o	;23
CPUAD.03:	u63.14o	;04	CPUAD.05:	v11.12o	;24
CPUAD.03:	u91.5i	;06	CPUAD.05:	v14.12o	;24
CPUAD.03:	v09.5i	;06	CPUAD.05:	v13.12o	;24

CPUAD.05: v12.12o ;24

CPUAD.06: u92.13i ;01  
CPUAD.06: u93.17i ;01  
CPUAD.06: v29.10o ;02  
CPUAD.06: v30.10o ;02  
CPUAD.06: v31.10o ;02  
CPUAD.06: v32.10o ;02  
CPUAD.06: #TP9.1i ;02  
CPUAD.06: v01.13o ;03  
CPUAD.06: v00.13o ;03  
CPUAD.06: u99.13o ;03  
CPUAD.06: u98.13o ;03  
CPUAD.06: u94.13o ;04  
CPUAD.06: u97.13o ;04  
CPUAD.06: u96.13o ;04  
CPUAD.06: u95.13o ;04  
CPUAD.06: u91.8i ;06  
CPUAD.06: v09.8i ;06  
CPUAD.06: v18.13o ;23  
CPUAD.06: v17.13o ;23  
CPUAD.06: v16.13o ;23  
CPUAD.06: v15.13o ;23  
CPUAD.06: v11.13o ;24  
CPUAD.06: v14.13o ;24  
CPUAD.06: v13.13o ;24  
CPUAD.06: v12.13o ;24

CPUAD.07: u92.12i ;01  
CPUAD.07: u93.18i ;01  
CPUAD.07: v29.9o ;02  
CPUAD.07: v30.9o ;02  
CPUAD.07: v31.9o ;02  
CPUAD.07: v32.9o ;02  
CPUAD.07: #TP10.1i ;02  
CPUAD.07: v01.14o ;03  
CPUAD.07: v00.14o ;03  
CPUAD.07: u99.14o ;03  
CPUAD.07: u98.14o ;03  
CPUAD.07: u94.14o ;04  
CPUAD.07: u97.14o ;04  
CPUAD.07: u96.14o ;04  
CPUAD.07: u95.14o ;04  
CPUAD.07: u91.9i ;06  
CPUAD.07: v09.9i ;06  
CPUAD.07: v18.14o ;23  
CPUAD.07: v17.14o ;23  
CPUAD.07: v16.14o ;23  
CPUAD.07: v15.14o ;23  
CPUAD.07: v11.14o ;24  
CPUAD.07: v14.14o ;24  
CPUAD.07: v13.14o ;24  
CPUAD.07: v12.14o ;24

CPUAddr.00: u92.28o, v10.3i ;01  
CPUAddr.00: #TP17.1i ;01

CPUAddr.01: u92.27o, v10.4i ;01  
CPUAddr.01: #TP18.1i ;01

CPUAddr.02: u92.26o, v10.7i ;01  
CPUAddr.02: #TP19.1i ;01

CPUAddr.03: u92.25o, v10.8i ;01  
CPUAddr.03: #TP20.1i ;01

CPUAddr.04: u92.24o, v10.13i ;01  
CPUAddr.04: #TP21.1i ;01

CPUAddr.05: u92.23o, v10.14i ;01  
CPUAddr.05: #TP22.1i ;01

CPUAddr.06: u92.22o, v10.17i ;01  
CPUAddr.06: #TP23.1i ;01

CPUAddr.07: u92.21o, v10.18i ;01  
CPUAddr.07: #TP24.1i ;01

CpuALE: u92.30o ;01  
CpuALE: v39.6i ;01

CpuClk: u92.37o ;01  
CpuClk: v39.2i ;01

CpuHold: v39.12o, #TP26.1i ;01  
CpuHold: u92.39i ;01

CpuHoldAck: u92.38o, #TP13.1i ;01  
CpuHoldAck: v39.17i ;07

CpuIO/M': u92.34o, #TP14.1i ;01  
CpuIO/M': u73.3i ;21

CpuRD': u92.32o, #TP15.1i ;01  
CpuRD': v04.11i ;01  
CpuRD': u73.1i ;21

CpuRD: v04.10o ;01  
CpuRD: v02.6i ;01  
CpuRD: v03.5i ;01

CpuReady: u92.35i ;01  
CpuReady: #TP156.1i, v39.5o ;21

CpuReset: v39.4i ;01  
CpuReset: u92.3o ;01

CpuResetIn': u92.36i ;01  
CpuResetIn': v07.6o ;21

CpuRST5: u92.9i ;01  
CpuRST5: u55.11o, #TP162.1i ;14

CpuWaitReq: u71.8o ;21  
CpuWaitReq: u71.5i ;21

CpuWR': u92.31o, #TP16.1i ;01  
CpuWR': v04.13i ;01

XEROX	PROPRIETARY NOTE ON COVER SHEET APPLIES TO ALL SHEETS		DWG SIZE A4	DWG NO. 156P11446	SHEET REV.
	TITLE SCHEMATIC, IOP			SHEET 38 OF	B

CpuWR': u73.2i ;21  
 CpuWR: v04.12o ;01  
 CpuWR: v02.10i ;01  
 CpuWR: v03.11i ;01  
 CSParErr: E37, u48.13i ;20  
 CSWE.a': v34.15o, E81 ;15  
 CSWE.b': v34.14o, E181 ;15  
 CSWE.c': v34.13o, E82 ;15  
 CSWE.d': v34.12o, E182 ;15  
 CSWE.e': v34.11o, E83 ;15  
 CSWE.f': v34.10o, E183 ;15  
 CurState.1: u13.2i, u12.6o, u14.2i ;12  
 CurState.1: #TP145.1i ;12  
 CurState.2: u13.3i, u12.9o, u14.3i ;12  
 CurState.2: #TP146.1i ;12  
 CurState.3: u13.4i, u12.12o ;12  
 CurState.3: u14.4i  
 CurState.3: #TP147.1i ;12  
 CurState.4: u13.7i, u12.15o ;12  
 CurState.4: u14.7i  
 CurState.4: #TP148.1i ;12  
 CurState.5: u13.6i, u12.16o ;12  
 CurState.5: u14.6i  
 CurState.5: #TP149.1i ;12  
 CurState.6: u13.5i, u12.19o ;12  
 CurState.6: u14.5i  
 CurState.6: #TP150.1i ;12  
 DiagKB: u05.10i, u05.11i, u05.9i ;18  
 DiagKB: u47.9o ;20  
 DisableFDC': u58.19i ;08  
 DisableFDC': u89.15o ;08  
 DisableFDC': u30.3i ;10  
 DisableFDC': u41.13i ;12  
 DisableFDC': u12.1i ;12  
 DisableWait': u89.2o ;08  
 DisableWait': u85.13i ;21  
 DisableWDCComp': u89.5o ;08  
 DisableWDCComp': u43.10i, u43.4i ;09  
 DischC: u30.6i, #C71.2i ;30  
 DischHigh: u30.10i, #R26.2o ;30  
 DischHigh: u30.11i  
 DischR: #C71.1i, u30.7i, #R23.2o ;30  
 DisCpuTest': v39.19i, v39.1i ;01  
 DisCpuTest': #TP27.1i  
 DisCpuTest': #R15.2o ;27  
 DisDmaTest': u74.19i, u74.1i ;07  
 DisDmaTest': #TP048.1i  
 DisDmaTest': #R12.2o ;27  
 DisFDCTest': u60.19i, u60.1i ;09  
 DisFDCTest': #TP59.1i  
 DisFDCTest': #R11.2o ;27  
 DmaAddr.00: u61.40o ;07  
 DmaAddr.00: u62.2i ;07  
 DmaAddr.01: u61.39o ;07  
 DmaAddr.01: u62.3i ;07  
 DmaAddr.02: u61.38o ;07  
 DmaAddr.02: u62.4i ;07  
 DmaAddr.03: u61.37o ;07  
 DmaAddr.03: u62.5i ;07  
 DmaAddr.04: u61.35o ;07  
 DmaAddr.04: u62.6i ;07  
 DmaAddr.05: u61.34o ;07  
 DmaAddr.05: u62.7i ;07  
 DmaAddr.06: u61.33o ;07  
 DmaAddr.06: u62.8i ;07  
 DmaAddr.07: u61.32o ;07  
 DmaAddr.07: u62.9i ;07  
 DmaAddrStrobe: #TP58.1i, u74.7o ;07  
 DmaAddrStrobe: u75.11i ;07  
 DmaAdStb: u61.8o, u74.13i ;07  
 DmaAEN: u61.9o ;07  
 DmaAEN: u74.15i ;07  
 DmaCh2Ack': v06.2i ;06  
 DmaCh2Ack': u74.14o, E161 ;07  
 DmaCh2Req: E61, u61.17i ;07  
 DmaCh3Ack': v06.3i ;06  
 DmaCh3Ack': u74.12o, E162 ;07  
 DmaCh3Req: E62, u61.16i ;07

XEROX	PROPRIETARY NOTE ON COVER SHEET APPLIES TO ALL SHEETS		DWG SIZE A4	DWG NO. 156P11446	SHEET REV. B
	TITLE SCHEMATIC, IOP			SHEET 39 OF	

DmaCycle': u75.1i ;07  
 DmaCycle': v05.4o ;07  
 DmaCycle: v02.2i ;01  
 DmaCycle: u93.1i ;01  
 DmaCycle: v10.1i ;01  
 DmaCycle: v03.2i ;01  
 DmaCycle: u74.5o, E63 ;07  
 DmaCycle: v05.3i ;07  
 DmaCycle: u62.1i ;07  
 DmaCycle: u19.19i ;07  
 DmaDAck0': u61.25o ;07  
 DmaDAck0': u74.2i ;07  
 DmaDAck1': u61.24o ;07  
 DmaDAck1': u74.4i ;07  
 DmaDAck2': u61.14o ;07  
 DmaDAck2': u74.6i ;07  
 DmaDAck3': u61.15o ;07  
 DmaDAck3': u74.8i ;07  
 DmaEndCount: #TP56.1i, u74.3o ;07  
 DmaEndCount: u71.13i ;08  
 DmaEndCount: u72.2i ;17  
 DmaHLDA: #TP46.1i, v39.3o, u61.7i ;07  
 DmaHoldReq: v39.8i ;01  
 DmaHoldReq: u61.10o, #TP49.1i ;07  
 DmaIORd': v05.11i ;01  
 DmaIORd': u61.1o, #TP50.1i ;07  
 DmaIORd': u19.3o ;07  
 DmaIORd': u73.6i ;21  
 DmaIORd: v05.10o ;01  
 DmaIORd: v03.4i, v03.3i ;01  
 DmaIOWr': v05.13i ;01  
 DmaIOWr': u61.2o, #TP51.1i ;07  
 DmaIOWr': u19.5o ;07  
 DmaIOWr': u73.7i ;21  
 DmaIOWr: v05.12o ;01  
 DmaIOWr: v03.12i, v03.13i ;01  
 DmaMemRd': v05.5i ;01  
 DmaMemRd': u61.3o, #TP52.1i ;07  
 DmaMemRd': u73.4i ;21  
 DmaMemRd: v05.6o ;01  
 DmaMemRd: v02.4i, v02.3i ;01  
 DmaMemWr': v05.9i ;01  
 DmaMemWr': u61.4o, #TP53.1i ;07  
 DmaMemWr': u73.5i ;21  
 DmaMemWr: v05.8o ;01  
 DmaMemWr: v02.12i, v02.13i ;01  
 DmaRDY: u61.6i ;07  
 DmaRDY: #TP47.1i ;07  
 DmaRDY: v19.3o ;07  
 DmaTC: u61.36o, u74.17i ;07  
 DmaTestRd': u87.6o ;13  
 DmaTestRd': u90.1i ;13  
 DmaTestWr': u87.3o ;13  
 DmaTestWr': u90.11i ;13  
 DmaWaitReq: v06.9o ;06  
 DmaWaitReq: #TP141.1i ;06  
 DmaWaitReq: u20.13i, v19.1i ;07  
 DmaWRL: u20.8o, v19.2i ;07  
 Early: #TP67.1i, u60.12o, u43.2i ;09  
 EarlyHold: u43.5o ;09  
 EarlyHold: u16.2i ;09  
 EmuWake: v41.12o ;15  
 EmuWake: v40.8i ;15  
 EmuWake: v38.1i ;17  
 EmuWake: v23.4i ;17  
 EnableIPADData': v09.19i ;06  
 EnableIPADData': v19.11o ;06  
 EnableIPData': u91.19i ;06  
 EnableIPData': v19.8o ;06  
 EnDataCS': v06.13i, v06.14i ;06  
 EnDataCS': #TP45.1i  
 EnDataCS': #R13.2o ;27  
 EnDataSep': u27.10o, u14.10i ;12  
 EnDataSep': u14.8i  
 EnDataSep': u13.8i, u13.10i ;12  
 EnFDCData': u59.19i, u72.8o ;08  
 EnFDCIn': u02.19i, u02.1i ;11  
 EnFDCIn': #TP77.1i  
 EnFDCIn': #R6.2o ;27  
 EnIOPReq': v38.13i, v38.14i ;17  
 EnIOPReq': #TP89.1i  
 EnIOPReq': #R14.2o ;27  
 EnIOPRqAd': v37.1i, #TP154.1i ;17  
 EnIOPRqAd': #R27.2o ;27

EnIPADData': v06.11o, #TP43.1i ;06  
 EnIPADData': u71.9i ;21  
  
 EnIPADData: #TP44.1i, v06.12o ;06  
 EnIPADData: v19.12i  
  
 EnIPData: #TP42.1i, v06.10o ;06  
 EnIPData: v19.9i  
  
 EnKBBell: u47.6o, #TP101.1i ;20  
 EnKBBell: u50.14i ;22  
  
 EnMouseProm': u21.13i, u21.14i ;19  
 EnMouseProm': #TP96.1i  
 EnMouseProm': #R16.2o ;27  
  
 EnMouseX': u21.9o ;19  
 EnMouseX': u33.7i, u33.12i ;19  
 EnMouseX': u31.7i ;19  
 EnMouseX': #TP97.1i ;19  
  
 EnMouseY': u21.11o ;19  
 EnMouseY': u34.7i, u34.12i ;19  
 EnMouseY': u32.7i ;19  
 EnMouseY': #TP99.1i ;19  
  
 EnMPsignals': u24.1i ;26  
 EnMPsignals': C410 ;28  
  
 EnRdWr': v02.1i, v04.8o, v02.15i ;01  
 EnRdWr': v03.1i, v03.15i ;01  
  
 ExtWaitReq': E163, u71.10i ;21  
 ExtWaitReq': u73.11o ;21  
  
 FDCCS: u72.11o, u72.9i ;08  
  
 FDCData.00: u58.14o ;08  
 FDCData.00: u59.18o ;08  
  
 FDCData.01: u58.13o ;08  
 FDCData.01: u59.17o ;08  
  
 FDCData.02: u58.12o ;08  
 FDCData.02: u59.16o ;08  
  
 FDCData.03: u58.11o ;08  
 FDCData.03: u59.15o ;08  
  
 FDCData.04: u58.10o ;08  
 FDCData.04: u59.14o ;08  
  
 FDCData.05: u58.9o ;08  
 FDCData.05: u59.13o ;08  
  
 FDCData.06: u58.8o ;08  
 FDCData.06: u59.12o ;08  
  
 FDCData.07: u58.7o ;08

FDCData.07: u59.11o ;08  
  
 FDCDataReq: u56.11i ;08  
 FDCDataReq: u60.18o ;09  
 FDCDataReq: u73.8i ;21  
  
 FDCdblDen': u58.37i ;08  
 FDCdblDen': u14.16i, u13.16i ;12  
 FDCdblDen': u71.3o, #TP143.1i ;12  
  
 FDCDirIn: u58.16o, #TP62.1i ;08  
 FDCDirIn: u37.6i ;11  
  
 FDCDRQ: u58.38o ;08  
 FDCDRQ: u60.2i ;09  
  
 FDCEarly: u58.17o ;08  
 FDCEarly: u60.8i ;09  
  
 FDCHdLd: u57.6o ;08  
 FDCHdLd: u30.2i ;10  
 FDCHdLd: u37.2i ;11  
  
 FDCHdLdDone: u58.23i ;08  
 FDCHdLdDone: u30.4o, #TP72.1i ;10  
  
 FDCIndex': u58.35i ;08  
 FDCIndex': u46.4o, #TP73.1i ;11  
  
 FDCIndex: u46.3i, u02.18o, E18 ;11  
  
 FDCLate: u58.18o ;08  
 FDCLate: u60.17i ;09  
  
 FDCRC1k: u58.26i ;08  
 FDCRC1k: u13.1i, u12.5o, u14.1i ;12  
 FDCRC1k: #TP144.1i ;12  
  
 FDCRdD: u41.5o, u41.12i ;12  
  
 FDCRdDataSync': u58.27i ;08  
 FDCRdDataSync': u41.1i, u41.8o ;12  
 FDCRdDataSync': #TP78.1i  
  
 FDCRdDataSync: u41.9o ;12  
 FDCRdDataSync: u14.15i, u13.15i ;12  
  
 FDCReady: u58.32i ;08  
 FDCReady: E19, u02.16o ;11  
  
 FDCStep: u58.15o, #TP61.1i ;08  
 FDCStep: u37.4i ;11  
  
 FDCTG43: u58.29o, #TP64.1i ;08  
 FDCTG43: u37.11i ;11  
  
 FDCTr00': u58.34i ;08  
 FDCTr00': u46.6o, #TP74.1i ;11

XEROX	PROPRIETARY NOTE ON COVER SHEET APPLIES TO ALL SHEETS		DWG SIZE A4	DWG NO. 156P11446	SHEET REV. B
	TITLE SCHEMATIC, IOP		SHEET 41	OF	

FDCTr00: u46.5i, u02.14o, E22 ;11  
 FDCVFOen': u58.33o ;08  
 FDCVFOen': u60.13i ;12  
 FDCVFOen': u73.10o ;21  
 FDCWD: u58.31o ;08  
 FDCWD: u60.6i ;09  
 FDCWG: u58.30o ;08  
 FDCWG: u60.4i ;09  
 FDCWrData: u57.2i, u57.1i ;09  
 FDCWrData: u60.14o, #TP65.1i ;09  
 FDCWrGate': u46.2o, u27.2i ;09  
 FDCWrGate: u46.1i ;09  
 FDCWrGate: u60.16o, #TP63.1i ;09  
 FDCWrGate: u37.8i ;11  
 FDCWrProt': u58.36i ;08  
 FDCWrProt': u46.8o, #TP75.1i ;11  
 FDCWrProt: u46.9i, u02.12o, E24 ;11  
 FDDirectionIn': E122, u37.14o ;11  
 FDDirectionIn': C234 ;28  
 FDDiskChange': u02.13i ;11  
 FDDiskChange': u01.9o ;27  
 FDDiskChange': C212 ;28  
 FDHdLoad': E118, u37.18o ;11  
 FDHdLoad': C218 ;28  
 FDHRC: #CR1.2i, #R2.2o, #C2.1i ;27  
 FDIId: u19.8i ;08  
 FDIId: u73.12o ;21  
 FDIId: #TP157.1i, s01.2i ;27  
 FDIndex': u02.2i ;11  
 FDIndex': u01.15o ;27  
 FDIndex': C220 ;28  
 FDIInUse': u17.16o ;11  
 FDIInUse': C216 ;28  
 FDRawRdData: E25, u02.5o ;11  
 FDRawRdData: u41.3i ;12  
 FDRdData': u02.15i ;11  
 FDRdData': u01.10o ;27  
 FDRdData': C246 ;28  
 FDReady': u02.4i ;11  
 FDReady': u01.14o ;27  
 FDReady': C222 ;28

FDSelDrive1': u17.18o ;11  
 FDSelDrive1': C226 ;28  
 FDSelSide0: u17.14o ;11  
 FDSelSide0: C214 ;28  
 FDStep': E119, u37.16o ;11  
 FDStep': C236 ;28  
 FDTrk00': u02.6i ;11  
 FDTrk00': u01.13o ;27  
 FDTrk00': C242 ;28  
 FDTwoSided': u02.17i ;11  
 FDTwoSided': u01.11o ;27  
 FDTwoSided': C210 ;28  
 FDWrCurrent': u37.9o ;11  
 FDWrCurrent': C202 ;28  
 FDWrData': E125, u37.3o ;11  
 FDWrData': C238 ;28  
 FDWrGate': E124, u37.12o ;11  
 FDWrGate': C240 ;28  
 FDWrProt': u02.8i ;11  
 FDWrProt': u01.12o ;27  
 FDWrProt': C244 ;28  
 Floppy16MHz: u29.11i ;09  
 Floppy16MHz: u15.11i ;09  
 Floppy16MHz: #TP71.1i, u28.2i ;10  
 Floppy16MHz: u60.5o  
 Floppy2MHz: u58.24i ;08  
 Floppy2MHz: #TP70.1i, u28.12o ;10  
 Floppy8MHz: u28.14o ;10  
 Floppy8MHz: u41.11i ;12  
 Floppy8MHz: u12.11i ;12  
 FloppyDataAck': u74.18o, #TP54.1i ;07  
 FloppyDataAck': u57.12i ;08  
 FloppyDataAck': u46.13i ;08  
 FloppyDataAck': u57.10i ;08  
 FloppyDataAck': u72.13i ;08  
 FloppyDataAck: v06.15i ;06  
 FloppyDataAck: u55.5i, u55.1i ;08  
 FloppyDataAck: u71.12i ;08  
 FloppyDataAck: u46.12o ;08  
 FloppyDataReq: u61.19i ;07  
 FloppyDataReq: u56.9o ;08  
 FloppyDCA0: u58.5i ;08  
 FloppyDCA0: u55.6o ;08

XEROX	PROPRIETARY NOTE ON COVER SHEET APPLIES TO ALL SHEETS		DWG SIZE A4	DWG NO. 156P11446	SHEET REV. B
	TITLE SCHEMATIC, IOP		SHEET 42	OF	

FloppyDCA1: u55.3o ;08  
 FloppyDCA1: u58.6i ;08  
  
 FloppyDCCS': u57.11o ;08  
 FloppyDCCS': u58.3i ;08  
  
 FloppyDiskChg: u19.2i ;08  
 FloppyDiskChg: u02.7o ;11  
  
 FloppyEndCount: u19.4i ;08  
 FloppyEndCount: u56.5o ;08  
  
 FloppyIntReq: u92.7i ;01  
 FloppyIntReq: u58.39o ;08  
 FloppyIntReq: #TP159.1i, v08.2i ;14  
 FloppyIntReq: u73.9o ;21  
  
 FloppyTwoSided: u19.6i ;08  
 FloppyTwoSided: u02.3o ;11  
  
 GND: u92.6i, u92.10i ;01  
 GND: v02.5i ;01  
 GND: v02.11i ;01  
 GND: v03.6i ;01  
 GND: v03.10i ;01  
 GND: v45.3i ;05  
 GND: u61.20i ;07  
 GND: u62.19i ;07  
 GND: u58.20i ;08  
 GND: u56.2i ;08  
 GND: u15.1i ;09  
 GND: u16.1i ;09  
 GND: u30.1i ;10  
 GND: u02.11i ;11  
 GND: u17.1i ;11  
 GND: v24.1i, v24.2i, v24.3i ;14  
 GND: v24.15i  
 GND: v27.1i, v27.19i ;15  
 GND: v28.1i ;15  
 GND: v21.12i ;16  
 GND: v36.2i ;16  
 GND: v36.12i ;16  
 GND: u86.2i ;17  
 GND: v38.15i ;17  
 GND: v27.17i ;17  
 GND: v47.5i ;17  
 GND: u35.1i ;19  
 GND: u36.1i ;19  
 GND: u35.19i ;20  
 GND: u49.1i ;20  
 GND: u20.2i ;20  
 GND: u84.4i, u84.2i ;21  
 GND: u84.1i ;21  
 GND: #J1.9i ;21  
 GND: u22.4i ;22  
 GND: u07.7i ;22  
 GND: u07.13i, u07.9i, u07.10i ;22  
 GND: u07.12i

GND: u17.19i ;22  
 GND: u38.15i ;25  
 GND: u23.1i, u23.19i ;25  
 GND: u54.15i ;26  
 GND: u40.15i ;26  
 GND: u26.15i ;26  
 GND: u11.15i ;26  
 GND: u54.10i ;26  
 GND: #R17.1i ;27  
 GND: #R27.1i, #R16.1i, #R14.1i ;27  
 GND: #R13.1i, #R11.1i, #R12.1i  
 GND: #R15.1i, #R6.1i  
 GND: s01.3i ;27  
 GND: C227, C229, C231, C247, C249 ;28  
 GND: C223  
 GND: C201, C213, C225, C237, C239 ;28  
 GND: C233, C235, C217, C215  
 GND: C211, C245, C209, C243, C241 ;28  
 GND: C221, C219  
 GND: C1 ;28  
 GND: C7 ;28  
 GND: C407, C406 ;28  
 GND: #C68.2i ;29  
 GND: #C69.2i ;29  
 GND: #C67.1i ;29  
 GND: C87, C89 ;29  
 GND: C65, C63, C61, C59, C57, C55 ;29  
 GND: C53, C51  
 GND: C73 ;29  
 GND: #C70.2i, #R19.2i, #C1.2i ;30  
 GND: #R21.2i  
 GND: #Q2.1i ;30  
 GND: #C66.2o, #C65.2o, #C64.2o ;31  
 GND: #C63.2o, #C62.2o, #C61.2o  
 GND: #C60.2o, #C59.2o, #C58.2o  
 GND: #C57.2o, #C56.2o, #C55.2o  
 GND: #C54.2o, #C53.2o, #C52.2o  
 GND: #C51.2o, #C50.2o, #C49.2o ;31  
 GND: #C48.2o, #C47.2o, #C46.2o  
 GND: #C45.2o, #C44.2o, #C43.2o  
 GND: #C42.2o, #C41.2o, #C40.2o  
 GND: #C39.2o, #C38.2o, #C37.2o  
 GND: #C36.2o  
 GND: #C35.2o, #C34.2o, #C33.2o ;31  
 GND: #C32.2o, #C31.2o, #C30.2o  
 GND: #C29.2o, #C28.2o, #C27.2o  
 GND: #C26.2o, #C25.2o, #C23.2o  
 GND: #C22.2o, #C21.2o, #C20.2o  
 GND: #C19.2o  
 GND: #C18.2o, #C16.2o, #C15.2o ;31  
 GND: #C14.2o, #C12.2o, #C11.2o  
 GND: #C10.2o, #C8.2o, #C7.2o  
 GND: #C6.2o, #C5.2o, #C4.2o  
 GND: #C3.2o, #C24.2o, #C17.2o  
 GND: #C9.2o  
  
 HdLdC: u30.14i ;10  
 HdLdC: #C2.2o, #TP114.1i ;27

XEROX	PROPRIETARY NOTE ON COVER SHEET APPLIES TO ALL SHEETS		DWG SIZE	DWG NO. 156P11446	SHEET REV.
	TITLE SCHEMATIC, IOP		A4	SHEET 43 OF	B



HdLdR: u30.15i ;10  
 HdLdR: #CR1.1o, #TP115.1i ;27  
  
 High/a: u20.10i ;07  
 High/a: u29.4i, u29.10i, u29.2i ;09  
 High/a: u43.13i, u43.1i ;09  
 High/a: u02.9o, #TP76.1i ;11  
 High/a: u41.4i, u41.10i, u41.2i ;12  
 High/a: u18.3i ;18  
 High/a: u20.1i ;20  
 High/a: u50.16i, u50.11i ;22  
 High/a: u07.4i ;22  
  
 High/b: u86.11i, u86.12i ;01  
 High/b: u58.22i ;08  
 High/b: u56.10i, u56.12i ;08  
 High/b: u28.10i, u28.9i, u28.7i ;10  
 High/b: u85.4i, u85.2i, u85.10i ;21  
 High/b: u84.18o ;21  
  
 High/c: v40.4i, v40.2i ;15  
 High/c: v21.4i ;16  
 High/c: v36.13i ;16  
 High/c: v27.3o ;17  
 High/c: u33.11i, u33.9i ;19  
 High/c: u31.11i, u31.9i ;19  
 High/c: u32.11i, u32.9i ;19  
 High/c: u34.11i, u34.9i ;19  
  
 High/d: u72.4i ;17  
 High/d: u84.16o ;21  
  
 IncMPanel': u49.6o ;20  
 IncMPanel': C401 ;28  
  
 Int.5: u92.8i ;01  
 Int.5: #TP160.1i, E64, v08.15i ;14  
  
 Int.6: E164, v08.13i ;14  
  
 Int.7: E65, v08.11i ;14  
  
 IOPAttn': v22.11o ;15  
 IOPAttn': v37.13i ;17  
 IOPAttn': v23.6i ;17  
  
 IOPAttnSync': v40.6i ;15  
 IOPAttnSync': v38.2i ;17  
 IOPAttnSync': #TP153.1i, v37.15o ;17  
  
 IOPC1k': v05.2o ;01  
 IOPC1k': u61.12i ;07  
 IOPC1k': u20.11i ;07  
 IOPC1k': u36.11i ;19  
 IOPC1k': u33.2i, u31.2i ;19  
 IOPC1k': u34.2i, u32.2i ;19  
  
 IOPC1k: v39.18o, E12 ;01  
 IOPC1k: v05.1i ;01

IOPC1k: u85.11i ;21  
 IOPC1k: u22.20i ;22  
  
 IOPCtl←': E121, v27.6i ;15  
  
 IOPCtl←: v27.14o ;15  
 IOPCtl←: v26.10i ;15  
  
 IOPOData←': E21, v27.8i ;16  
  
 IOPReq': v40.11i ;15  
 IOPReq': v37.16o, E84 ;17  
  
 IOPWait: v22.3o ;15  
 IOPWait: v28.3i ;15  
  
 IOPWaitSync: v28.5o, E189 ;15  
  
 IP6MC1k: y03.8o, v04.3i ;21  
  
 IPADData.0: v09.18o, #TP34.1i ;06  
 IPADData.0: u61.21i ;07  
 IPADData.0: u75.3i ;07  
 IPADData.0: u59.2i ;08  
 IPADData.0: #J1.17o ;21  
 IPADData.0: u22.8o ;22  
 IPADData.0: u50.1o ;22  
  
 IPADData.1: v09.17o, #TP35.1i ;06  
 IPADData.1: u61.22i ;07  
 IPADData.1: u75.4i ;07  
 IPADData.1: u59.3i ;08  
 IPADData.1: #J1.16o ;21  
 IPADData.1: u22.7o ;22  
 IPADData.1: u50.2o ;22  
  
 IPADData.2: v09.16o, #TP36.1i ;06  
 IPADData.2: u61.23i ;07  
 IPADData.2: u75.7i ;07  
 IPADData.2: u59.4i ;08  
 IPADData.2: #J1.15o ;21  
 IPADData.2: u22.6o ;22  
 IPADData.2: u50.3o ;22  
  
 IPADData.3: v09.15o, #TP37.1i ;06  
 IPADData.3: u61.26i ;07  
 IPADData.3: u75.8i ;07  
 IPADData.3: u59.5i ;08  
 IPADData.3: #J1.14o ;21  
 IPADData.3: u22.5o ;22  
 IPADData.3: u50.4o ;22  
  
 IPADData.4: v09.14o, #TP38.1i ;06  
 IPADData.4: u61.27i ;07  
 IPADData.4: u75.13i ;07  
 IPADData.4: u59.6i ;08  
 IPADData.4: #J1.13o ;21  
 IPADData.4: u22.2o ;22  
 IPADData.4: u50.5o ;22

XEROX	PROPRIETARY NOTE ON COVER SHEET APPLIES TO ALL SHEETS		DWG SIZE A4	DWG NO. 156P11446	SHEET REV. B
	TITLE SCHEMATIC, IOP			SHEET 44 OF	

IPADData.5: v09.13o, #TP39.1i ;06	IPAddr.04: u75.12o ;07
IPADData.5: u61.28i ;07	IPAddr.05: v10.15o, E172 ;01
IPADData.5: u75.14i ;07	IPAddr.05: v29.19i ;02
IPADData.5: u59.7i ;08	IPAddr.05: v30.19i ;02
IPADData.5: #J1.12o ;21	IPAddr.05: v31.19i ;02
IPADData.5: u22.1o ;22	IPAddr.05: v32.19i ;02
IPADData.5: u50.6o ;22	IPAddr.05: v46.1i ;05
	IPAddr.05: v44.1i ;05
	IPAddr.05: u75.15o ;07
IPADData.6: v09.12o, #TP40.1i ;06	
IPADData.6: u61.29i ;07	IPAddr.06: v10.16o, E73 ;01
IPADData.6: u75.17i ;07	IPAddr.06: v29.22i ;02
IPADData.6: u59.8i ;08	IPAddr.06: v30.22i ;02
IPADData.6: #J1.11o ;21	IPAddr.06: v31.22i ;02
IPADData.6: u22.28o ;22	IPAddr.06: v32.22i ;02
IPADData.6: u50.7o ;22	IPAddr.06: u70.15i ;03
	IPAddr.06: u69.15i ;03
IPADData.7: v09.11o, #TP41.1i ;06	IPAddr.06: u68.15i ;03
IPADData.7: u61.30i ;07	IPAddr.06: u99.15i ;03
IPADData.7: u75.18i ;07	IPAddr.06: v00.15i ;03
IPADData.7: u59.9i ;08	IPAddr.06: u98.15i ;03
IPADData.7: #J1.10o ;21	IPAddr.06: u67.15i ;03
IPADData.7: u22.27o ;22	IPAddr.06: v01.15i ;03
IPADData.7: u50.8o ;22	IPAddr.06: u66.15i ;04
	IPAddr.06: u65.15i ;04
IPAddr.00': v04.2o ;01	IPAddr.06: u64.15i ;04
IPAddr.00': v25.2i, v25.14i ;05	IPAddr.06: u95.15i ;04
IPAddr.00': v45.6i ;05	IPAddr.06: u96.15i ;04
	IPAddr.06: u94.15i ;04
IPAddr.00: v10.2o, E69 ;01	IPAddr.06: u63.15i ;04
IPAddr.00: v04.1i ;01	IPAddr.06: u97.15i ;04
IPAddr.00: v03.14i ;01	IPAddr.06: u75.16o ;07
IPAddr.00: v02.14i ;01	IPAddr.06: u83.15i ;23
IPAddr.00: u51.6i ;05	IPAddr.06: u82.15i ;23
IPAddr.00: v06.4i ;06	IPAddr.06: u81.15i ;23
IPAddr.00: u75.2o ;07	IPAddr.06: v16.15i ;23
	IPAddr.06: v17.15i ;23
IPAddr.01: v10.5o, E169 ;01	IPAddr.06: v15.15i ;23
IPAddr.01: v45.5i ;05	IPAddr.06: u80.15i ;23
IPAddr.01: v46.5i ;05	IPAddr.06: v18.15i ;23
IPAddr.01: v44.6i ;05	IPAddr.06: u79.15i ;24
IPAddr.01: u75.5o ;07	IPAddr.06: u78.15i ;24
	IPAddr.06: u77.15i ;24
IPAddr.02: v10.6o, E71 ;01	IPAddr.06: v12.15i ;24
IPAddr.02: v45.4i ;05	IPAddr.06: v13.15i ;24
IPAddr.02: v46.6i ;05	IPAddr.06: v11.15i ;24
IPAddr.02: v44.5i ;05	IPAddr.06: u76.15i ;24
IPAddr.02: u75.6o ;07	IPAddr.06: v14.15i ;24
IPAddr.03: v10.9o, E171 ;01	IPAddr.07: v10.19o, E173 ;01
IPAddr.03: v45.2i ;05	IPAddr.07: v29.23i ;02
IPAddr.03: v46.3i ;05	IPAddr.07: v30.23i ;02
IPAddr.03: v44.3i ;05	IPAddr.07: v31.23i ;02
IPAddr.03: u75.9o ;07	IPAddr.07: v32.23i ;02
	IPAddr.07: u70.16i ;03
IPAddr.04: v10.12o, E72 ;01	IPAddr.07: u69.16i ;03
IPAddr.04: v45.1i ;05	IPAddr.07: u68.16i ;03
IPAddr.04: v46.2i ;05	IPAddr.07: u99.16i ;03
IPAddr.04: v44.2i ;05	

IPAddr.07: v00.16i ;03  
 IPAddr.07: u98.16i ;03  
 IPAddr.07: u67.16i ;03  
 IPAddr.07: v01.16i ;03  
 IPAddr.07: u66.16i ;04  
 IPAddr.07: u65.16i ;04  
 IPAddr.07: u64.16i ;04  
 IPAddr.07: u95.16i ;04  
 IPAddr.07: u96.16i ;04  
 IPAddr.07: u94.16i ;04  
 IPAddr.07: u63.16i ;04  
 IPAddr.07: u97.16i ;04  
 IPAddr.07: u75.19o ;07  
 IPAddr.07: u83.16i ;23  
 IPAddr.07: u82.16i ;23  
 IPAddr.07: u81.16i ;23  
 IPAddr.07: v16.16i ;23  
 IPAddr.07: v17.16i ;23  
 IPAddr.07: v15.16i ;23  
 IPAddr.07: u80.16i ;23  
 IPAddr.07: v18.16i ;23  
 IPAddr.07: u79.16i ;24  
 IPAddr.07: u78.16i ;24  
 IPAddr.07: u77.16i ;24  
 IPAddr.07: v12.16i ;24  
 IPAddr.07: v13.16i ;24  
 IPAddr.07: v11.16i ;24  
 IPAddr.07: u76.16i ;24  
 IPAddr.07: v14.16i ;24

IPAddr.08: u93.2o, E74 ;01  
 IPAddr.08: v29.1i ;02  
 IPAddr.08: v30.1i ;02  
 IPAddr.08: v31.1i ;02  
 IPAddr.08: v32.1i ;02  
 IPAddr.08: u70.17i ;03  
 IPAddr.08: u69.17i ;03  
 IPAddr.08: u68.17i ;03  
 IPAddr.08: u99.17i ;03  
 IPAddr.08: v00.17i ;03  
 IPAddr.08: u98.17i ;03  
 IPAddr.08: u67.17i ;03  
 IPAddr.08: v01.17i ;03  
 IPAddr.08: u66.17i ;04  
 IPAddr.08: u65.17i ;04  
 IPAddr.08: u64.17i ;04  
 IPAddr.08: u95.17i ;04  
 IPAddr.08: u96.17i ;04  
 IPAddr.08: u94.17i ;04  
 IPAddr.08: u63.17i ;04  
 IPAddr.08: u97.17i ;04  
 IPAddr.08: u62.18o ;07  
 IPAddr.08: u83.17i ;23  
 IPAddr.08: u82.17i ;23  
 IPAddr.08: u81.17i ;23  
 IPAddr.08: v16.17i ;23  
 IPAddr.08: v17.17i ;23  
 IPAddr.08: v15.17i ;23  
 IPAddr.08: u80.17i ;23

IPAddr.08: v18.17i ;23  
 IPAddr.08: u79.17i ;24  
 IPAddr.08: u78.17i ;24  
 IPAddr.08: u77.17i ;24  
 IPAddr.08: v12.17i ;24  
 IPAddr.08: v13.17i ;24  
 IPAddr.08: v11.17i ;24  
 IPAddr.08: u76.17i ;24  
 IPAddr.08: v14.17i ;24

IPAddr.09: u93.5o, E174 ;01  
 IPAddr.09: v29.2i ;02  
 IPAddr.09: v30.2i ;02  
 IPAddr.09: v31.2i ;02  
 IPAddr.09: v32.2i ;02  
 IPAddr.09: u70.1i ;03  
 IPAddr.09: u69.1i ;03  
 IPAddr.09: u68.1i ;03  
 IPAddr.09: u99.1i ;03  
 IPAddr.09: v00.1i ;03  
 IPAddr.09: u98.1i ;03  
 IPAddr.09: u67.1i ;03  
 IPAddr.09: v01.1i ;03  
 IPAddr.09: u66.1i ;04  
 IPAddr.09: u65.1i ;04  
 IPAddr.09: u64.1i ;04  
 IPAddr.09: u95.1i ;04  
 IPAddr.09: u96.1i ;04  
 IPAddr.09: u94.1i ;04  
 IPAddr.09: u63.1i ;04  
 IPAddr.09: u97.1i ;04  
 IPAddr.09: u51.5i ;05  
 IPAddr.09: v25.15i, v25.1i ;05  
 IPAddr.09: v06.7i ;06  
 IPAddr.09: u62.17o ;07  
 IPAddr.09: u83.1i ;23  
 IPAddr.09: u82.1i ;23  
 IPAddr.09: u81.1i ;23  
 IPAddr.09: v16.1i ;23  
 IPAddr.09: v17.1i ;23  
 IPAddr.09: v15.1i ;23  
 IPAddr.09: u80.1i ;23  
 IPAddr.09: v18.1i ;23  
 IPAddr.09: u79.1i ;24  
 IPAddr.09: u78.1i ;24  
 IPAddr.09: u77.1i ;24  
 IPAddr.09: v12.1i ;24  
 IPAddr.09: v13.1i ;24  
 IPAddr.09: v11.1i ;24  
 IPAddr.09: u76.1i ;24  
 IPAddr.09: v14.1i ;24

IPAddr.10: u93.6o, E75 ;01  
 IPAddr.10: v29.3i ;02  
 IPAddr.10: v30.3i ;02  
 IPAddr.10: v31.3i ;02  
 IPAddr.10: v32.3i ;02  
 IPAddr.10: u70.2i ;03  
 IPAddr.10: u69.2i ;03

XEROX	PROPRIETARY NOTE ON COVER SHEET APPLIES TO ALL SHEETS		DWG SIZE A4	DWG NO. 156P11446	SHEET REV.
	TITLE SCHEMATIC, IOP			SHEET 46 OF	B

IPAddr.10: u68.2i ;03  
 IPAddr.10: u99.2i ;03  
 IPAddr.10: v00.2i ;03  
 IPAddr.10: u98.2i ;03  
 IPAddr.10: u67.2i ;03  
 IPAddr.10: v01.2i ;03  
 IPAddr.10: u66.2i ;04  
 IPAddr.10: u65.2i ;04  
 IPAddr.10: u64.2i ;04  
 IPAddr.10: u95.2i ;04  
 IPAddr.10: u96.2i ;04  
 IPAddr.10: u94.2i ;04  
 IPAddr.10: u63.2i ;04  
 IPAddr.10: u97.2i ;04  
 IPAddr.10: u51.4i ;05  
 IPAddr.10: v25.3i ;05  
 IPAddr.10: v06.6i ;06  
 IPAddr.10: u62.16o ;07  
 IPAddr.10: u83.2i ;23  
 IPAddr.10: u82.2i ;23  
 IPAddr.10: u81.2i ;23  
 IPAddr.10: v16.2i ;23  
 IPAddr.10: v17.2i ;23  
 IPAddr.10: v15.2i ;23  
 IPAddr.10: u80.2i ;23  
 IPAddr.10: v18.2i ;23  
 IPAddr.10: u79.2i ;24  
 IPAddr.10: u78.2i ;24  
 IPAddr.10: u77.2i ;24  
 IPAddr.10: v12.2i ;24  
 IPAddr.10: v13.2i ;24  
 IPAddr.10: v11.2i ;24  
 IPAddr.10: u76.2i ;24  
 IPAddr.10: v14.2i ;24

IPAddr.11: u93.9o, E175 ;01  
 IPAddr.11: v29.4i ;02  
 IPAddr.11: v30.4i ;02  
 IPAddr.11: v31.4i ;02  
 IPAddr.11: v32.4i ;02  
 IPAddr.11: u70.3i ;03  
 IPAddr.11: u69.3i ;03  
 IPAddr.11: u68.3i ;03  
 IPAddr.11: u99.3i ;03  
 IPAddr.11: v00.3i ;03  
 IPAddr.11: u98.3i ;03  
 IPAddr.11: u67.3i ;03  
 IPAddr.11: v01.3i ;03  
 IPAddr.11: u66.3i ;04  
 IPAddr.11: u65.3i ;04  
 IPAddr.11: u64.3i ;04  
 IPAddr.11: u95.3i ;04  
 IPAddr.11: u96.3i ;04  
 IPAddr.11: u94.3i ;04  
 IPAddr.11: u63.3i ;04  
 IPAddr.11: u97.3i ;04  
 IPAddr.11: u51.3i ;05  
 IPAddr.11: v25.13i ;05  
 IPAddr.11: v06.5i ;06

IPAddr.11: u62.15o ;07  
 IPAddr.11: u83.3i ;23  
 IPAddr.11: u82.3i ;23  
 IPAddr.11: u81.3i ;23  
 IPAddr.11: v16.3i ;23  
 IPAddr.11: v17.3i ;23  
 IPAddr.11: v15.3i ;23  
 IPAddr.11: u80.3i ;23  
 IPAddr.11: v18.3i ;23  
 IPAddr.11: u79.3i ;24  
 IPAddr.11: u78.3i ;24  
 IPAddr.11: u77.3i ;24  
 IPAddr.11: v12.3i ;24  
 IPAddr.11: v13.3i ;24  
 IPAddr.11: v11.3i ;24  
 IPAddr.11: u76.3i ;24  
 IPAddr.11: v14.3i ;24  
 IPAddr.12: u93.12o, E76 ;01  
 IPAddr.12: v29.5i ;02  
 IPAddr.12: v30.5i ;02  
 IPAddr.12: v31.5i ;02  
 IPAddr.12: v32.5i ;02  
 IPAddr.12: u70.4i ;03  
 IPAddr.12: u69.4i ;03  
 IPAddr.12: u68.4i ;03  
 IPAddr.12: u99.4i ;03  
 IPAddr.12: v00.4i ;03  
 IPAddr.12: u98.4i ;03  
 IPAddr.12: u67.4i ;03  
 IPAddr.12: v01.4i ;03  
 IPAddr.12: u66.4i ;04  
 IPAddr.12: u65.4i ;04  
 IPAddr.12: u64.4i ;04  
 IPAddr.12: u95.4i ;04  
 IPAddr.12: u96.4i ;04  
 IPAddr.12: u94.4i ;04  
 IPAddr.12: u63.4i ;04  
 IPAddr.12: u97.4i ;04  
 IPAddr.12: u45.6i, u44.6i ;05  
 IPAddr.12: u51.2i ;05  
 IPAddr.12: u62.14o ;07  
 IPAddr.12: v24.4i ;14  
 IPAddr.12: v34.6i ;15  
 IPAddr.12: u83.4i ;23  
 IPAddr.12: u82.4i ;23  
 IPAddr.12: u81.4i ;23  
 IPAddr.12: v16.4i ;23  
 IPAddr.12: v17.4i ;23  
 IPAddr.12: v15.4i ;23  
 IPAddr.12: u80.4i ;23  
 IPAddr.12: v18.4i ;23  
 IPAddr.12: u79.4i ;24  
 IPAddr.12: u78.4i ;24  
 IPAddr.12: u77.4i ;24  
 IPAddr.12: v12.4i ;24  
 IPAddr.12: v13.4i ;24  
 IPAddr.12: v11.4i ;24  
 IPAddr.12: u76.4i ;24

IPAddr.12: v14.4i ;24  
 IPAddr.13: u93.15o, E176 ;01  
 IPAddr.13: v29.6i ;02  
 IPAddr.13: v30.6i ;02  
 IPAddr.13: v31.6i ;02  
 IPAddr.13: v32.6i ;02  
 IPAddr.13: u70.7i ;03  
 IPAddr.13: u69.7i ;03  
 IPAddr.13: u68.7i ;03  
 IPAddr.13: u99.7i ;03  
 IPAddr.13: v00.7i ;03  
 IPAddr.13: u98.7i ;03  
 IPAddr.13: u67.7i ;03  
 IPAddr.13: v01.7i ;03  
 IPAddr.13: u66.7i ;04  
 IPAddr.13: u65.7i ;04  
 IPAddr.13: u64.7i ;04  
 IPAddr.13: u95.7i ;04  
 IPAddr.13: u96.7i ;04  
 IPAddr.13: u94.7i ;04  
 IPAddr.13: u63.7i ;04  
 IPAddr.13: u97.7i ;04  
 IPAddr.13: u45.3i ;05  
 IPAddr.13: u44.3i ;05  
 IPAddr.13: u51.1i ;05  
 IPAddr.13: u62.13o ;07  
 IPAddr.13: v24.7i ;14  
 IPAddr.13: v34.3i ;15  
 IPAddr.13: u83.7i ;23  
 IPAddr.13: u82.7i ;23  
 IPAddr.13: u81.7i ;23  
 IPAddr.13: v16.7i ;23  
 IPAddr.13: v17.7i ;23  
 IPAddr.13: v15.7i ;23  
 IPAddr.13: u80.7i ;23  
 IPAddr.13: v18.7i ;23  
 IPAddr.13: u79.7i ;24  
 IPAddr.13: u78.7i ;24  
 IPAddr.13: u77.7i ;24  
 IPAddr.13: v12.7i ;24  
 IPAddr.13: v13.7i ;24  
 IPAddr.13: v11.7i ;24  
 IPAddr.13: u76.7i ;24  
 IPAddr.13: v14.7i ;24  
 IPAddr.14: u93.16o, E77 ;01  
 IPAddr.14: v29.7i ;02  
 IPAddr.14: v30.7i ;02  
 IPAddr.14: v31.7i ;02  
 IPAddr.14: v32.7i ;02  
 IPAddr.14: u70.6i ;03  
 IPAddr.14: u69.6i ;03  
 IPAddr.14: u68.6i ;03  
 IPAddr.14: u99.6i ;03  
 IPAddr.14: v00.6i ;03  
 IPAddr.14: u98.6i ;03  
 IPAddr.14: u67.6i ;03  
 IPAddr.14: v01.6i ;03

IPAddr.14: u66.6i ;04  
 IPAddr.14: u65.6i ;04  
 IPAddr.14: u64.6i ;04  
 IPAddr.14: u95.6i ;04  
 IPAddr.14: u96.6i ;04  
 IPAddr.14: u94.6i ;04  
 IPAddr.14: u63.6i ;04  
 IPAddr.14: u97.6i ;04  
 IPAddr.14: u45.2i ;05  
 IPAddr.14: u44.2i ;05  
 IPAddr.14: u62.12o ;07  
 IPAddr.14: u55.2i ;08  
 IPAddr.14: v24.6i ;14  
 IPAddr.14: v34.2i ;15  
 IPAddr.14: #J1.5i ;21  
 IPAddr.14: u50.20i ;22  
 IPAddr.14: u83.6i ;23  
 IPAddr.14: u82.6i ;23  
 IPAddr.14: u81.6i ;23  
 IPAddr.14: v16.6i ;23  
 IPAddr.14: v17.6i ;23  
 IPAddr.14: v15.6i ;23  
 IPAddr.14: u80.6i ;23  
 IPAddr.14: v18.6i ;23  
 IPAddr.14: u79.6i ;24  
 IPAddr.14: u78.6i ;24  
 IPAddr.14: u77.6i ;24  
 IPAddr.14: v12.6i ;24  
 IPAddr.14: v13.6i ;24  
 IPAddr.14: v11.6i ;24  
 IPAddr.14: u76.6i ;24  
 IPAddr.14: v14.6i ;24  
 IPAddr.15: u93.19o, E177 ;01  
 IPAddr.15: v29.8i ;02  
 IPAddr.15: v30.8i ;02  
 IPAddr.15: v31.8i ;02  
 IPAddr.15: v32.8i ;02  
 IPAddr.15: u70.5i ;03  
 IPAddr.15: u69.5i ;03  
 IPAddr.15: u68.5i ;03  
 IPAddr.15: u99.5i ;03  
 IPAddr.15: v00.5i ;03  
 IPAddr.15: u98.5i ;03  
 IPAddr.15: u67.5i ;03  
 IPAddr.15: v01.5i ;03  
 IPAddr.15: u66.5i ;04  
 IPAddr.15: u65.5i ;04  
 IPAddr.15: u64.5i ;04  
 IPAddr.15: u95.5i ;04  
 IPAddr.15: u96.5i ;04  
 IPAddr.15: u94.5i ;04  
 IPAddr.15: u63.5i ;04  
 IPAddr.15: u97.5i ;04  
 IPAddr.15: u45.1i ;05  
 IPAddr.15: u44.1i ;05  
 IPAddr.15: u62.11o ;07  
 IPAddr.15: u55.4i ;08  
 IPAddr.15: v24.5i ;14

XEROX	PROPRIETARY NOTE ON COVER SHEET APPLIES TO ALL SHEETS		DWG SIZE A4	DWG NO. 156P11446	SHEET REV.
	TITLE SCHEMATIC, IOP			SHEET 48 OF	B

IPAddr.15: v34.1i ;15  
 IPAddr.15: #J1.4i ;21  
 IPAddr.15: u50.19i ;22  
 IPAddr.15: u22.12i ;22  
 IPAddr.15: u83.5i ;23  
 IPAddr.15: u82.5i ;23  
 IPAddr.15: u81.5i ;23  
 IPAddr.15: v16.5i ;23  
 IPAddr.15: v17.5i ;23  
 IPAddr.15: v15.5i ;23  
 IPAddr.15: u80.5i ;23  
 IPAddr.15: v18.5i ;23  
 IPAddr.15: u79.5i ;24  
 IPAddr.15: u78.5i ;24  
 IPAddr.15: u77.5i ;24  
 IPAddr.15: v12.5i ;24  
 IPAddr.15: v13.5i ;24  
 IPAddr.15: v11.5i ;24  
 IPAddr.15: u76.5i ;24  
 IPAddr.15: v14.5i ;24

IPALE: v10.11i ;01  
 IPALE: u93.11i ;01  
 IPALE: v39.14o, E36 ;01  
 IPALE: u85.3i ;21

IPALECycle': u85.8o, u85.1i ;21  
 IPALECycle: u85.9o, u71.4i ;21

IPClockX1: u92.1i ;01  
 IPClockX1: v04.5i, v04.4o ;21  
 IPClockX1: #R4.1i ;27

IPClockX2: u92.2i ;01  
 IPClockX2: v04.6o ;21  
 IPClockX2: #R3.1i ;27

IPData.00: u91.18o, E92 ;06  
 IPData.00: u89.3i ;08  
 IPData.00: u19.18o ;08  
 IPData.00: u90.2o ;13  
 IPData.00: u90.3i ;13  
 IPData.00: v08.18o ;14  
 IPData.00: v22.4i ;15  
 IPData.00: v42.2o ;16  
 IPData.00: v43.3i ;16  
 IPData.00: v23.18o ;17  
 IPData.00: u03.2o ;18  
 IPData.00: u31.13o ;19  
 IPData.00: u32.13o ;19  
 IPData.00: u49.3i ;20  
 IPData.00: u48.18o ;20  
 IPData.00: u47.3i ;20

IPData.01: u91.17o, E192 ;06  
 IPData.01: u89.4i ;08  
 IPData.01: u19.16o ;08  
 IPData.01: u90.5o ;13

IPData.01: u90.4i ;13  
 IPData.01: v08.16o ;14  
 IPData.01: v22.5i ;15  
 IPData.01: v42.5o ;16  
 IPData.01: v43.4i ;16  
 IPData.01: v23.16o ;17  
 IPData.01: u03.5o ;18  
 IPData.01: u31.14o ;19  
 IPData.01: u32.14o ;19  
 IPData.01: u49.4i ;20  
 IPData.01: u48.16o ;20  
 IPData.01: u47.4i ;20

IPData.02: u91.16o, E93 ;06  
 IPData.02: u89.7i ;08  
 IPData.02: u19.14o ;08  
 IPData.02: u90.6o ;13  
 IPData.02: u90.7i ;13  
 IPData.02: v08.14o ;14  
 IPData.02: v22.12i ;15  
 IPData.02: v42.6o ;16  
 IPData.02: v43.7i ;16  
 IPData.02: v23.14o ;17  
 IPData.02: u03.6o ;18  
 IPData.02: u31.15o ;19  
 IPData.02: u32.15o ;19  
 IPData.02: u47.7i ;20  
 IPData.02: u49.7i ;20  
 IPData.02: u48.3o ;20

IPData.03: u91.15o, E193 ;06  
 IPData.03: u89.8i ;08  
 IPData.03: u19.12o ;08  
 IPData.03: u90.9o ;13  
 IPData.03: u90.8i ;13  
 IPData.03: v08.12o ;14  
 IPData.03: v22.13i ;15  
 IPData.03: v42.9o ;16  
 IPData.03: v43.8i ;16  
 IPData.03: v23.12o ;17  
 IPData.03: u03.9o ;18  
 IPData.03: u31.16o ;19  
 IPData.03: u32.16o ;19  
 IPData.03: u47.8i ;20  
 IPData.03: u49.8i ;20  
 IPData.03: u48.5o ;20

IPData.04: u91.14o, E94 ;06  
 IPData.04: u89.13i ;08  
 IPData.04: u90.12o ;13  
 IPData.04: u90.13i ;13  
 IPData.04: v08.3o ;14  
 IPData.04: v24.9o ;14  
 IPData.04: u88.4i ;15  
 IPData.04: v42.12o ;16  
 IPData.04: v43.13i ;16  
 IPData.04: v23.3o ;17  
 IPData.04: u03.12o ;18  
 IPData.04: u33.13o ;19

IPData.04: u34.13o ;19  
 IPData.04: u47.13i ;20  
 IPData.04: u49.13i ;20  
 IPData.04: u48.7o ;20  
  
 IPData.05: u91.13o, E194 ;06  
 IPData.05: u89.14i ;08  
 IPData.05: u90.15o ;13  
 IPData.05: u90.14i ;13  
 IPData.05: v24.10o ;14  
 IPData.05: v08.5o ;14  
 IPData.05: u88.5i ;15  
 IPData.05: v42.15o ;16  
 IPData.05: v43.14i ;16  
 IPData.05: v23.5o ;17  
 IPData.05: u03.15o ;18  
 IPData.05: u33.14o ;19  
 IPData.05: u34.14o ;19  
 IPData.05: u47.14i ;20  
 IPData.05: u49.14i ;20  
 IPData.05: u48.14o ;20  
  
 IPData.06: u91.12o, E95 ;06  
 IPData.06: u89.17i ;08  
 IPData.06: u90.16o ;13  
 IPData.06: u90.17i ;13  
 IPData.06: v24.11o ;14  
 IPData.06: v08.7o ;14  
 IPData.06: u88.12i ;15  
 IPData.06: v42.16o ;16  
 IPData.06: v43.17i ;16  
 IPData.06: v23.7o ;17  
 IPData.06: u03.16o ;18  
 IPData.06: u33.15o ;19  
 IPData.06: u34.15o ;19  
 IPData.06: u47.17i ;20  
 IPData.06: u49.17i ;20  
 IPData.06: u48.12o ;20  
  
 IPData.07: u91.11o, E195 ;06  
 IPData.07: u89.18i ;08  
 IPData.07: u90.19o ;13  
 IPData.07: u90.18i ;13  
 IPData.07: v24.12o ;14  
 IPData.07: v08.9o ;14  
 IPData.07: u88.13i ;15  
 IPData.07: v42.19o ;16  
 IPData.07: v43.18i ;16  
 IPData.07: v23.9o ;17  
 IPData.07: u03.19o ;18  
 IPData.07: u33.16o ;19  
 IPData.07: u34.16o ;19  
 IPData.07: u47.18i ;20  
 IPData.07: u49.18i ;20  
 IPData.07: u48.9o ;20  
  
 IPDataOut: u86.9o, E15 ;01  
 IPDataOut: u91.1i ;06  
 IPDataOut: v09.1i ;06

IPDataOut: u59.1i ;08  
  
 IPIORd': v03.7o, E179 ;01  
 IPIORd': u86.13i ;01  
 IPIORd': u45.4i ;05  
 IPIORd': v19.4i ;06  
 IPIORd': u19.17i ;07  
 IPIORd': u58.4i ;08  
 IPIORd': u87.5i ;13  
 IPIORd': v24.13i ;14  
 IPIORd': v33.1i ;15  
 IPIORd': u87.9i ;16  
 IPIORd': #J1.1i ;21  
 IPIORd': u22.13i ;22  
 IPIORd': u06.4i ;22  
  
 IPIORdWr: v19.6o, v19.10i, v19.13i ;06  
 IPIORdWr: u20.12i ;07  
 IPIORdWr: u72.10i ;08  
  
 IPIOWr': v03.9o, E187 ;01  
 IPIOWr': u86.10i ;01  
 IPIOWr': u44.4i ;05  
 IPIOWr': v19.5i ;06  
 IPIOWr': u19.15i ;07  
 IPIOWr': u58.2i ;08  
 IPIOWr': u87.2i ;13  
 IPIOWr': v34.4i ;15  
 IPIOWr': u87.12i ;16  
 IPIOWr': #J1.2i ;21  
 IPIOWr': u22.10i ;22  
 IPIOWr': u06.9i ;22  
  
 IPMemRd': v07.1i ;01  
 IPMemRd': v02.7o, E79 ;01  
 IPMemRd': v29.20i ;02  
 IPMemRd': v30.20i ;02  
 IPMemRd': v31.20i ;02  
 IPMemRd': v32.20i ;02  
  
 IPMemRdWr': v07.3o ;01  
 IPMemRdWr': v46.4i ;05  
 IPMemRdWr': v44.4i ;05  
  
 IPMemWr': v07.2i ;01  
 IPMemWr': v02.9o, E87 ;01  
 IPMemWr': u70.10i ;03  
 IPMemWr': u69.10i ;03  
 IPMemWr': u68.10i ;03  
 IPMemWr': u99.10i ;03  
 IPMemWr': v00.10i ;03  
 IPMemWr': u98.10i ;03  
 IPMemWr': u67.10i ;03  
 IPMemWr': v01.10i ;03  
 IPMemWr': u66.10i ;04  
 IPMemWr': u65.10i ;04  
 IPMemWr': u64.10i ;04  
 IPMemWr': u95.10i ;04  
 IPMemWr': u96.10i ;04

XEROX	PROPRIETARY NOTE ON COVER SHEET APPLIES TO ALL SHEETS		DWG SIZE A4	DWG NO. 156P11446	SHEET REV.
	TITLE SCHEMATIC, IOP			SHEET 50 OF	B

IPMemWr' : u94.10i ;04  
 IPMemWr' : u63.10i ;04  
 IPMemWr' : u97.10i ;04  
 IPMemWr' : u83.10i ;23  
 IPMemWr' : u82.10i ;23  
 IPMemWr' : u81.10i ;23  
 IPMemWr' : v16.10i ;23  
 IPMemWr' : v17.10i ;23  
 IPMemWr' : v15.10i ;23  
 IPMemWr' : u80.10i ;23  
 IPMemWr' : v18.10i ;23  
 IPMemWr' : u79.10i ;24  
 IPMemWr' : u78.10i ;24  
 IPMemWr' : u77.10i ;24  
 IPMemWr' : v12.10i ;24  
 IPMemWr' : v13.10i ;24  
 IPMemWr' : v11.10i ;24  
 IPMemWr' : u76.10i ;24  
 IPMemWr' : v14.10i ;24  
  
 IPReady: u71.6o, v39.15i ;21  
 IPReady: #TP155.1i  
  
 IPReset' : u84.14o ;01  
 IPReset' : u89.1i ;08  
 IPReset' : u56.1i ;08  
 IPReset' : u57.9i ;08  
 IPReset' : v22.1i ;15  
 IPReset' : u88.1i ;15  
 IPReset' : v41.1i ;15  
 IPReset' : v36.1i ;16  
 IPReset' : v35.12i ;16  
 IPReset' : v35.9i ;16  
 IPReset' : v21.13i ;16  
 IPReset' : u86.1i ;17  
 IPReset' : u47.1i ;20  
  
 IPReset: #TP12.1i, v39.16o ;01  
 IPReset: v27.2i, u84.6i ;01  
 IPReset: u61.13i ;07  
 IPReset: #J1.6i ;21  
 IPReset: u22.21i ;22  
  
 IPResetBP' : v27.18o, E117 ;01  
  
 KBBC: u17.8i, u17.3o, #TP105.1i ;22  
  
 KBBC1k: u17.12o ;22  
 KBBC1k: #R7.1i ;27  
  
 KBBellC1k: u50.13o ;22  
 KBBellC1k: u17.17i ;22  
  
 KBData.0' : u04.1i ;18  
 KBData.0' : u03.18i ;18  
 KBData.0' : C52 ;29  
  
 KBData.1' : u04.2i ;18  
 KBData.1' : u03.17i ;18  
  
 KBData.1' : C54 ;29  
 KBData.2' : u04.3i ;18  
 KBData.2' : u03.14i ;18  
 KBData.2' : C56 ;29  
  
 KBData.3' : u04.4i ;18  
 KBData.3' : u03.13i ;18  
 KBData.3' : C58 ;29  
  
 KBData.4' : u04.5i ;18  
 KBData.4' : u03.8i ;18  
 KBData.4' : C60 ;29  
  
 KBData.5' : u04.6i ;18  
 KBData.5' : u03.7i ;18  
 KBData.5' : C62 ;29  
  
 KBData.6' : u04.7i ;18  
 KBData.6' : u03.4i ;18  
 KBData.6' : C64 ;29  
  
 KBData.7' : u04.9o ;18  
 KBData.7' : u03.3i ;18  
 KBData.7' : C66 ;29  
  
 KBDiag' : u05.15i, u05.14o ;18  
 KBDiag' : C70 ;29  
  
 KBDiagRet: u05.12i, u05.13o ;18  
 KBDiagRet: C69 ;29  
  
 KBIntr: v08.4i ;14  
 KBIntr: #TP91.1i, u18.2o, u18.1o ;18  
  
 KBReq' : u18.5i ;18  
 KBReq' : C68 ;29  
  
 KBReqRet: u18.7i ;18  
 KBReqRet: #C13.2o ;27  
 KBReqRet: C67 ;29  
  
 KBReqTerm: u18.6i ;18  
 KBReqTerm: #C13.1i ;27  
  
 KBSe1' : u05.4i, u05.3o ;18  
 KBSe1' : C72 ;29  
  
 KBSe1Ret: u05.1i, u05.2o ;18  
 KBSe1Ret: C71 ;29  
  
 KBTone' : #R9.2o ;27  
 KBTone' : C74 ;29  
  
 Late: #TP68.1i, u60.3o, u43.12i ;09  
  
 LateHold: u43.9o ;09  
 LateHold: u16.14i ;09

XEROX	PROPRIETARY NOTE ON COVER SHEET APPLIES TO ALL SHEETS		DWG SIZE A4	DWG NO. 156P11446	SHEET REV. B
	TITLE SCHEMATIC, IOP		SHEET 51	OF	



MiscInt: E165, v08.17i ;14  
MiscInt: u50.17o ;22  
  
MouseSw1': u48.6i ;20  
MouseSw1': C77 ;29  
  
MouseSw2': u48.11i ;20  
MouseSw2': u73.13o ;21  
MouseSw2': C76 ;29  
  
MouseSw3': u48.8i ;20  
MouseSw3': C75 ;29  
  
MouseXA': u35.2i ;19  
MouseXA': C82 ;29  
  
MouseXB': u35.4i ;19  
MouseXB': C80 ;29  
  
MouseYA': u35.6i ;19  
MouseYA': C79 ;29  
  
MouseYB': u35.8i ;19  
MouseYB': C78 ;29  
  
MVcc: u53.14i, u10.14i, u39.14i ;26  
MVcc: u23.20i, u38.16i, u09.14i  
MVcc: u40.16i, u26.16i, u54.16i  
MVcc: u11.16i, u24.20i, u25.14i  
MVcc: u52.16i ;26  
MVcc: C414 ;28  
MVCC: #C9.1i, #C17.1i, #C24.1i ;31  
  
MXA: u35.18o, u36.3i ;19  
  
MXB: u35.16o, u36.4i ;19  
  
MXCarry': u33.19o, u31.12i ;19  
  
MYA: u35.14o, u36.7i ;19  
  
MYB: u35.12o, u36.8i ;19  
  
MYCarry': u34.19o, u32.12i ;19  
  
newXA: u36.2o, u36.13i ;19  
newXA: u21.2i ;19  
  
newXB: u36.5o, u36.14i ;19  
newXB: u21.3i ;19  
  
newYA: u36.6o, u36.17i ;19  
newYA: u21.6i ;19  
  
newYB: u36.9o, u36.18i ;19  
newYB: u21.5i ;19  
  
oldXA: u36.12o ;19  
oldXA: u21.15i ;19  
  
oldXA: #TP92.1i ;19  
  
oldXB: u36.15o ;19  
oldXB: u21.1i ;19  
oldXB: #TP93.1i ;19  
  
oldYA: u36.16o ;19  
oldYA: u21.4i ;19  
oldYA: #TP94.1i ;19  
  
oldYB: u36.19o ;19  
oldYB: u21.7i ;19  
oldYB: #TP95.1i ;19  
  
Osc16MHz: y02.8o, u60.15i ;10  
  
pALECy: u85.5o, u85.12i ;21  
  
pClk': v26.1i, v47.4o, v26.2i ;17  
  
pCPInRd: u87.8o, v35.2i ;16  
  
pCPInWr: v27.12o, v26.13i ;16  
  
pCPOutWr: u87.11o, v35.5i ;16  
  
pFDCHdLd: u57.4i, u57.5i ;08  
pFDCHdLd: u58.28o, #TP60.1i ;08  
  
pInWS: v37.4i, v37.2o ;17  
  
pIOPAttnS': v37.14i, v37.12o ;17  
  
pIOPReq': #TP88.1i, v38.9o ;17  
pIOPReq': v37.17i  
  
pIOPWS: v28.2o, v28.4i ;15  
  
pOutWS: v37.8i, v37.6o ;17  
  
PowerFailed': u24.4i ;26  
PowerFailed': C404 ;28  
  
PowerFailedB': u48.17i ;20  
PowerFailedB': u24.16o ;26  
  
ppClk: E9, v47.2i, v47.6i ;17  
ppClk: #R18.1i, #R17.2o, #TP116.1i ;27  
  
ppWDLate: u15.14i, u15.12o ;09  
  
PrinterClk: u50.10o, #TP104.1i ;22  
PrinterClk: u22.25i, u22.9i ;22  
  
PrinterRxRdy: v08.8i ;14  
PrinterRxRdy: u22.14o ;22  
  
PrinterTxRdy: v08.6i ;14  
PrinterTxRdy: u22.15o ;22

XEROX	PROPRIETARY NOTE ON COVER SHEET APPLIES TO ALL SHEETS		DWG SIZE A4	DWG NO. 156P11446	SHEET REV. B
	TITLE SCHEMATIC, IOP		SHEET 52	OF	

PTCCTS': u08.8o, u22.17i ;22  
 PTCDSR': u08.6o, u22.22i ;22  
 PTCRTS': u22.23o, u07.5i ;22  
 PTCRxD: u08.3o, u22.3i ;22  
 PTCTS: u07.6o ;22  
 PTCTS: C5 ;28  
 PTCTxD: u22.19o, u07.2i ;22  
 PTDSR: u07.8o ;22  
 PTDSR: C6 ;28  
 PTDTR: u08.4i ;22  
 PTDTR: C10 ;28  
 PTRTS: u08.10i ;22  
 PTRTS: C4 ;28  
 PTRxDData: u07.3o ;22  
 PTRxDData: C3 ;28  
 PTTxDData: u08.1i ;22  
 PTTxDData: C2 ;28  
 PU1: #TP28.1i, v04.9i ;01  
 PU1: u73.15o ;21  
 PU2: u27.9i, #TP83.1i, u27.8i ;12  
 PU2: u73.14o ;21  
 pWaitClk': v26.9i ;15  
 pWaitClk': v26.12i ;16  
 pWaitClk': v47.1o ;17  
 pWDLate: u15.17i, u15.15o ;09  
 pWDLClr: u15.7i, u15.5o ;09  
 ReadClk': u49.9o ;20  
 ReadClk': u09.5i, u09.4i ;25  
 ReadClk': u52.8i ;26  
 ReadCPIn': u45.12o ;05  
 ReadCPIn': v35.1i ;16  
 ReadCPStatus': u45.11o ;05  
 ReadCPStatus': v23.1i, v23.19i ;17  
 ReadCSEn': v33.3o, E188 ;15  
 ReadFlopStat': u45.15o ;05

ReadFlopStat': u56.3i ;08  
 ReadFlopStat': u19.1i ;08  
 ReadIntReq': u45.14o ;05  
 ReadIntReq': v08.1i, v08.19i ;14  
 ReadKB': u05.6i, u05.7i, u05.5i ;18  
 ReadKB': u03.11i ;18  
 ReadKB': u17.5o ;20  
 ReadKB: u47.5o, u17.15i ;20  
 ReadKBData': u45.13o, #TP32.1i ;05  
 ReadKBData': u03.1i ;18  
 ReadMisc1': u45.7o ;05  
 ReadMisc1': u48.1i, u48.19i ;20  
 ReadMouseX': u45.10o ;05  
 ReadMouseX': u33.17i, u31.17i ;19  
 ReadMouseY': u45.9o ;05  
 ReadMouseY': u34.17i, u32.17i ;19  
 ReadT: u23.5o ;25  
 ReadT: u54.1i ;26  
 ReadT: u40.1i ;26  
 ReadT: u26.1i ;26  
 ReadT: u11.1i ;26  
 ReadTime': u35.5o ;20  
 ReadTime': u23.15i ;25  
 ReadTime': u52.4i ;26  
 ReadTime: u47.15o, u35.15i ;20  
 SecondsClkA: u38.4o, u53.1i ;25  
 SecondsClkB: u38.7o, u39.1i ;25  
 SecondsClkC: u38.9o, u25.1i ;25  
 SecondsClkD: u38.12o, u10.1i ;25  
 SelAltoPPI': u51.15o, #TP29.1i ;05  
 SelAltoPPI': #J1.3i ;21  
 SelBank1a': u51.11o, E66 ;05  
 SelBank1b': u51.10o, E166 ;05  
 SelBank1c': u51.9o, E67 ;05  
 SelBank1d': u51.7o, E167 ;05  
 SelBank4': v25.7o, E68 ;05  
 SelBank5': v25.6o, E168 ;05  
 SelBank5': u87.1i, u87.4i ;13

XEROX	PROPRIETARY NOTE ON COVER SHEET APPLIES TO ALL SHEETS		DWG SIZE A4	DWG NO. 156P11446	SHEET REV. B
	TITLE SCHEMATIC, IOP		SHEET 53	OF	

SelControlSt': v25.4o ;05  
SelControlSt': v34.5i ;15  
SelControlSt': v33.2i ;15  
  
SelDma': v25.11o ;05  
SelDma': u61.11i ;07  
  
SelDoubleDen: u89.12o ;08  
SelDoubleDen: u71.1i, u71.2i ;12  
  
SelFDSide0': u89.6o ;08  
SelFDSide0': u17.6i ;11  
  
SelFloppyDC': u51.14o ;05  
SelFloppyDC': u57.13i ;08  
SelFloppyDC': u72.12i ;08  
  
SelFloppyDrive: u89.19o ;08  
SelFloppyDrive: u17.4i, u17.2i ;11  
  
SelHostAd': v25.12o, #TP120.1i ;05  
SelHostAd': v24.14i ;14  
  
SelPrinter': u51.13o, #TP30.1i ;05  
SelPrinter': u22.11i ;22  
  
SelPROMBank0': v29.18i ;02  
SelPROMBank0': v45.15o, #TP121.1i ;05  
  
SelPROMBank1': v30.18i ;02  
SelPROMBank1': v45.14o, #TP122.1i ;05  
  
SelPROMBank2': v31.18i ;02  
SelPROMBank2': v45.13o, #TP123.1i ;05  
  
SelPROMBank3': v32.18i ;02  
SelPROMBank3': v45.12o, #TP124.1i ;05  
  
SelRAMBank0': v01.8i ;03  
SelRAMBank0': u70.8i ;03  
SelRAMBank0': v46.15o, #TP125.1i ;05  
  
SelRAMBank1': u69.8i ;03  
SelRAMBank1': v00.8i ;03  
SelRAMBank1': v46.14o, #TP126.1i ;05  
  
SelRAMBank10': v44.13o, #TP135.1i ;05  
SelRAMBank10': u81.8i ;23  
SelRAMBank10': v16.8i ;23  
  
SelRAMBank11': v44.12o, #TP136.1i ;05  
SelRAMBank11': u80.8i ;23  
SelRAMBank11': v15.8i ;23  
  
SelRAMBank12': v44.11o, #TP137.1i ;05  
SelRAMBank12': u79.8i ;24  
SelRAMBank12': v14.8i ;24  
  
SelRAMBank13': v44.10o, #TP138.1i ;05  
SelRAMBank13': u78.8i ;24  
SelRAMBank13': v13.8i ;24  
  
SelRAMBank14': v44.9o, #TP139.1i ;05  
SelRAMBank14': u77.8i ;24  
SelRAMBank14': v12.8i ;24  
  
SelRAMBank15': v44.7o, #TP140.1i ;05  
SelRAMBank15': u76.8i ;24  
SelRAMBank15': v11.8i ;24  
  
SelRAMBank2': u68.8i ;03  
SelRAMBank2': u99.8i ;03  
SelRAMBank2': v46.13o, #TP127.1i ;05  
  
SelRAMBank3': u67.8i ;03  
SelRAMBank3': u98.8i ;03  
SelRAMBank3': v46.12o, #TP128.1i ;05  
  
SelRAMBank4': u66.8i ;04  
SelRAMBank4': u97.8i ;04  
SelRAMBank4': v46.11o, #TP129.1i ;05  
  
SelRAMBank5': u65.8i ;04  
SelRAMBank5': u96.8i ;04  
SelRAMBank5': v46.10o, #TP130.1i ;05  
  
SelRAMBank6': u64.8i ;04  
SelRAMBank6': u95.8i ;04  
SelRAMBank6': v46.9o, #TP131.1i ;05  
  
SelRAMBank7': u63.8i ;04  
SelRAMBank7': u94.8i ;04  
SelRAMBank7': v46.7o, #TP132.1i ;05  
  
SelRAMBank8': v44.15o, #TP133.1i ;05  
SelRAMBank8': u83.8i ;23  
SelRAMBank8': v18.8i ;23  
  
SelRAMBank9': v44.14o, #TP134.1i ;05  
SelRAMBank9': u82.8i ;23  
SelRAMBank9': v17.8i ;23  
  
SelRegBlk': u45.5i, u44.5i ;05  
SelRegBlk': v25.5o ;05  
  
SelTimer': u51.12o, #TP31.1i ;05  
SelTimer': u06.5i, u06.10i ;22  
SelTimer': u50.21i  
  
SelTroyMode: u89.9o ;08  
SelTroyMode: u37.19i, u37.1i, E16 ;11  
  
SepState.0: u13.12o, #TP80.1i ;12  
SepState.0: u12.4i ;12  
  
SepState.1: u13.13o, #TP81.1i ;12  
SepState.1: u12.7i ;12

XEROX	PROPRIETARY NOTE ON COVER SHEET APPLIES TO ALL SHEETS		DWG SIZE A4	DWG NO. 156P11446	SHEET REV. B
	TITLE SCHEMATIC, IOP		SHEET 54	OF	

SepState.2: u13.14o, #TP82.1i ;12  
 SepState.2: u12.8i ;12  
 SepState.3: u14.11o, #TP84.1i ;12  
 SepState.3: u12.13i ;12  
 SepState.4: u14.12o, #TP85.1i ;12  
 SepState.4: u12.14i ;12  
 SepState.5: u14.13o, #TP86.1i ;12  
 SepState.5: u12.17i ;12  
 SepState.6: u14.14o, #TP87.1i ;12  
 SepState.6: u12.18i ;12  
 SetClkA': u49.12o ;20  
 SetClkA': u23.2i ;25  
 SetClkA': u52.1i ;26  
 SetClkA: u38.2i ;25  
 SetClkA: u23.18o ;25  
 SetClkB': u49.15o ;20  
 SetClkB': u23.4i ;25  
 SetClkB': u52.3i ;26  
 SetClkB: u38.5i ;25  
 SetClkB: u23.16o ;25  
 SetClkC': u49.16o ;20  
 SetClkC': u23.6i ;25  
 SetClkC': u52.5i ;26  
 SetClkC: u38.11i ;25  
 SetClkC: u23.14o ;25  
 SetClkD': u49.19o ;20  
 SetClkD': u23.8i ;25  
 SetClkD': u52.7i ;26  
 SetClkD: u38.14i ;25  
 SetClkD: u23.12o ;25  
 SetDmaC': u72.3o, u86.4i ;17  
 SetFECnt': u71.11o, u56.4i ;08  
 SetT': u23.13i, u09.8o ;25  
 SetT': u38.1i ;25  
 SetT': C405 ;28  
 SetT: u23.7o ;25  
 SetT: u09.2i ;25  
 SetTime': u35.9o ;20  
 SetTime': u09.10i, u09.9i ;25  
 SetTime': u52.2i ;26  
 SetTime: u47.19o, u35.11i ;20  
 SwTAddr': v22.7o ;15  
 SwTAddr': v28.8i ;15  
 SwTAddr: v22.6o ;15  
 SwTAddr: v28.7i ;15  
 SwTAddrSync': v28.9o, E196 ;15  
 SwTAddrSync: v28.6o, E96 ;15  
 Time.00: u10.8o, #TP109.1i ;25  
 Time.00: u11.6i ;26  
 Time.01: u10.9o ;25  
 Time.01: u11.5i ;26  
 Time.02: u10.10o ;25  
 Time.02: u11.4i ;26  
 Time.03: u10.11o ;25  
 Time.03: u11.3i ;26  
 Time.04: u10.6o, u10.13i ;25  
 Time.04: u11.14i ;26  
 Time.05: u10.5o ;25  
 Time.05: u11.13i ;26  
 Time.06: u10.4o ;25  
 Time.06: u11.12i ;26  
 Time.07: u10.3o ;25  
 Time.07: u11.11i ;26  
 Time.08: u25.8o, #TP108.1i ;25  
 Time.08: u38.13i ;25  
 Time.08: u26.6i ;26  
 Time.09: u25.9o ;25  
 Time.09: u26.5i ;26  
 Time.10: u25.10o ;25  
 Time.10: u26.4i ;26  
 Time.11: u25.11o ;25  
 Time.11: u26.3i ;26  
 Time.12: u25.6o, u25.13i ;25  
 Time.12: u26.14i ;26  
 Time.13: u25.5o ;25  
 Time.13: u26.13i ;26  
 Time.14: u25.4o ;25  
 Time.14: u26.12i ;26  
 Time.15: u25.3o ;25

XEROX	PROPRIETARY NOTE ON COVER SHEET APPLIES TO ALL SHEETS		DWG SIZE A4	DWG NO. 156P11446	SHEET REV. B
	TITLE SCHEMATIC, IOP			SHEET 55 OF	

Time.15:	u26.11i ;26	TimeData16:	u26.10i
Time.16:	u39.8o, #TP107.1i ;25	TimeData24:	u54.9o, #TP110.1i ;26
Time.16:	u38.10i ;25	TimeData24:	u40.10i
Time.16:	u40.6i ;26	TimeDataB':	u48.4i ;20
Time.17:	u39.9o ;25	TimeDataB':	u24.14o ;26
Time.17:	u40.5i ;26	TimerRd':	u06.6o, u50.22i ;22
Time.18:	u39.10o ;25	TimerWr':	u06.8o, u50.23i ;22
Time.18:	u40.4i ;26	TODIntr':	u20.6o ;20
Time.19:	u39.11o ;25	TODIntr':	u48.15i ;20
Time.19:	u40.3i ;26	UpMouseX:	u21.10o ;19
Time.20:	u39.6o, u39.13i ;25	UpMouseX:	u33.1i ;19
Time.20:	u40.14i ;26	UpMouseX:	u31.1i ;19
Time.21:	u39.5o ;25	UpMouseX:	#TP98.1i ;19
Time.21:	u40.13i ;26	UpMouseY:	u21.12o ;19
Time.22:	u39.4o ;25	UpMouseY:	u34.1i ;19
Time.22:	u40.12i ;26	UpMouseY:	u32.1i ;19
Time.23:	u39.3o ;25	UpMouseY:	#TP100.1i ;19
Time.23:	u40.11i ;26	VCC:	v29.21i ;02
Time.24:	u53.8o, #TP106.1i ;25	VCC:	v30.21i ;02
Time.24:	u38.6i ;25	VCC:	v31.21i ;02
Time.24:	u54.6i ;26	VCC:	v32.21i ;02
Time.25:	u53.9o ;25	VCC:	u61.31i ;07
Time.25:	u54.5i ;26	VCC:	u58.21i ;08
Time.26:	u53.10o ;25	VCC:	#J1.18o ;21
Time.26:	u54.4i ;26	VCC:	u73.16i ;21
Time.27:	u53.11o ;25	VCC:	u22.26i ;22
Time.27:	u54.3i ;26	VCC:	#R18.2o ;27
Time.28:	u53.6o, u53.13i ;25	VCC:	u01.2i, u01.3i, u01.4i ;27
Time.28:	u54.14i ;26	VCC:	u01.5i, u01.6i, u01.7i
Time.29:	u53.5o ;25	VCC:	u01.8i, #R2.1i, u01.1i
Time.29:	u54.13i ;26	VCC:	#Q1.1i, #R8.2i, #CR3.1o ;27
Time.30:	u53.4o ;25	VCC:	#R3.2o, #R4.2o ;27
Time.30:	u54.12i ;26	VCC:	#R1.1i ;27
Time.31:	u53.3o ;25	VCC:	#R28.1i ;27
Time.31:	u54.11i ;26	VCC:	C409, C408 ;28
TimeData':	#TP113.1i, u11.7o ;26	VCC:	#C68.1o, #F2.1o, #TP117.1i ;29
TimeData':	u24.6i	VCC:	C88, C86, C90 ;29
TimeData08:	u11.10i ;26	VCC:	#R5.1i ;30
TimeData08:	u26.9o, #TP112.1i ;26	VCC:	#R23.1i ;30
TimeData16:	u40.9o, #TP111.1i ;26	VCC:	#R26.1i ;30
		VCC:	#C3.1i, #C4.1i, #C5.1i ;31
		VCC:	#C6.1i, #C7.1i, #C8.1i
		VCC:	#C10.1i, #C11.1i, #C12.1i
		VCC:	#C14.1i, #C15.1i, #C16.1i
		VCC:	#C18.1i
		VCC:	#C19.1i, #C20.1i, #C21.1i ;31
		VCC:	#C22.1i, #C23.1i, #C25.1i
		VCC:	#C26.1i, #C27.1i, #C28.1i
		VCC:	#C29.1i, #C30.1i, #C31.1i
		VCC:	#C32.1i, #C33.1i, #C34.1i
		VCC:	#C35.1i

VCC: #C52.1i, #C53.1i, #C54.1i ;31  
VCC: #C55.1i, #C56.1i, #C57.1i  
VCC: #C58.1i, #C59.1i, #C60.1i  
VCC: #C61.1i, #C62.1i, #C63.1i  
VCC: #C64.1i, #C65.1i, #C66.1i  
VCC: #C36.1i, #C37.1i, #C38.1i ;31  
VCC: #C39.1i, #C40.1i, #C41.1i  
VCC: #C42.1i, #C43.1i, #C44.1i  
VCC: #C45.1i, #C46.1i, #C47.1i  
VCC: #C48.1i, #C49.1i, #C50.1i  
VCC: #C51.1i

VDD: u58.40i ;08  
VDD: u07.14i ;28  
VDD: #C69.1o, #F3.1o, #TP118.1i ;29

VFF: u07.1o ;28  
VFF: #C67.2o, #F1.1o, #TP119.1i ;29

VFOEn': u14.17i, #TP79.1i, u60.7o ;12  
VFOEn': u13.17i

Wait: E17, v47.3i ;17

WakeMode.0: v41.16o ;15  
WakeMode.0: v40.15i ;15  
WakeMode.0: v33.5i ;15  
WakeMode.0: v38.6i ;17

WakeMode.1: v41.19o ;15  
WakeMode.1: v40.13i ;15  
WakeMode.1: v33.4i ;15  
WakeMode.1: v38.5i ;17

WDB: u43.3i, u43.11i, u57.3o ;09  
WDB: u29.3i  
WDB: u16.3i ;09

WDCComp: u16.7o ;09  
WDCComp: u37.17i ;11

WDEarly: u15.4i, u15.2o ;09  
WDEarly: u16.4i ;09

WDL: u29.5o, u29.12i ;09

WDLate: u15.18i, u15.16o ;09  
WDLate: u16.5i ;09

WDLClr: #TP69.1i, u15.8i, u15.6o ;09  
WDLClr: u27.3i ;09

WDSNorm: u15.13i, u15.9o ;09  
WDSNorm: u16.6i ;09

WDS: u29.9o, #TP66.1i ;09  
WDS: u15.3i ;09

WrFlopState': u44.15o ;05

WrFlopState': u89.11i ;08

WriteClocks1': u44.14o ;05  
WriteClocks1': u49.11i ;20

WriteControl1': u44.7o ;05  
WriteControl1': u47.11i ;20

WriteCPControl': u44.11o ;05  
WriteCPControl': v22.9i ;15  
WriteCPControl': u88.9i ;15

WriteCPOut': u44.12o ;05  
WriteCPOut': v35.4i ;16

WriteTPCHigh': v34.9o, E91 ;15

WriteTPCLow': v34.7o, v27.4i ;15

WriteTPCLow: v27.16o, E191 ;15

X.10: E46, v41.7i ;15  
X.10: v40.14o ;15  
X.10: v42.7i ;16  
X.10: v43.6o ;16

X.11: E146, v41.8i ;15  
X.11: v40.12o ;15  
X.11: v42.8i ;16  
X.11: v43.9o ;16

X.12: E47, v41.13i ;15  
X.12: v40.3o ;15  
X.12: v42.13i ;16  
X.12: v43.12o ;16

X.13: E147, v41.14i ;15  
X.13: v40.5o ;15  
X.13: v42.14i ;16  
X.13: v43.15o ;16

X.14: E48, v41.17i ;15  
X.14: v40.7o ;15  
X.14: v42.17i ;16  
X.14: v43.16o ;16

X.15: E148, v41.18i ;15  
X.15: v40.9o ;15  
X.15: v42.18i ;16  
X.15: v43.19o ;16

X.8: E45, v41.3i ;15  
X.8: v40.18o ;15  
X.8: v42.3i ;16  
X.8: v43.2o ;16

X.9: E145, v41.4i ;15  
X.9: v40.16o ;15  
X.9: v42.4i ;16

XEROX	PROPRIETARY NOTE ON COVER SHEET APPLIES TO ALL SHEETS		DWG SIZE A4	DWG NO. 156P11446	SHEET REV. B
	TITLE SCHEMATIC, IOP		SHEET 57	OF	

X.9: v43.5o ;16

<IOPIData': v43.1i, E34 ;16

<IOPIData': v35.13i ;16

<IOPIData': v21.11i ;16

<IOPStatus': v40.1i, E134, v40.19i ;15

XEROX	PROPRIETARY NOTE ON COVER SHEET APPLIES TO ALL SHEETS		DWG SIZE A4	DWG NO. 156P11446	SHEET REV.
	TITLE	SCHEMATIC, IOP		SHEET 58 OF	B