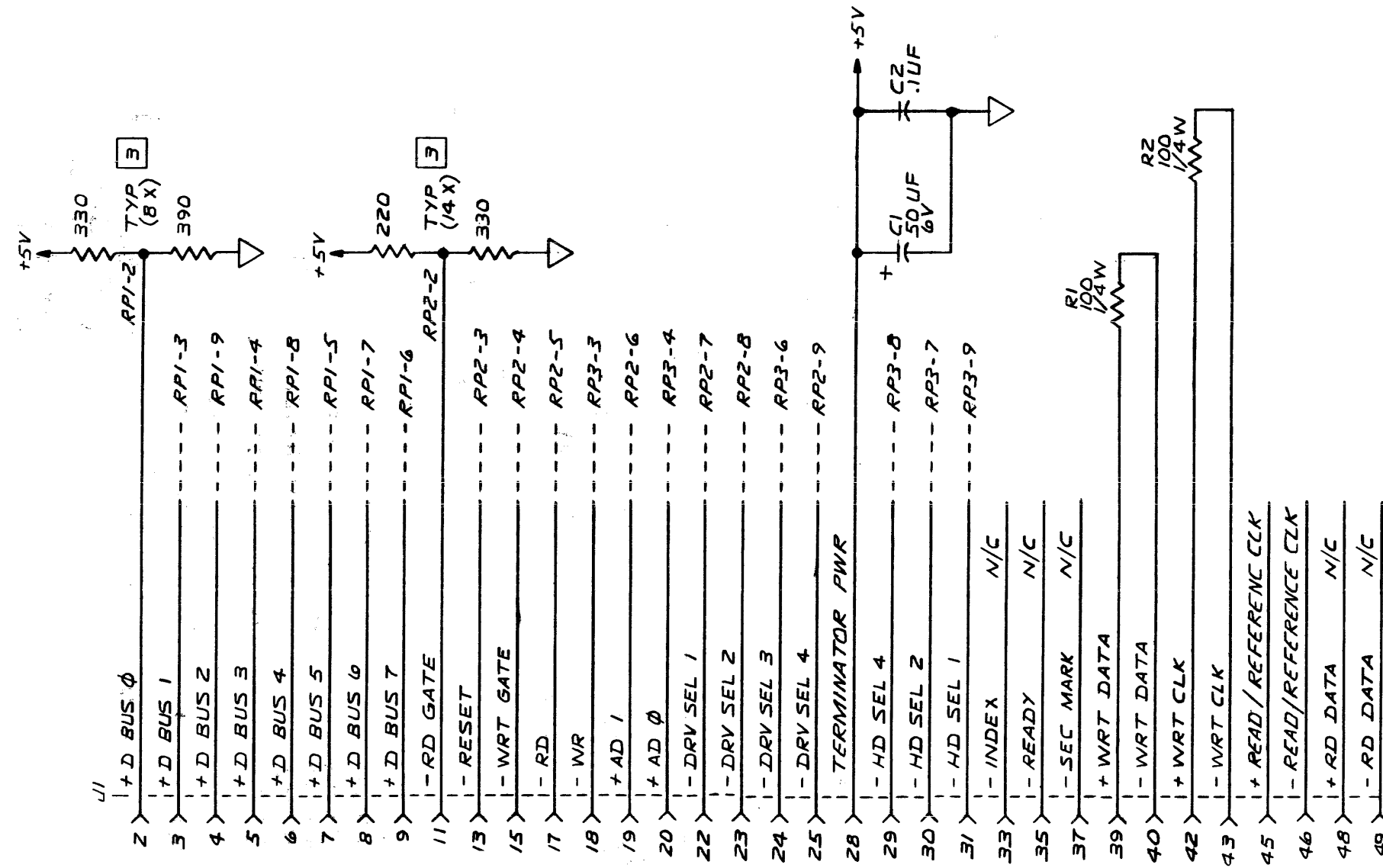


DATE	E/C NO.	DATE	E/C NO.	200134
9-8-80	1189			RELEASED FOR ASSEMBLY
2-24-81	1324			200133



NOTES: UNLESS OTHERWISE SPECIFIED

- THIS SCHEMATIC DIAGRAM REPRESENTS ASM. 200133 AT E.C. LEVEL 1324.
- PINS 1, 10, 12, 14, 16, 21, 26, 27, 32, 34, 36, 38, 41, 44, 47, 50, ON J1 ARE TIED TO GND.
- RESISTORS ARE IN GROUPS OF TWO HOOKED TOGETHER IN SERIES HAVING A COMMON LINE FOR POWER (1/6) AND A COMMON LINE FOR GROUND (8).

MATERIAL	TOLERANCE UNLESS OTHERWISE NOTED	TITLE SCHEMATIC DIAGRAM		PRIAM
		PRIAM TERMINATOR		
	LINEAR ± .XX ± .XXX	DESIGN 985 9-8-80	SCALE	
HARDNESS	ANGULAR	DETAIL WJM 7-3-80	SHEET 1 OF 1	
SURFACE TREATMENT	CORNERS OUTSIDE MAX DFTG CHK EDGES INSIDE MAX APPRO	7-3-80	C	200134