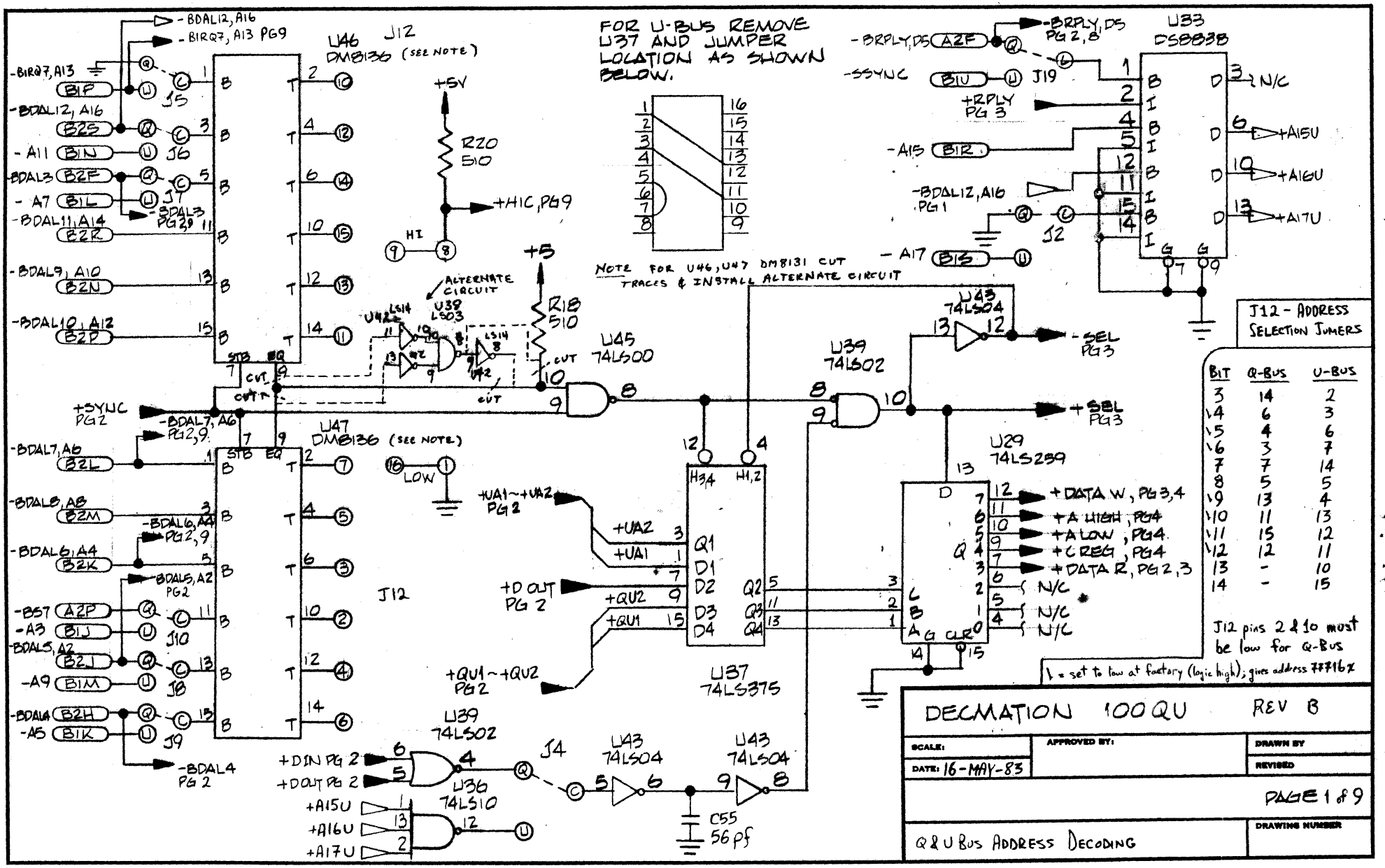
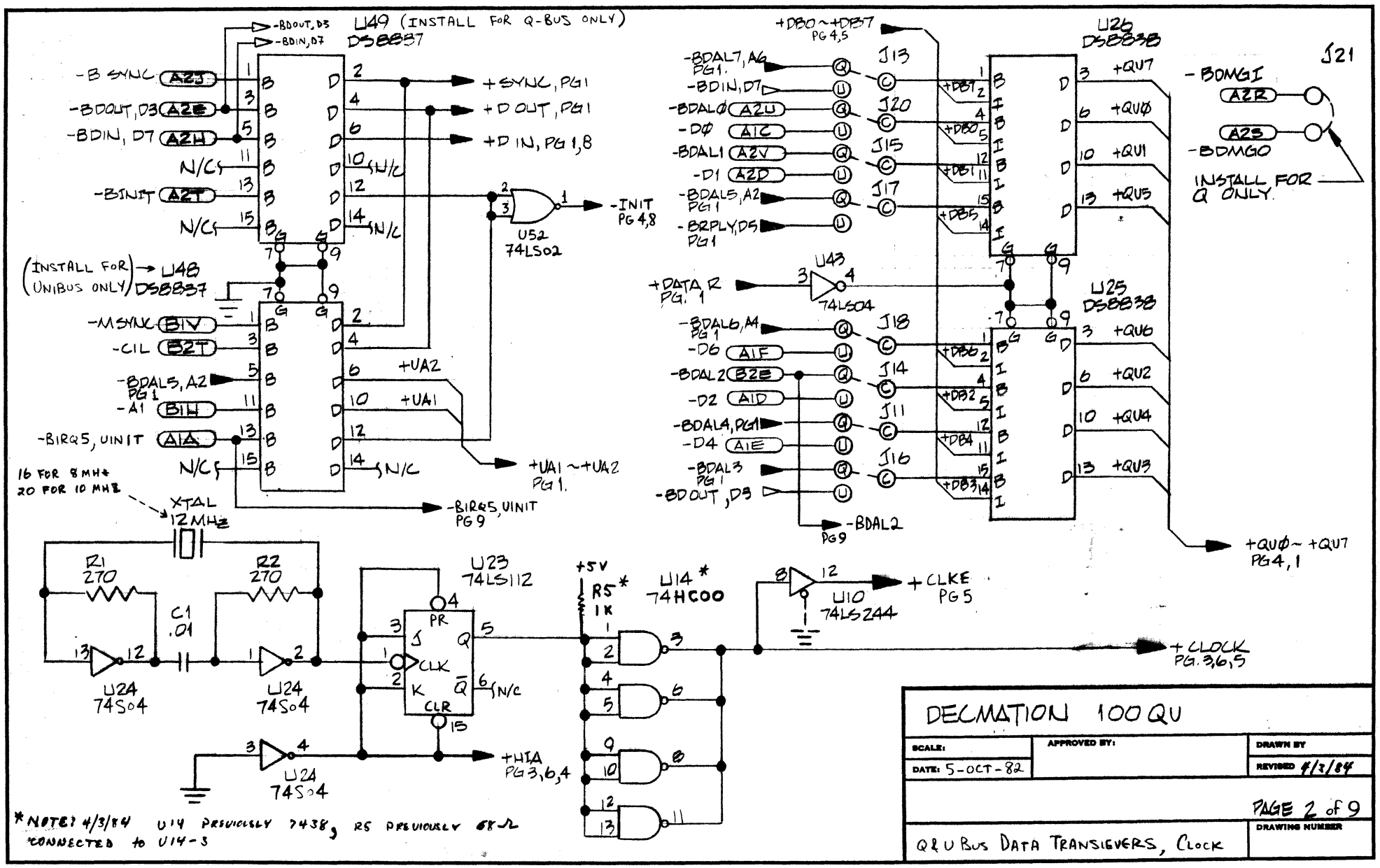


BIT	Q-BUS	U-BUS
3	14	2
14	6	3
15	4	6
16	3	7
17	7	14
18	5	5
19	13	4
110	11	13
111	15	12
112	12	11
13	-	10
14	-	15

SCALE:	APPROVED BY:	DRAWN BY	
DATE: 16-MAY-83		REVISED	



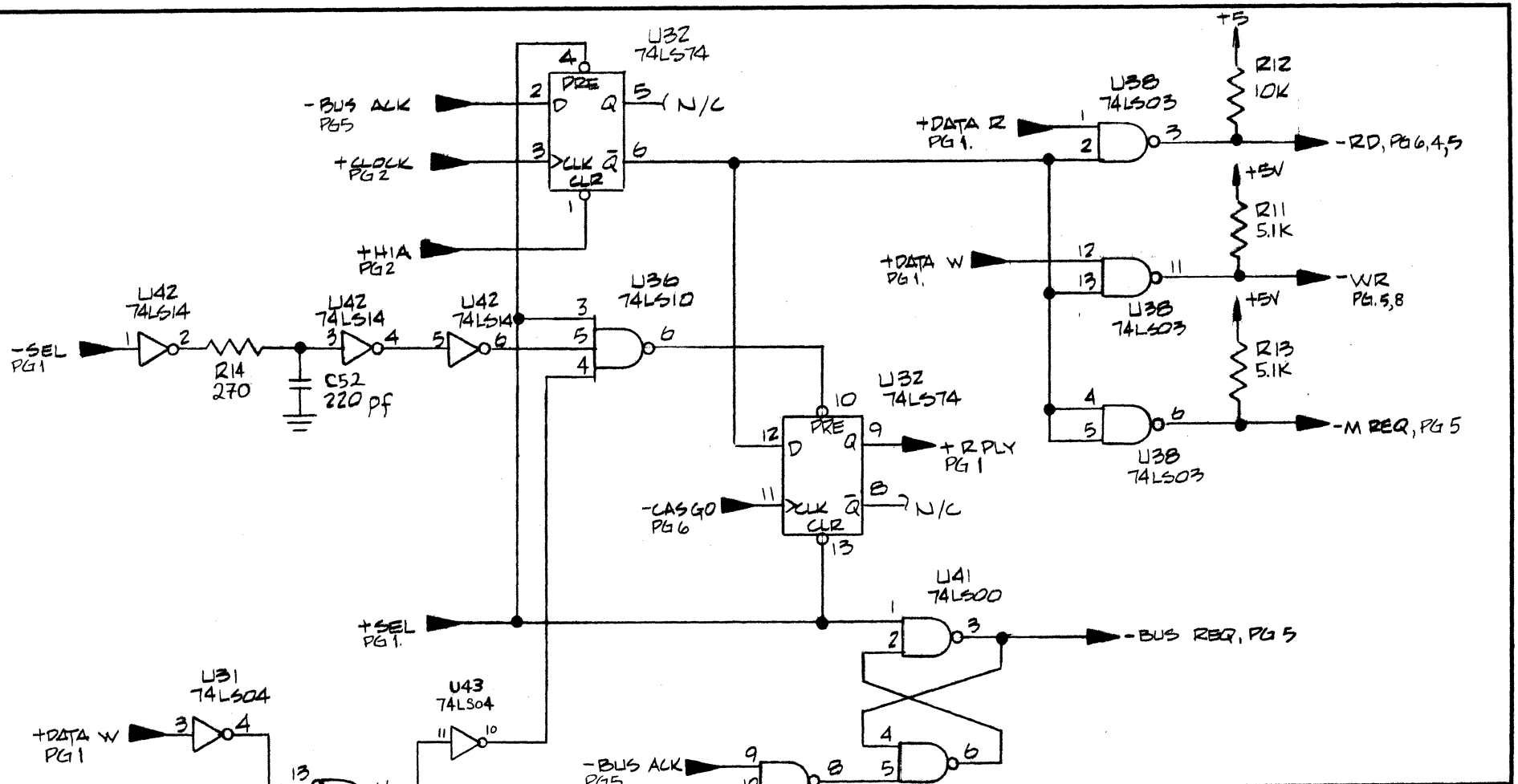
DECMATION 100 QU

SCALE:	APPROVED BY:	DRAWN BY
DATE: 5-OCT-82		REVISED 4/3/84

PAGE 2 of 9

Q & U BUS DATA TRANSCEIVERS, CLOCK

DRAWING NUMBER



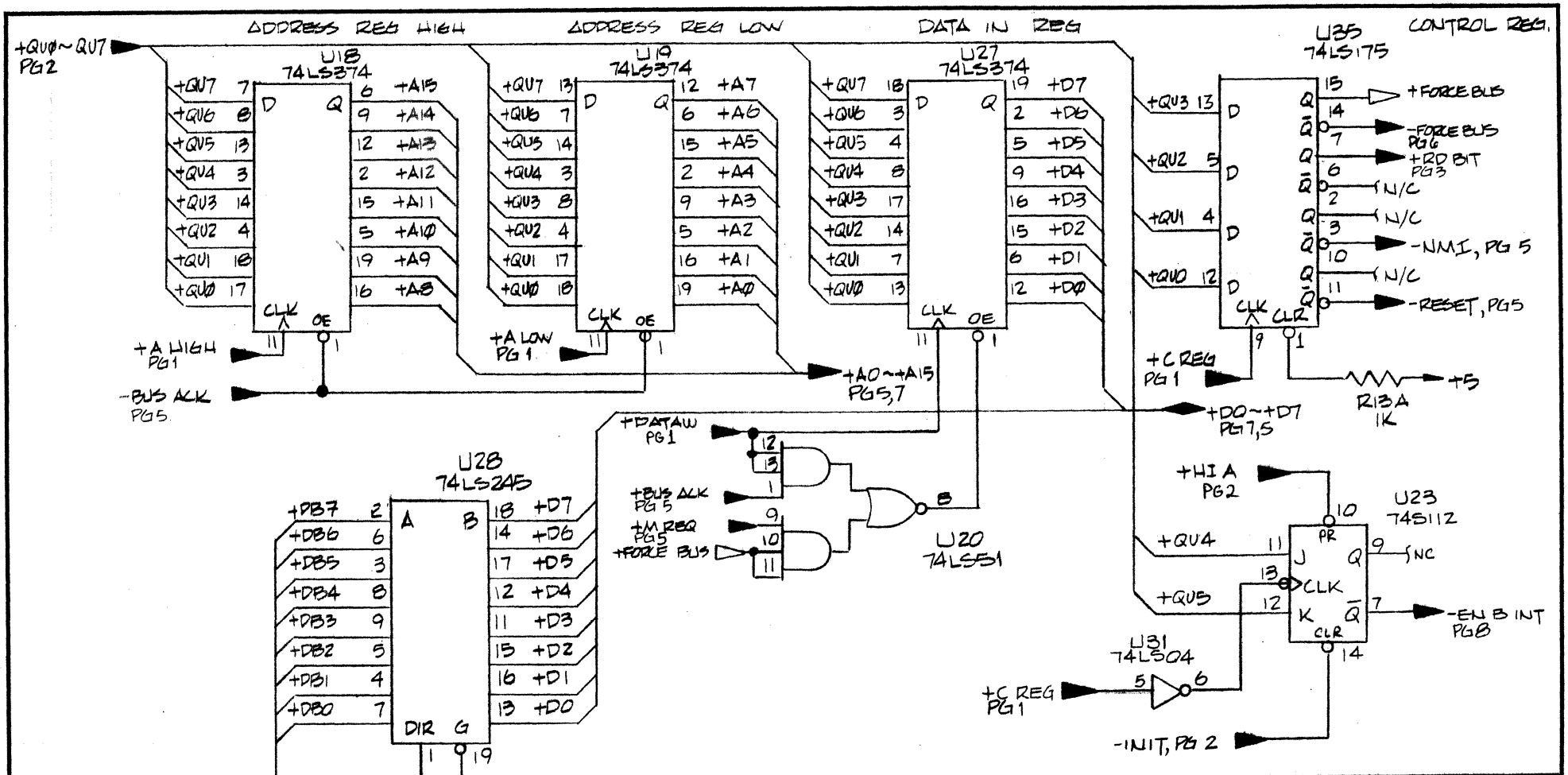
DECMATION 100 QU

SCALE: 5-OCT-82	APPROVED BY:	DRAWN BY
DATE:		REVISED

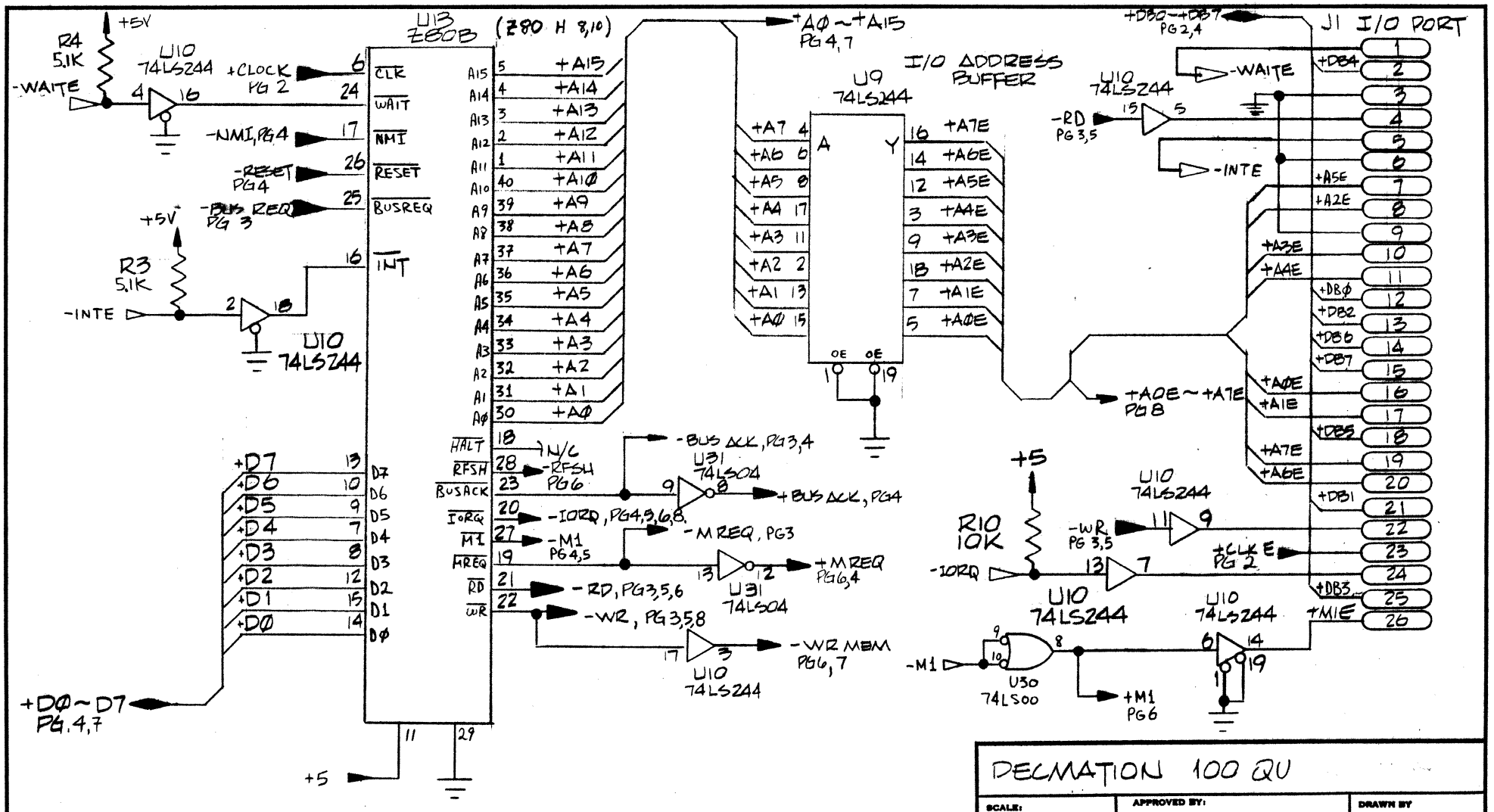
PAGE 3 of 9

DMA CONTROL LOGIC

DRAWING NUMBER



DECMATION 100 QU		
SCALE:	APPROVED BY:	DRAWN BY
DATE: 5-OCT-82		REVISED
		PAGE 4 of 9
Q & U Bus Registers		DRAWING NUMBER



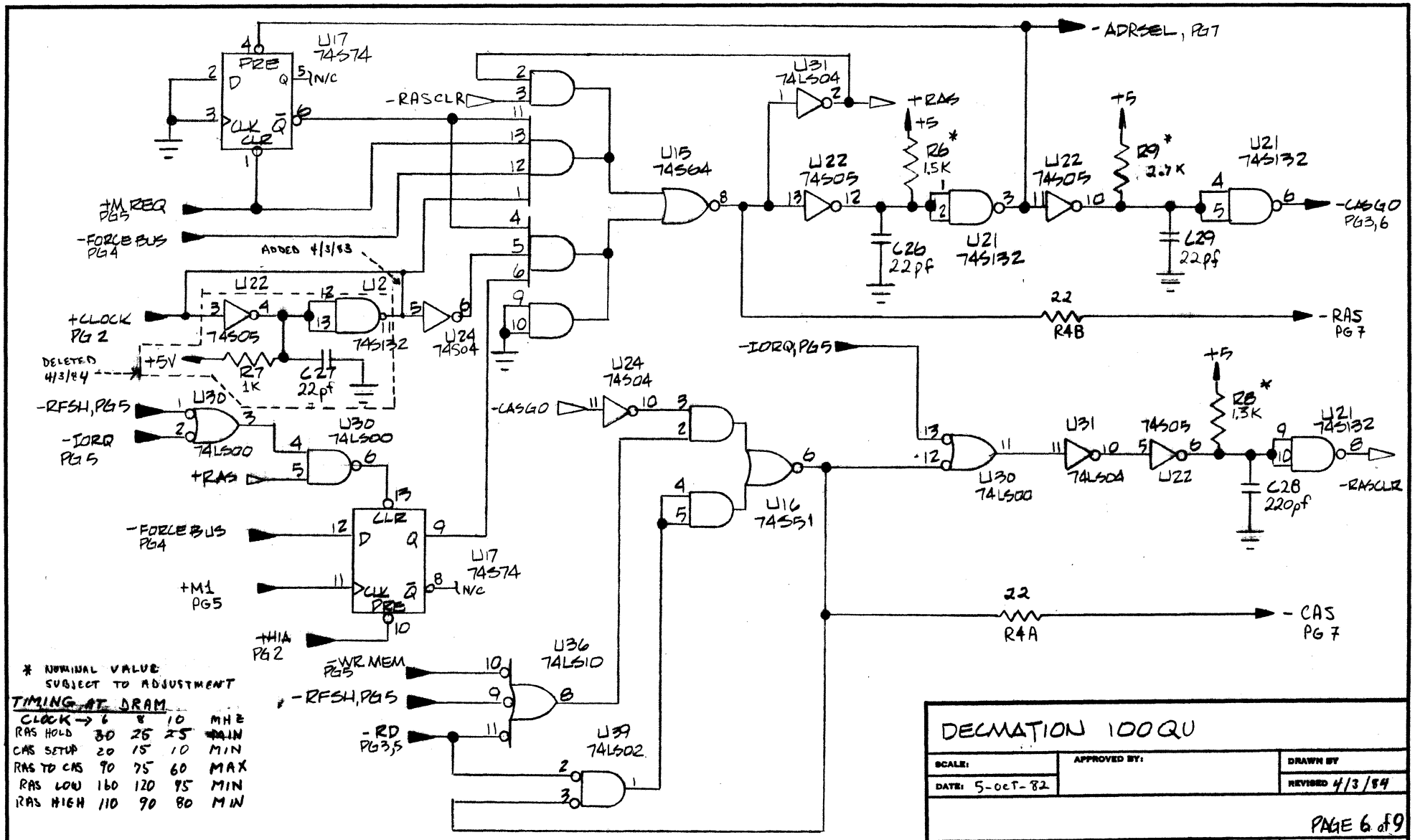
DECMATION 100 QU

SCALE:	APPROVED BY:	DRAWN BY
DATE: 5-OCT-82		REVISED 4/3/84

PAGE 5 of 9

Z80 & I/O PORT

DRAWING NUMBER



* NOMINAL VALUE
SUBJECT TO ADJUSTMENT

TIMING AT DRAM

CLOCK →	6	8	10	MHz
RAS HOLD	30	25	25	MIN
CAS SETUP	20	15	10	MIN
RAS TO CAS	90	75	60	MAX
RAS LOW	160	120	95	MIN
RAS HIGH	110	90	80	MIN

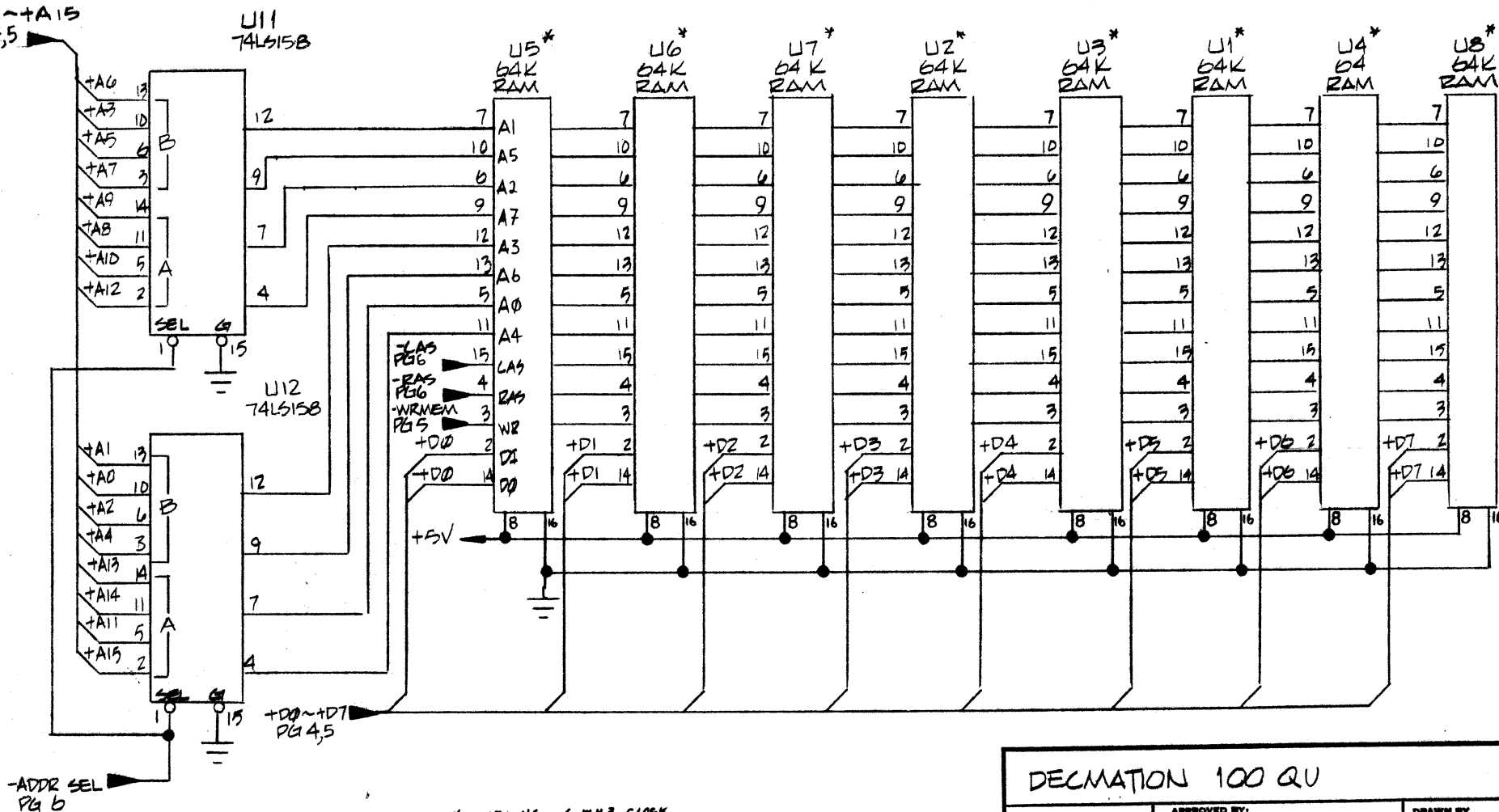
DECMATION 100QU

SCALE:	APPROVED BY:	DRAWN BY
DATE: 5-OCT-82		REVISED 4/3/84

PAGE 6 of 9

DYNAMIC RAM TIMING GENERATION

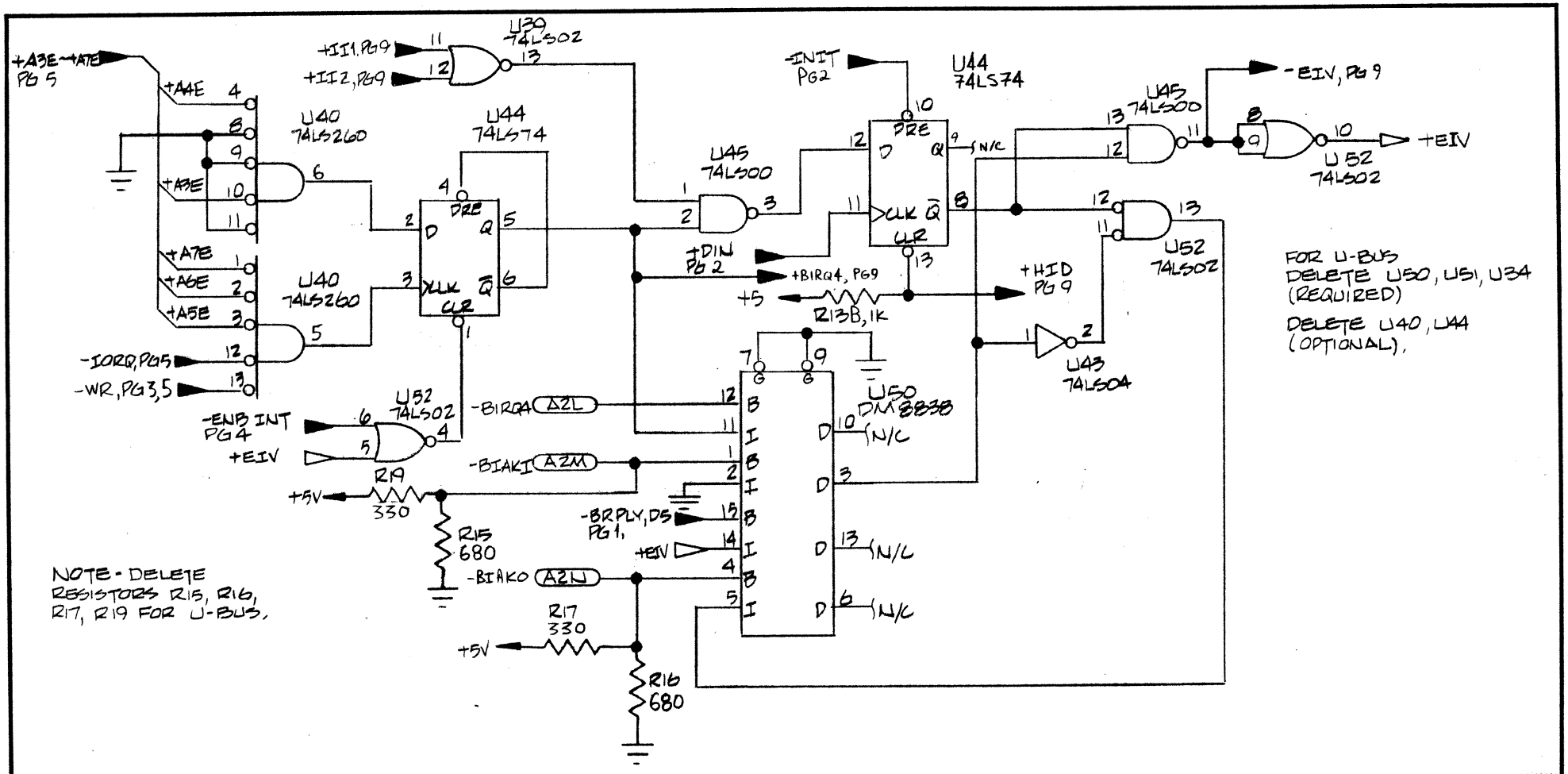
DRAWING NUMBER



* 150 NS 6-MHZ CLOCK
120 NS 8-MHZ CLOCK
100 NS 10-MHZ CLOCK

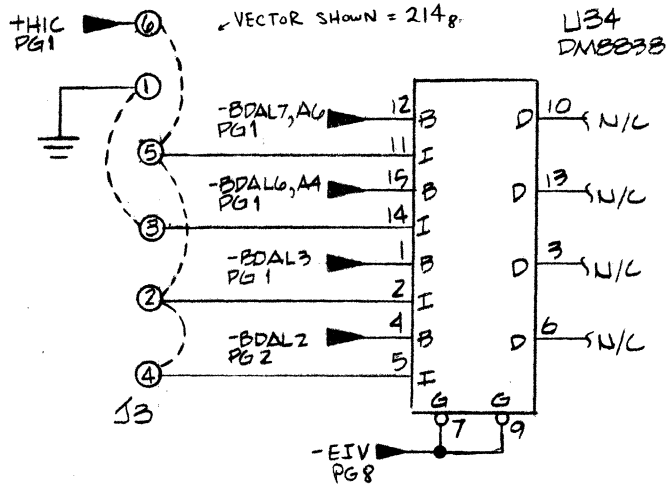
DECMATION 100 QU

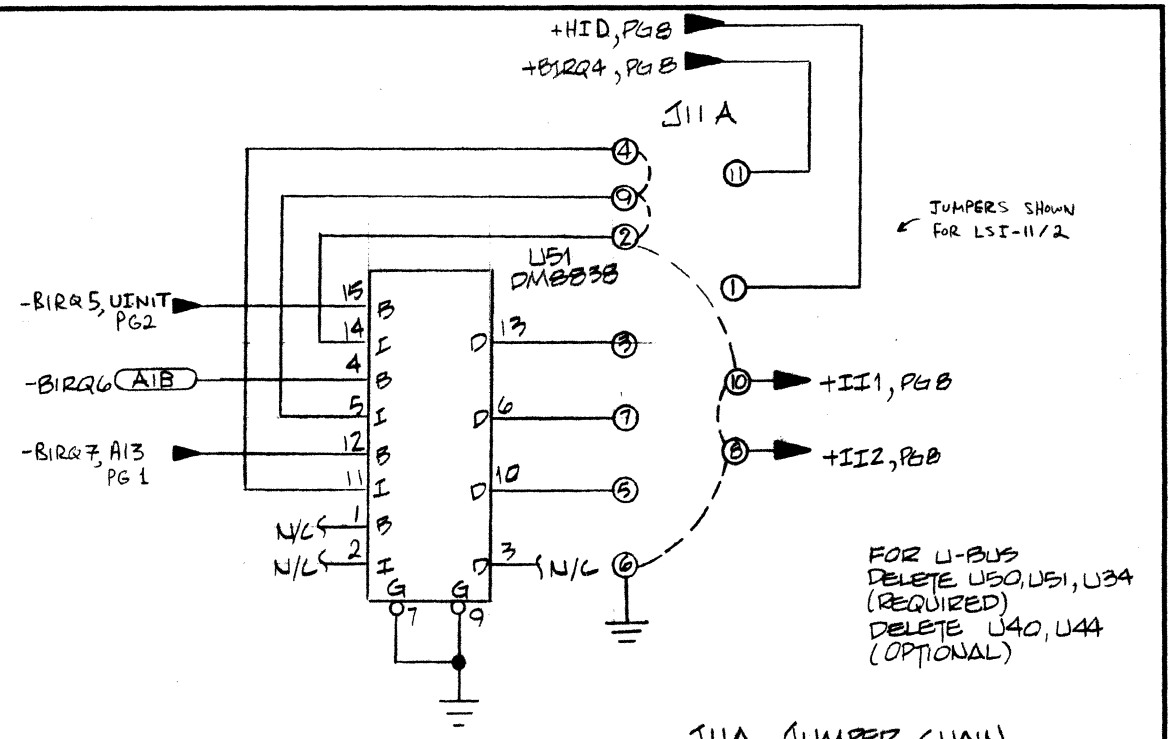
SCALE:	APPROVED BY:	DRAWN BY
DATE: 5-OCT-82		REVISED 4/3/84
		PAGE 7 of 9
DYNAMIC RAMS		DRAWING NUMBER



DECMATION 100 QU

SCALE:	APPROVED BY:	DRAWN BY
DATE: 5-OCT-82		REVISED
		PAGE 8 of 9
Q-BUS INTERRUPT LOGIC		DRAWING NUMBER





INTERRUPT VECTOR (OCTAL)	INTERRUPT VECTOR SELECTION J3 JUMPER CHAIN
100	6-3 / 1-2-4-5
104	6-3-4 / 1-2-5
110	6-3-2 / 1-4-5
114	6-3-2-4 / 1-5
200	6-5 / 1-2-3-4
204	6-5-4 / 1-2-3
210	6-5-2 / 1-3-4
214	6-5-4-2 / 1-3
300	6-3-5 / 1-2-4
304	6-3-5-4 / 1-2
310	6-3-5-2 / 1-4
314	6-2-3-4-5 / 1-N/C

J11A JUMPER CHAIN

LSI-11/2	6-2-9-4-10-8 / N.C. 5,7,3,11,1
LSI-11/23 P4	6-2-9-4/3-10/7-8 /N.C 5,1,11
INTERRUPT PRIORITY SELECTION. P:5	6-4-9-10/11-2/7-8/ N.C. 3,5,1
P:6	6-2-4-10/ 11-9/5-8/N.C 3,7,1
P:7	6-2-8-10/ 11-4-9 / N.C

DECMATION 100QU

SCALE:	APPROVED BY:	DRAWN BY
DATE: 5-OCT-82		REVISED

PG 9 of 9

INTERRUPT VECTOR & PRIORITY SELECT

DRAWING NUMBER