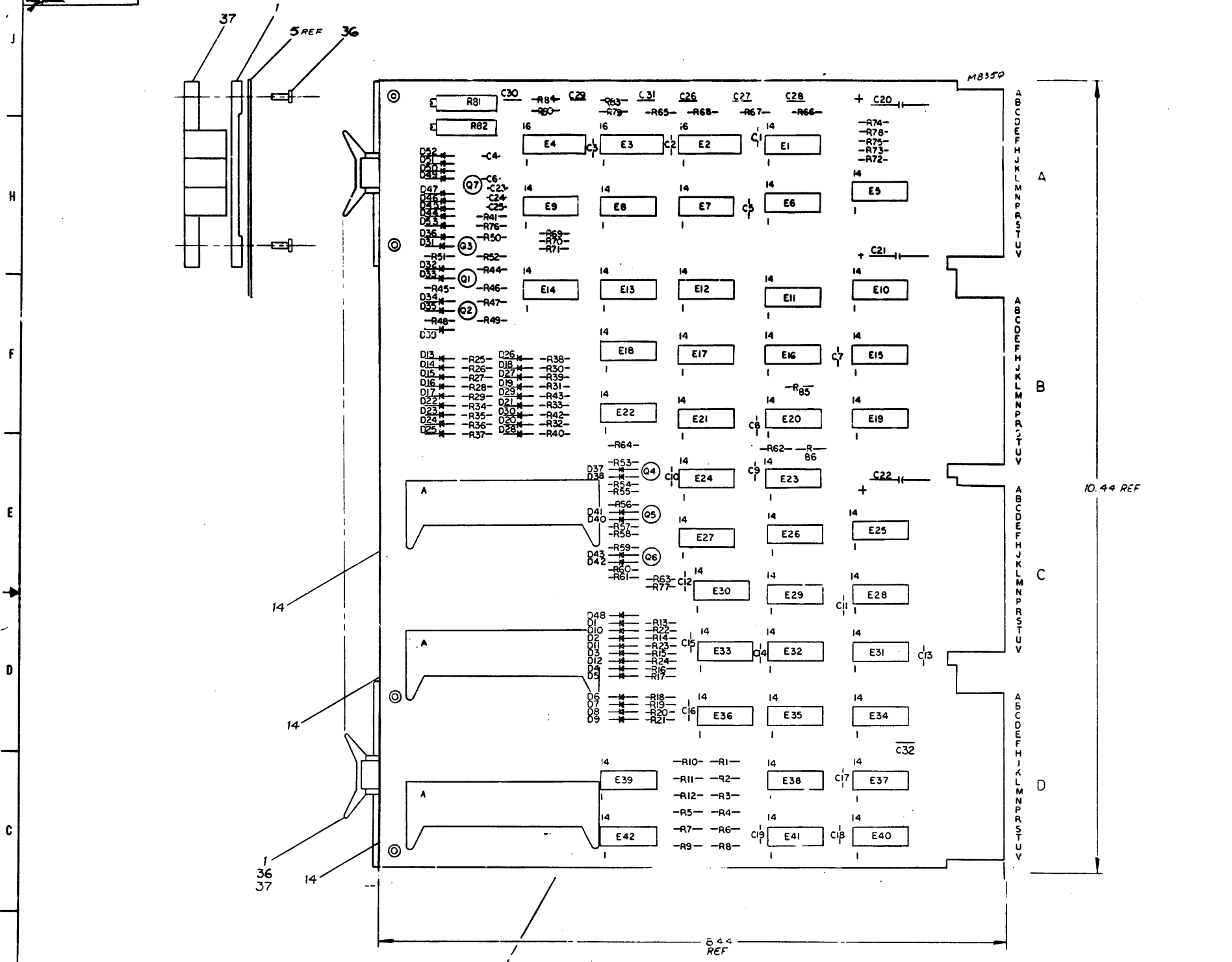


**KA8-E positive  
I/O bus interface  
engineering drawings**



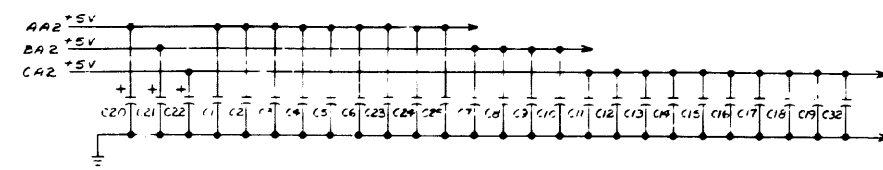
1. UNLESS OTHERWISE NOTED, CAPACITOR 0.1UF 100V 20% RESISTOR 220 1/4W 5%

NOTES:  
1. UNLESS OTHERWISE NOTED, CAPACITOR 0.1UF 100V 20% RESISTOR 220 1/4W 5%



QTY	REF DESIGNATION	DESCRIPTION	PART NO.
1	R74	RES. 10K 1/4W 5%	130017
1	E26	IC DEC 7414	1911324
2	R84, R83	RES 470K 1/4W 5%	1302344
1	C24	CAP 33 PF, 100V 5% D.M.	1000009
1/2		GRIPLET	1210249-C
4		HANDLE FLIP CHIP - MAGENTA	9006337-02
8		EYELET GS 4-11 STIMPDA	9006750
3	E2, E3, E4	IC DEC 74123	1912436
2	E23, E42	IC DEC 2501	1910010
5	E10, E12, E16, E20, E30	IC DEC 6380	1913771
4	E13, E18, E22, E27	IC DEC 7417	1909929
5	E1, E7, E8, E13, E36	IC DEC 7416	1915928
6	E125, E28, E31, E34, E40	IC DEC 8881	1909705
2	E38, E41	IC DEC 7404	1909656
4	E15, E32, E35, E37	IC DEC 384	1909486
1	E29	IC DEC 7402	1907004
1	E39	IC DEC 7430	1905578
1	E24	IC DEC 7420	1905577
2	E5, E8	IC DEC 7410	1905576
1	E11	IC DEC 7400	1905575
4	E6, E7, E9, E19	IC DEC 7474	1907547
7	Q1 THRU Q7	TRANSISTOR DEC 3009B	1503100
2	R91, R92	RES 60K 3/4W 10% 76 PR	1309143-13
2	R79, R80	RES 4.7K 1/4W 5%	1300447
4	R65 THRU R68	RES 6.8K 1/4W 5%	1301423
6	R50, R44, R47, R53, R54, R59	RES 27 1/4W 10%	1301420
1	R60	RES 5.6K 1/4W 5%	1301574
68	R1 THRU R43, R45, R46, R48, R49, R51, R52, R54, R55, R57, R58, R60, R61 THRU R64, R69 THRU R118	RES 220 1/4W 5%	1300271
3		HEADER RIGHT ANGLE	1207741
43	D1 THRU D43	DIODE D664	1100114
10	D44 THRU D53	DIODE D662	1100113
23	C1 THRU C19, C23, C24, C25, C32	CAP. 0.1UF, 100V, 20% DISC	1001610
3	C20, C21, C22	CAP. 6.8UF, 35V, 10% STMT	1005106
1	C30	CAP 150PF, 100V 5% D.M.	1000019
1	C28	CAP 68PF, 100V, 5% D.M.	1000014
1	C27	CAP 10PF, 100V, 5%	1000016
2	C31, C26	CAP 10PF, 100V, 5% D.M.	1000006
1		ETCHED CIRCUIT BOARD	5003504
REF		MODULE ECO HISTORY	B-MH-M8350-04
REF		ASSY/DRILLING HOLE LAYOUT	D-MH-M8350-2-5
REF		X-Y COORDINATE HOLE LAYOUT	K-CO-M8350-0-4
4		SPALER (CABLE CLAMP)	1202754

IC TYPE	QTY	REF	PT	TO PT
DEC 2501	NONE	NONE		
DEC 74123	8	16		
DEC 6380	1	8		
DEC 384	1	8		



ETCH BOARD REV D

DEC 3009B IN 3009B

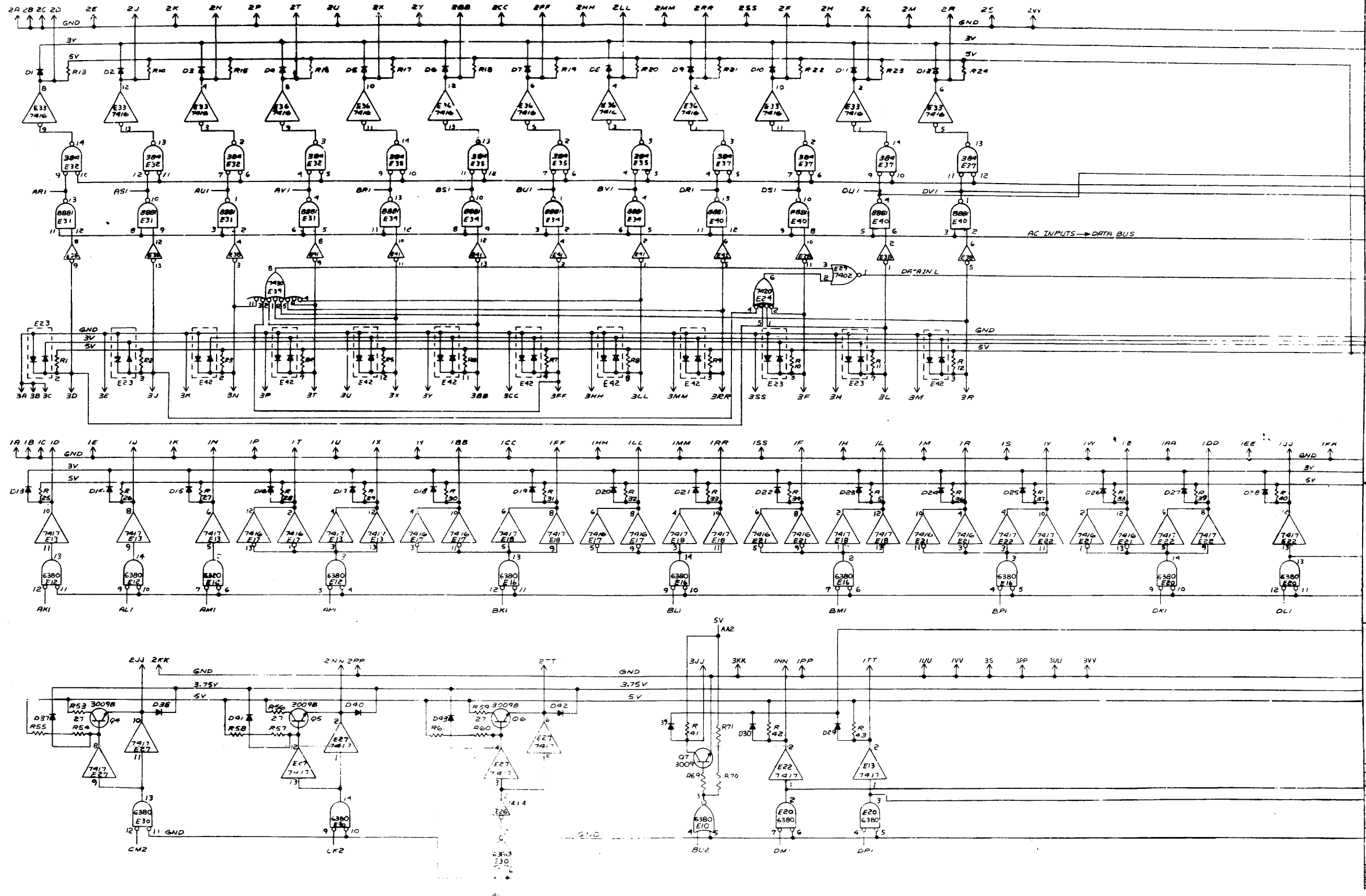
D664 IN 3606

D662 IN 495

ECS M8350-0-1

POSITIVE I/O BUS INTERFACE

EQUIPMENT CORPORATION



QTY	DESCRIPTION	PART NO.	REV.
1	3009B	3009B	1
1	7416	7416	1
1	7417	7417	1
1	6380	6380	1
1	384	384	1
1	7416	7416	1
1	7417	7417	1
1	6380	6380	1
1	384	384	1

DATE	5-23-71
DESIGNED BY	[Signature]
CHECKED BY	[Signature]
DATE	5-23-71
DESIGNED BY	[Signature]
CHECKED BY	[Signature]
DATE	5-23-71
DESIGNED BY	[Signature]
CHECKED BY	[Signature]
DATE	5-23-71
DESIGNED BY	[Signature]
CHECKED BY	[Signature]

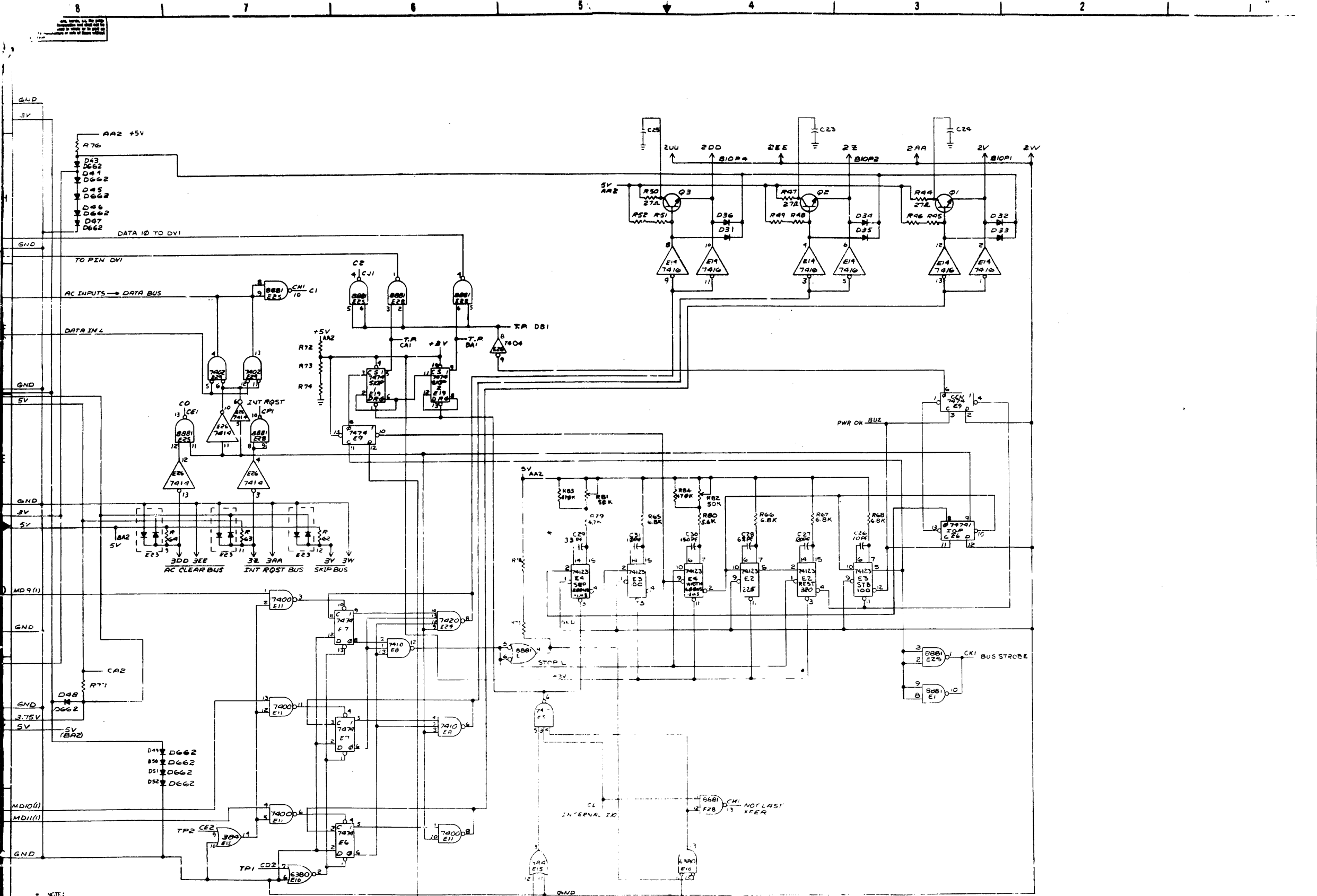
  

REV.	1	DATE	5-23-71
REV.	2	DATE	5-23-71
REV.	3	DATE	5-23-71

DESCRIPTION	POSITIVE I/O BUS INTERFACE
PARTS LIST	3009B, 7416, 7417, 6380, 384
REVISIONS	1, 2, 3
DATE	5-23-71
DESIGNED BY	[Signature]
CHECKED BY	[Signature]
DATE	5-23-71
DESIGNED BY	[Signature]
CHECKED BY	[Signature]
DATE	5-23-71
DESIGNED BY	[Signature]
CHECKED BY	[Signature]

REVISIONS  
 CHANGE NO.  
 8  
 7  
 6  
 5  
 4  
 3  
 2  
 1



\* NOTE:  
IF SEP IS INCREASED TO 500 NS OR MORE  
THE SLOW CYCLE JUMPER MUST BE INSTALLED  
ON THE TIMING BOARD (M833 OR M833C)

QTY	DESCRIPTION	PART NO	ITEM NO.
PARTS LIST			
EQUIPMENT CORPORATION			
TITLE POSITIVE I/O BUS INTERFACE			
REV ECS M8350-0-1			
SCALE 3 OF 3			

DIGITAL EQUIPMENT CORPORATION MAYNARD, MASSACHUSETTS				LEGEND		QUANTITY/VARIATION												
MADE BY J. McCluskey DATE 2/23/72		CHECKED <i>[Signature]</i> DATE 3-7-72		SECTION		D	DOCUMENT											
ENG <i>[Signature]</i> DATE 3-7-72		PROD <i>[Signature]</i> DATE 3-7-72		ISSUED SECT.		DN	DOCUMENT CHANGE NOTICE											
						PA	PAPER TAPE ASCII											
						PB	PAPER TAPE BINARY											
						PM	PAPER TAPE READ-IN-MODE											
ITEM NO.	DWG NO. / PART NO.	DESCRIPTION				KA8-E						KIT CHECK	BY	DATE	INSTALLATION CHECK	BY	DATE	
1	M8350	Positive I/O Bus Interface Module				1												
2	BC08-J	Control Cable				3												
3	A-ML-KA8-E-F	KA8-E Print Set				1												
4	Dec-8E-HR2B-D-KA8	KA8-E Maintenance Manual				1												
		NOTE: If item 4 is temporarily waived, ship the following:																
	A-SP-KA8-E-4	KA8-E Engineering Specification				1												
	A-SP-KA8-E-3	KA8-E Acceptance Procedure				1												
TITLE ACCESSORY LIST				ASSY. NO.		SIZE	CODE	NUMBER				REV.	ECO NO					
				SHEET OF		A	AL	KA8-E-5										
						DIST.												

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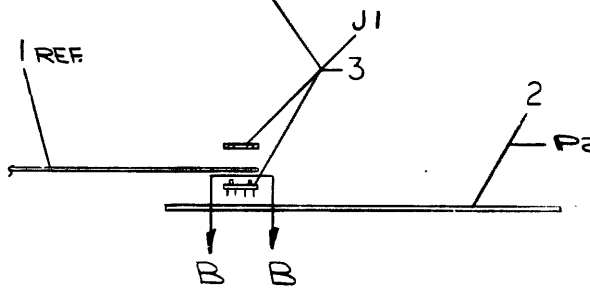
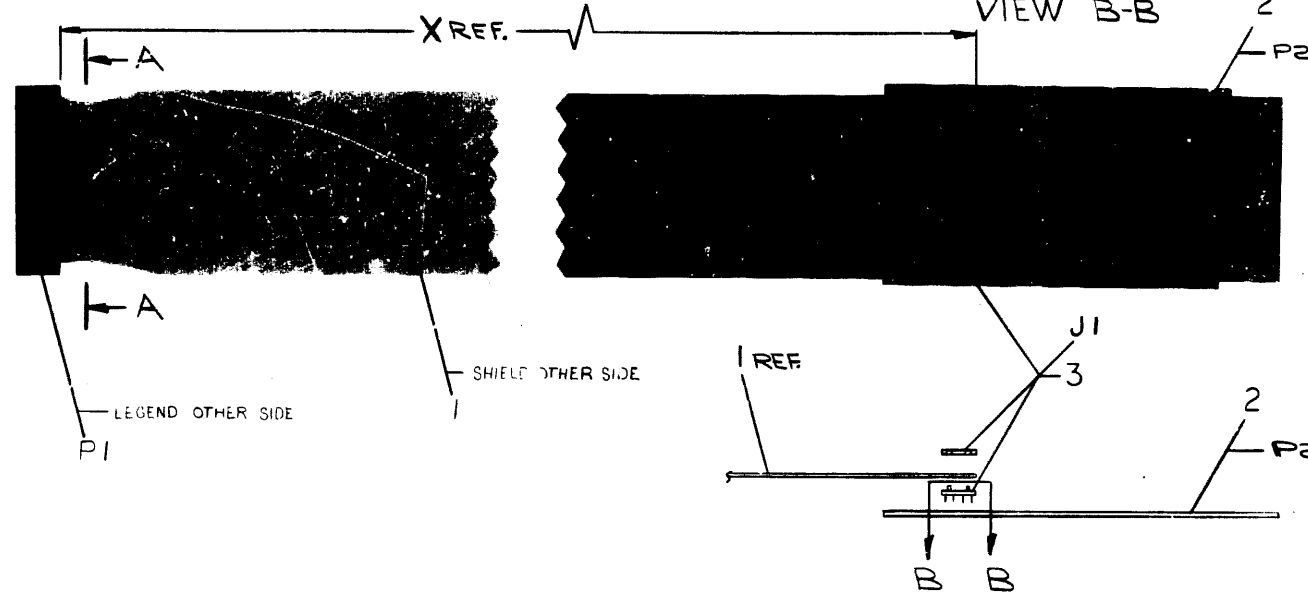
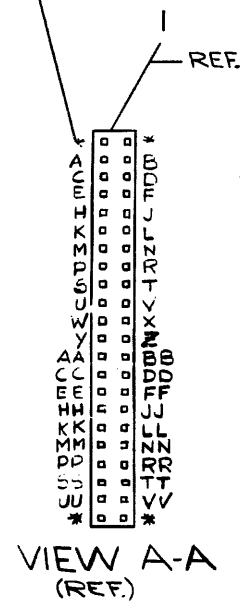
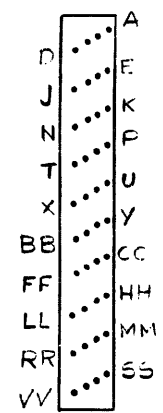
WIRE TABLE					
ITEM NO	DESCRIPTION	FROM CONNECTION	TO CONNECTION	REMARKS	
1	30	GREY	P1 - D	P2 - B1	
			P1 - J	P2 - D1	
			P1 - F	P2 - D2	
			P1 - N	P2 - E1	
			P1 - L	P2 - E2	
			P1 - T	P2 - H1	
			P1 - R	P2 - H2	
			P1 - X	P2 - J1	
			P1 - V	P2 - K2	
			P1 - BB	P2 - L1	
			P1 - FF	P2 - M1	
			P1 - LL	P2 - M2	
			P1 - RR	P2 - P1	
			P1 - DD	P2 - P2	
			P1 - JJ	P2 - S1	
			P1 - NN	P2 - S2	
			P1 - TT	P2 - T2	
				P2 - V2	

LEGEND		
NUMBER	ITEM NO.	DIM. X'
BC08J-0-06	7007036-20	6 FT ± 2 IN
BC08J-0-10	7007036-12	10 FT ± 2 IN
BC08J-0-15	7007036-15	15 FT ± 3 IN
BC08J-0-25	7007036-25	25 FT ± 3 IN
BC08J-0-50	7007036-50	50 FT ± 1 FT

- NOTES:
- CONNECTORS P1 AND J1 ARE TO BE WIRED POINT TO POINT P1-A TO J1-A THRU P1-VV TO J1-VV.
  - \*ASTERISKS INDICATE CAVITIES N.T. USED OR DESIGNATED BY LETTERS
  - ALL P1 & P2 CONNECTIONS NOT LISTED ON THE WIRE TABLE ARE GROUNDED.

SEE NOTE #2

SEE NOTE #3



REV	DATE	BY	CHK
1	1/6/71	GARDNER	
2	5/17/71	GARDNER	

FIRST USED ON OPTION/MODEL BC08J	DO NOT SCALE DRAWING UNLESS OTHERWISE SPECIFIED DIMENSIONS IN INCHES
TOLERANCES DECIMALS FRACTIONS ANGLES XXX ± .005 XX ± .01 X ± .02	TOLERANCES DECIMALS FRACTIONS ANGLES ± 0.30 FINAL SURFACE QUALITY REMOVE BURRS AND BREAK SHARP CORNERS
MATERIAL + + +	FINISH + + +

QTY.	DESCRIPTION	PART NO.	REV.
1	ETCH BOARD CONNECTOR	1210073-0	3
1	M953 I/O CABLE BD	M953-0-5	2
1	I/O CABLE	SEE LEGEND	1

DATE 12/1/70	DATE 12/1/70	DATE 12/1/70	DATE 12/1/70
BY MacDonald	BY MacDonald	BY MacDonald	BY MacDonald

EQUIPMENT CORPORATION MAYFIELD, MASSACHUSETTS	
TITLE I/O CABLE (BC08J)	REV. B
SCALE NONE	SHEET 1 OF 1