
* d i s i t a l *

INTEROFFICE MEMORANDUM

date: 3/28/83
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to: Vern Poulter
cc: Ulf Faugerquist
Len Kreidermacher

subject: FPA plan

I believe if we are to get going with 4050, we must make some serious changes in organization. First, I would comment that some meaningful work came out of the recent JAWS meetings; the results provide a good general guideline. I suggest it is time to pass project planning to the people who will really do the work, so that the details can be made to fit together and, moreover, so that we can find out and have confidence in a plan which should tell us who is doing what and when.

FPA ORGANIZATION

My responsibility lies with the FPA design. Therefore I propose the following organization for a successful design:

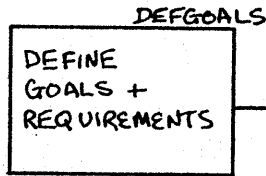
Resources:

- 1 supervisor/manager (ASAP)
- 2 strictly hardware designers (1 now, Arjun Kalra, 1 in two months)
- 1 hardware designer/ simulation & tools programmer (now, Don Hooper)
- 1 strictly simulation & tools programmer (now, Rich Braun)
- 2 diagnostic test programmers (2 in two months)
- (total= 1 manager, 6 engineers)
- 1 KL10 computer with terminals to the above people

The FPA group will begin by detailing a plan which will show approximately 63 task & check blocks. The plan will show for each of these the following information:

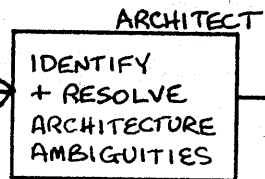
1. task description
2. predecessors
3. resources to do the task
4. resources to do the check
5. exit criteria (check OK)
6. quantity of items (subtasks) to be performed within the task
7. task duration by # of people and time
8. check latency (after task is done, time to wait for check)
9. check duration by # of people and time
10. loopback connectivity and % chance thereof

Attached please find a preliminary outline of the plan with examples.



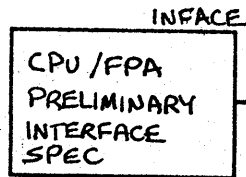
TASKS:

1. LIST OF INSNS
2. PERF. GOAL OF EACH
3. DESIGN SIZE
4. SCHEDULE/PLAN



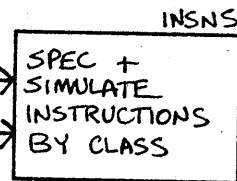
TASKS:

1. UNBIASED ROUNDING
2. GUARD BITS/ACCURACY
3. PACT
4. EXTEND



TASKS:

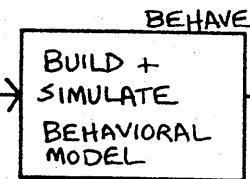
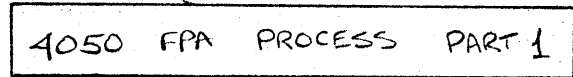
1. FPA/EBOX FLAGS
2. FPA/EBOX RESULTS
3. FPA/EBOX SYNC
4. FPA/IMBOX OPERANDS
5. FPA/IMBOX TAGS
6. FPA/IMBOX REFETCH
7. FPA/IMBOX INSN VALID



TASKS

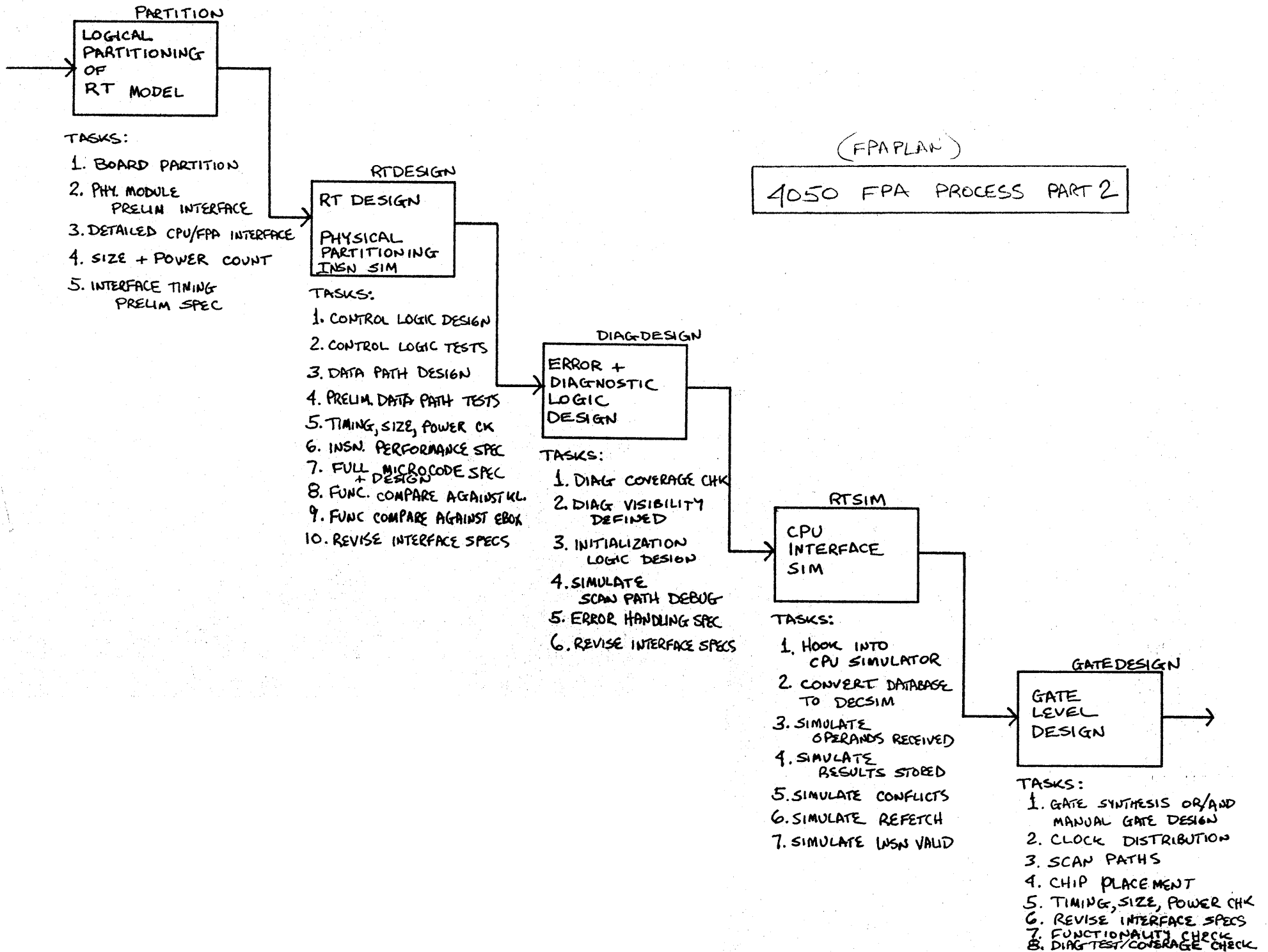
1. HIGH LEVEL LANGUAGE
2. SIMULATOR
3. SPEC INSNS
4. COMPARE AGAINST KL
5. COMPARE AGAINST EBOX

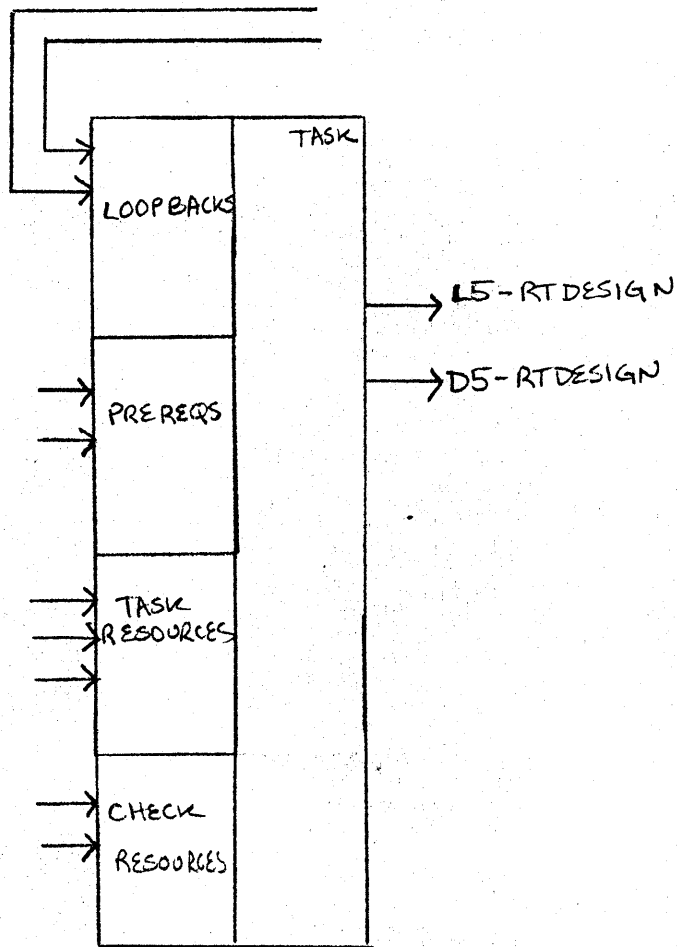
(FPAPLAN)



TASKS

1. FIND CRITICAL PATHS
2. DEFINE ALGORITHMS
3. INITIAL MICROCODE
4. REFINE MODEL FOR SIZE+SPEED
5. PRELIM. DATA PATH SPEC
6. FUNC. COMPARE AGAINST KL
7. FUNC. COMPARE AGAINST EBOX





COMMENTS :

- TASK → _____
- CHECKD → _____
- QUANTITY → _____
- TASK DUR → _____
- CHECK LATENCY → _____
- CHECK DUR → _____
- CHANGE OF LOOPBACK → _____

TASK-CHECK
BLOCK FORMAT

