REVISIONS			
SYM	DESCRIPTION	DATE	APPROVAL
A0	INITIAL RELEASE		
A1	INCORP EN 53227-XX-R1		
A2	INCORP EN 53227-XX-R2		
A3	INCORP EN 53227-XX-R3		
A4	INCORP EN 53227-XX-R4	1-6-73	

DEVICE ADDRESS DECODER

+5V RPI-6 1K
(75) $\overline{PB0}$ — $\overline{PS0}$ — 7486 — 836

+5V RPI-7 1K
(76) $\overline{PB1}$ — $\overline{PS1}$ — 7486 — 836

+5V RPI-8 1K
(77) $\overline{PB2}$ — $\overline{PS2}$ — 7486 — 836

+5V RPI-9
(78) $\overline{PB3}$ — $\overline{PS3}$ — 7486 — 836

+5V RPI-10 1K
(79) $\overline{PB4}$ — $\overline{PS4}$ — 7486 — 836

OUTPUT COMMAND DECODER

(C4) OUT, (C1) FCI, (C4) PLSE → 862 → TLD (C12,B10)

(C4)OUT, (C4)PLSE → 7408 → FC7 (B1) → 7408 → 846 → ENBL (B2,C2,A7)

OPF 7

(C3) INZX → 846

836 → DAXX (C2)

DAXX (D4,)

(A2) DB00 → EN (C15)
(A2) DB01 → EN (C11)
(A2) DB02 → EN (A8)
(A2) DB03 → EN (C7)
(B2) DB04 → EN (C3)
(B2) DB05 → EN (A3)
(B2) DB06 → EN (A4)
(B2) DB07 → EN (A5)

7475 latches

NBL (3) → 7408 → ECE (A10)

CAL → 836 → 25 → A19 CA
MC 1488L SEE NOTE NO. 4.

CDL (D7) → 836 → 25 → A18 CD
MC 1488L SEE NOTE NO 4.

CYL → 836 → 25 → A17 CY/SA/MB
MC 1488L SEE NOTE NO. 4.

NBL (3) → 846 → BRK (A10)

DATA BUS RECEIVERS

239 (B13) (39) $\overline{DB00}$ → 836 → DB00 (A3,A10 B11)
240 (C5) (40) $\overline{DB01}$ → 836 → DB01 (A3,A10 B11)
241 (C5) (41) $\overline{DB02}$ → 836 → DB02 (A3 A10 B11)
242 (B5) (42) $\overline{DB03}$ → 836 → DB03 (B3 A10 B11)
243 (B5) (45) $\overline{DB04}$ → 836 → DB04 (B3 A10, B11)
244 (B5) (46) $\overline{DB05}$ → 836 → DB05 (C3,A10 C11)
247 (B5) (47) $\overline{DB06}$ → 7404 → DB06 (C3,B10, C11)
248 (B5) (48) $\overline{DB07}$ → 7404 → DB07 (C10,C3 C11)

FUNCTION DECODER

280 (B0) $\overline{FB0}$ → 15
281 (B1) $\overline{FB1}$ → 14
282 (B2) $\overline{FB2}$ → 13
$\overline{DAXX}$ (B3) → 12

4 TO 10 DECODER (7442)

0 → 836 → 24 → FC7 (A3) (C4)
1 → N/C
2 → 836 → 24 → FC4 (C4)
3 → N/C
4 → N/C
5 → N/C
6 → 836 → 24 → FCI (D4,A3)
7 → 836 → 24 → FCO (D4)
8 → N/C
9 → N/C

CONTROL BUS INPUTS

(57) $\overline{EXEC}$ → 7404 → EXEC (C4)
(58) $\overline{IN}$ → 7404 → IN (D4)
(62) $\overline{OUT}$ → 7404 → OUT (A3,A4)
(71) $\overline{PLSE}$ → 836 → (A3,A4,D4,B7) PLSE
(69) $\overline{RST}$ → 836 → RST (C4)
(72) $\overline{ECHO}$ → 836 → ECHO (C7)
(67) $\overline{IAR}$ → 836 → IAR (B7)
(61) $\overline{IOCL}$ → 7404 → IOCL (B8)
(63) $\overline{CLK}$ → 836 → CLK (A11,A15)

SELECT COMMAND DECODER

(C4) EXEC, (B2) FC4 → 846 → 836 → INZX (C7,D7,C15,D15)
(C4) RST → 836 → $\overline{INZX}$ (A3,D7,C11,C12)

INPUT COMMAND DECODER

(B1) FC7 → 7408 → INF7 (D14)
(C4) IN, (C1) FCO, (C4) PLSE → 7408 → 846 → 836 → 836 → INPO (C7,D15)
RP2-4 → +5V.
INFO (D14)

SENSE ENCODER

(D14) RCV, (C1) FCO → 846
(C11) TNX, (C1) FCI → 846 → 836 → 858 → $\overline{SER}$ (64)
(B3) DAXX
264 ?

NOV 1ST 1974
K. Price D/35

NOTES: UNLESS O
1. ALL RESIS
2. REF. DESIG
3. SEE DWG. N
4. VCC & GRD. GRD PIN 7. F

INECTIONS AS FOLLOWS: +15 PIN 14, -15 PIN 1, MC1488L ONLY.
53227-0X FOR ASSY. DWG.
IONS NOT USED C15, C16.
S ARE 1/4 WATT, 5%.
RWISE SPECIFIED:

DASH NO.	NEXT ASSY	QTY REQD

QTY	ITEM NO.	PART NO.	DESCRIPTION	MAT'L., VENDOR

LIST OF MATERIAL

DR. [illegible] BUCH
CHK
DSG
ENGR
PROJ
REL

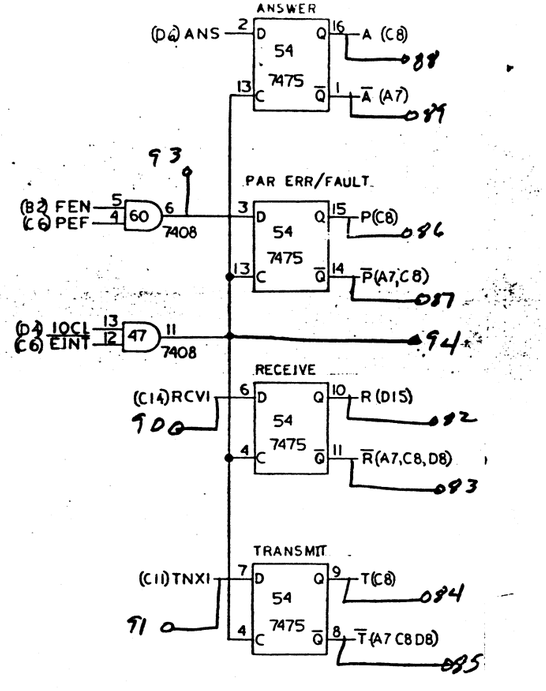
TOLERANCES
.X = ±.1
.XX = ±.03
.XXX = ±.010
ALL HOLE DIAMETERS ARE AFTER PLATING

COMPUTER AUTOMATION INC.
695 WEST SIXTEENTH STREET, NEWPORT BEACH, CALIFORNIA 92660

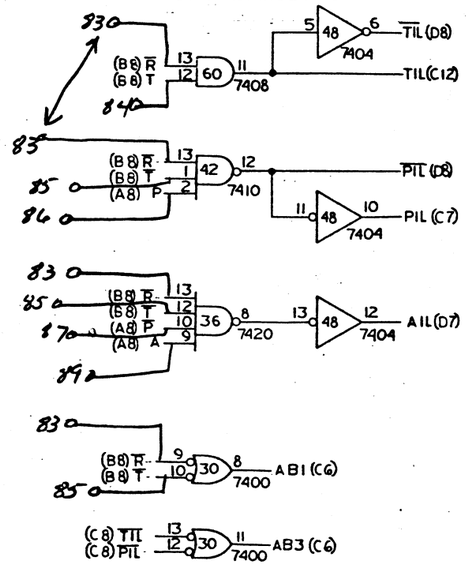
LOGIC DIAGRAM
ASYNCHRONOUS DATA SET CONTROLLER, ALPHA

SIZE D | 00-53227-XX | REV A4
SCALE = | SHT. 1 OF 4

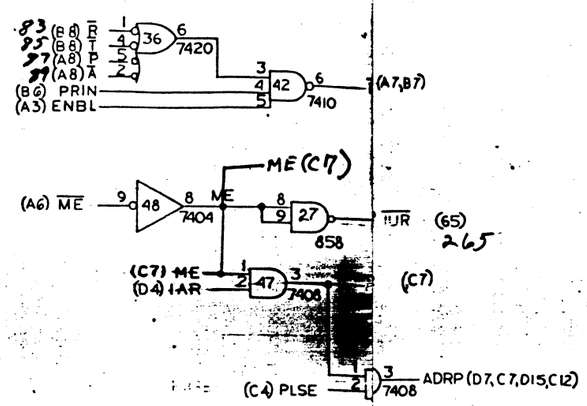
INTERRUPT REQUEST FLIP-FLOPS



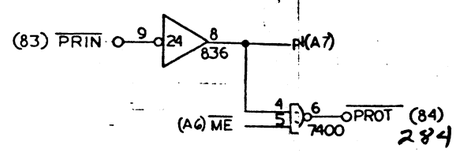
INTERRUPT DECODING



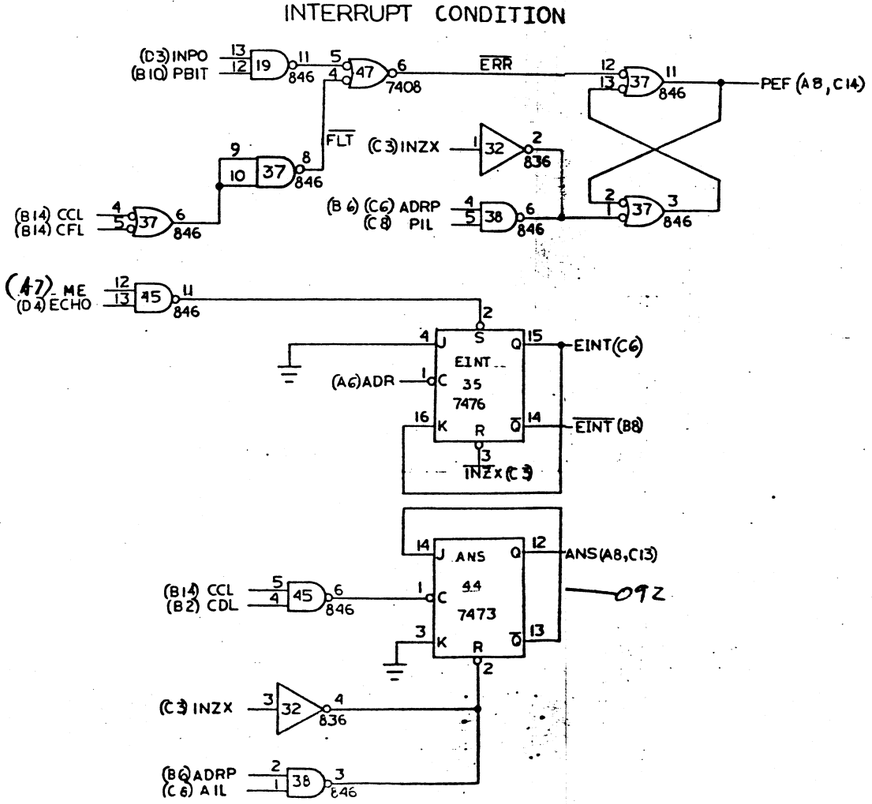
INTERRUPT REQUEST



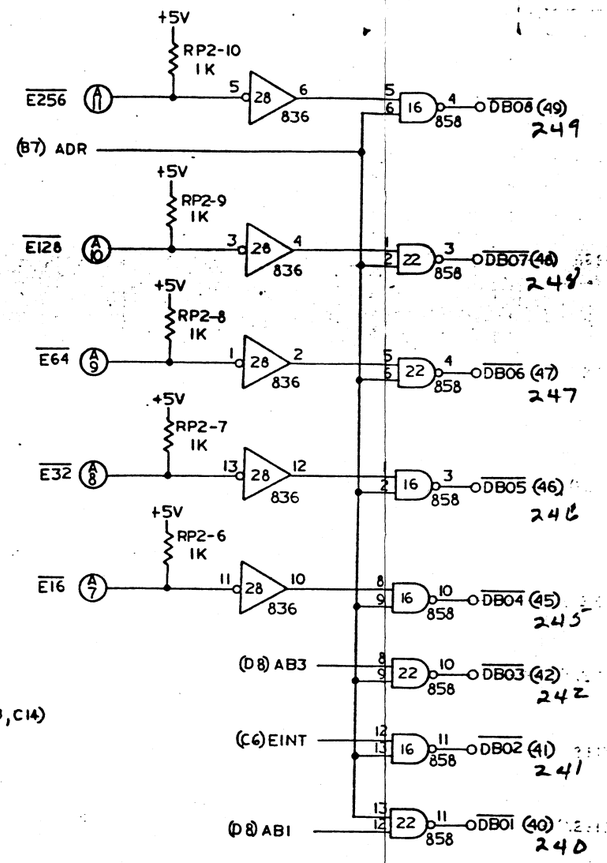
PRIORITY LOGIC



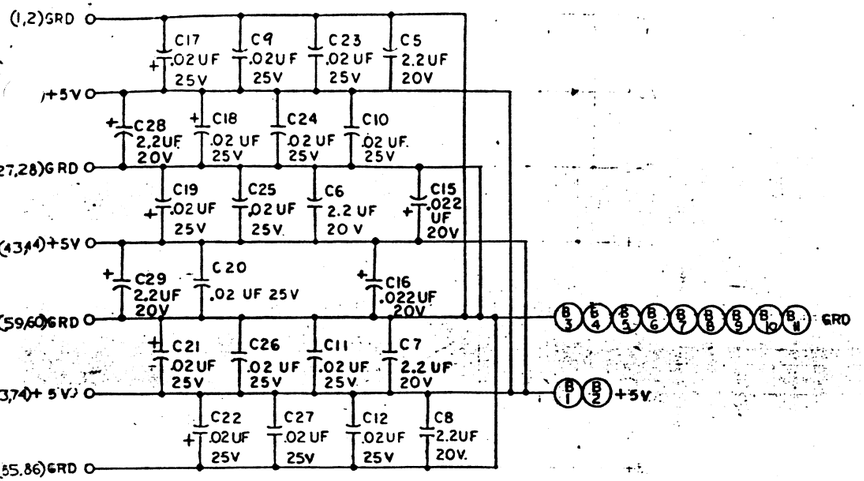
INTERRUPT CONDITION



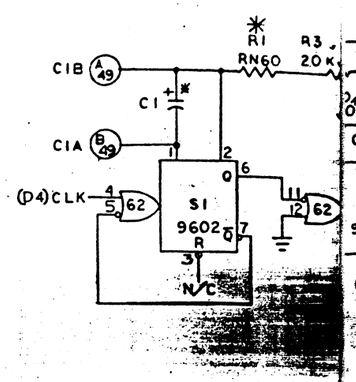
INTERRUPT ADDRESS GENERATOR



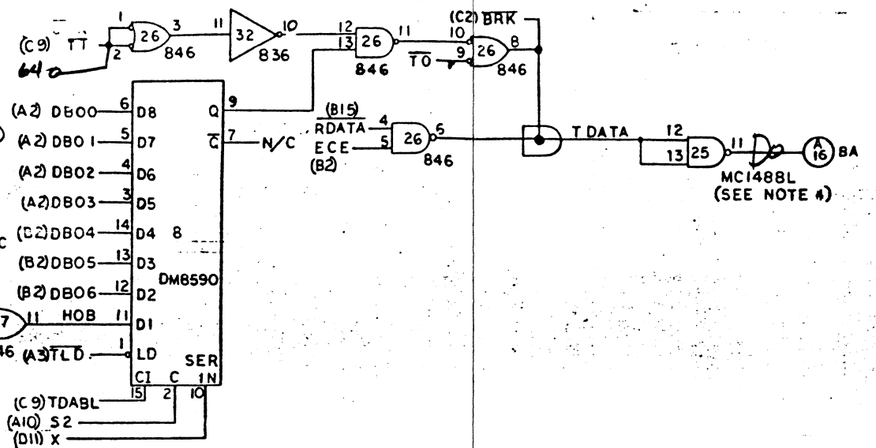
	SIZE		REV
	D	00-52227-XX	A4
	SCALE=	SHT. 2 OF 4	



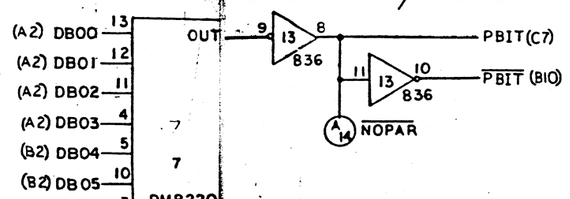
TRANSMIT SHIFT GENERATOR



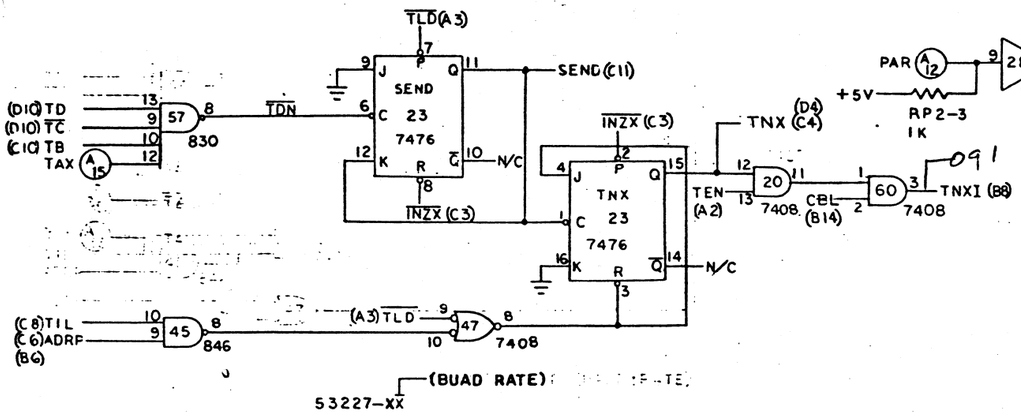
TRANSMIT DATA REGISTER LOGIC



PARITY GENERATOR/CHECKER



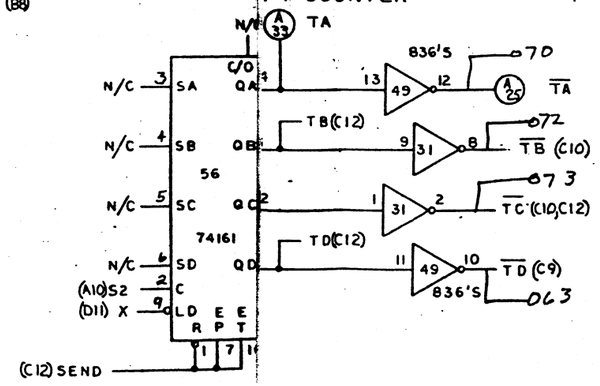
TRANSMIT CONTROL IN



BAUD RATE		TABULATION			
DIGIT NO.	BAUD RATE	VALUE C1	VALUE C3	VALUE R1	VALUE R2
-11	110	1.5 UF	.68 UF	14.3K	14.3K
-12	150	1.5 UF	.68 UF	14.3K	14.3K
-13	300	1.5 UF	.68 UF	4.99K	4.99K
-14	600	.33 UF	.15 UF	14.3K	14.3K
-15	1200	.15 UF	.082 UF	4.99K	4.99K
-16	1800	.15 UF	.082 UF	4.99K	4.99K
-17	2400	.15 UF	.082 UF	4.99K	4.99K
-18	4800	.039 UF	.039 UF	4.99K	4.99K
-19	9600	.015 UF	.015 UF	4.99K	4.99K

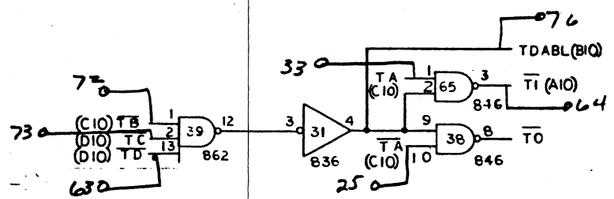
+5V
RP2-2
1K
X (A13, B10, D11, B16)

TRANSMIT SHIFT COUNTER



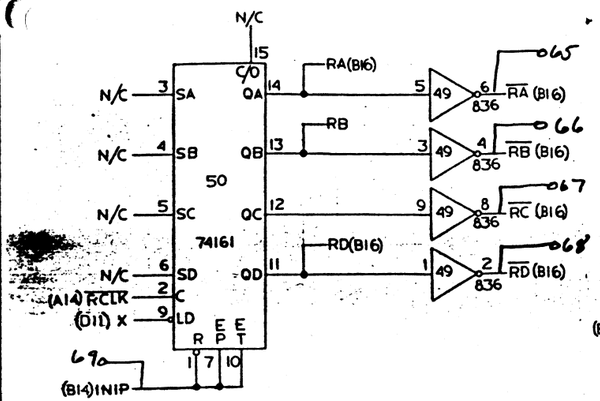
* TEST
SEE TABULATION BLOCK (THIS SHT)

P/O TRANSMIT CONTROL

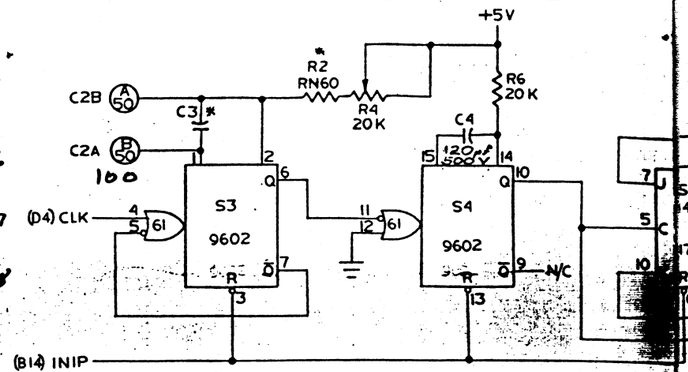


	SIZE	CO-52227-XX	REV
	D		A4
	SCALE=	SHT. 3 OF 4	

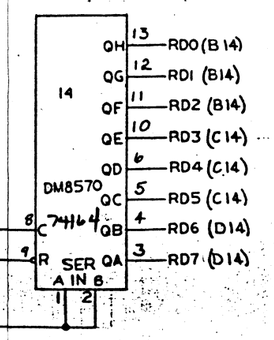
RECEIVE SHIFT COUNTER



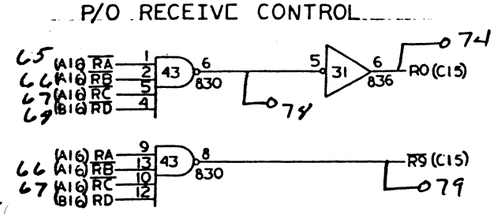
RECEIVE SHIFT GENERATOR



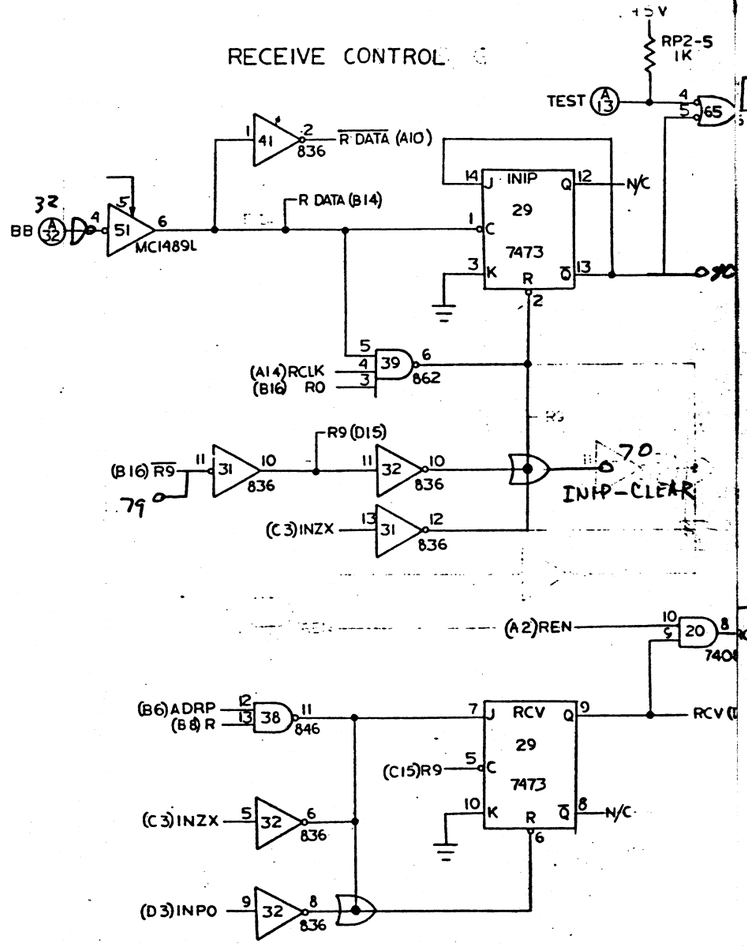
RECEIVE DATA REGISTER.



P/O RECEIVE CONTROL

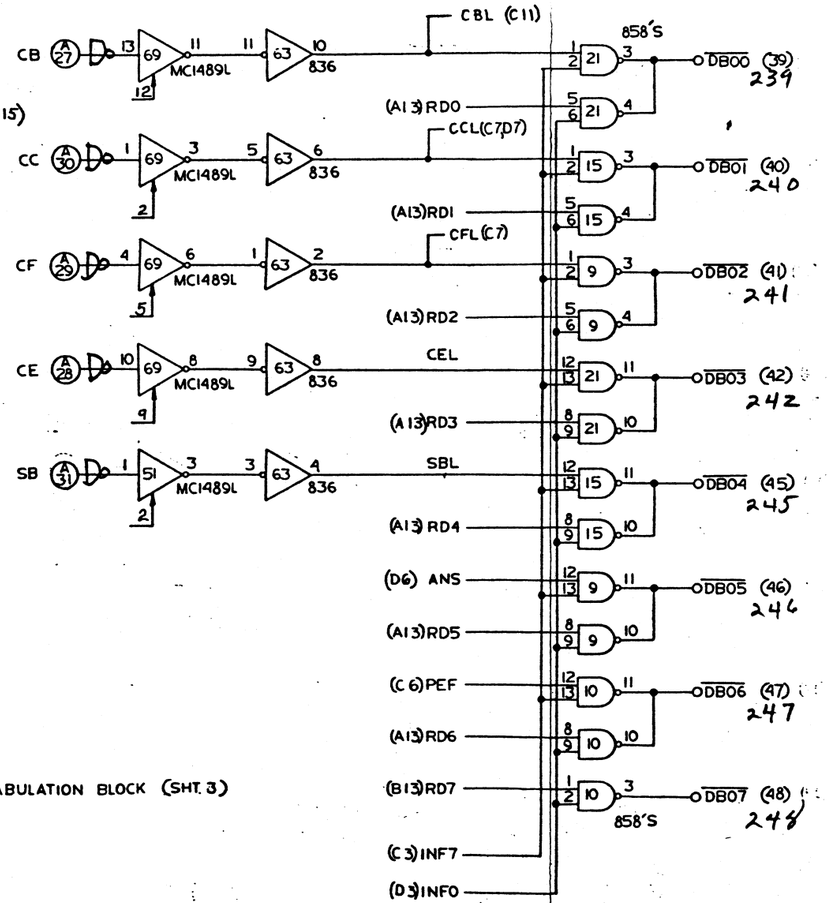


RECEIVE CONTROL



* TEST,
SEE TABULATION BLOCK (SHT. 3)

DATA BUS DRIVERS



	SIZE		REV
	D	00-52227-XX	A4
		SCALE =	SHT. 4 OF 4