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PRODUCT NUMBER / NOMENCLATURE

HASP MULTI-LEAVING PROTOCOL

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1.0 SCOPE

The purpose of this document is to define a communication protocol which is known as MULTI-LEAVING. This protocol is used on IBM 360 and 370's when operating under HASP* and when computer-to-computer communication is desired. The MULTI-LEAVING protocol provides the capability to transmit a variable number of data streams between two computers in a two-way alternate, synchronous transmission mode using either ASCII or EBCDIC code. Line speeds up to 50,000 bps are attainable with the MULTI-LEAVING protocol, and transmission can be in either a transparent or a non-transparent mode.

* HASP operates on the OS/MFT/MVT operating system.

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2.0 APPLICABLE DOCUMENTS

The following documents provide a background which is useful in understanding the contents of this document:

IBM No. 6A27-3004-2, General Information - Binary Synchronous Communication

IBM No. C33-4001-4, IBM System/360 Model 20 Input/Output Control System for the Binary Synchronous Communications Adapter

IBM No. 360D-05.1.014 HASP II Manual

COPE.3X Simulation of IBM MULTI-LEAVING Remote Terminal {HASP}

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3.0 GENERAL DESCRIPTION

3.1 MULTI-LEAVING Protocol Definitions

The MULTI-LEAVING protocol consists of the bi-directional transmission of informational blocks between two or more computers in a transparent or a non-transparent mode. The informational blocks are defined to be the following types of blocks:

1. Control blocks
2. Data blocks

Control blocks contain control characters, control bytes, and data records. {Section 4.3} Control characters are defined in Section 4.1. Data records are character strings and their associated character string control bytes, SCB {Section 4.2.5}.

Each data record in the data block is associated with a specific peripheral device. In order to facilitate identification, a stream number and a device type are assigned to the data record via a record control byte, RCB {Section 4.2.3}. Each record control byte has a sub-record control byte, SRCB, associated with it to provide additional information about the data record {Section 4.2.4}.

A data block may consist of several data records, all of which may or may not be from the same device. In order to control the flow of data from or to any particular device, a function control sequence, FCS, is added to each data block {Section 4.2.2}

To facilitate error detection, a block control byte, BCB, is added to each data block {Section 4.2.1}

3.2 MULTI-LEAVING Protocol Operations Description

The following narrative is a general description of how the MULTI-LEAVING protocol operates:

The terminal software is loaded {Section 6.1} and the communication line is initialized {Section 6.2}. After the SIGN-ON command is transmitted, the terminal and the central processor transmit idle blocks until a function is desired {Section 6.3}.

When a function other than a console message or console command {Section 4.4.1} is desired, the processor desiring to initiate the function transmits a request to initiate a function transmission RCB {Section 4.2.3}. The processor that receives the request to initiate a function transmission RCB, transmits a permission to initiate a function transmission RCB if the

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data from the requesting processor can be processed. If the data cannot be processed, or if the function is now in process, the request to initiate a function transmission RCB is ignored.

When a permission to initiate a function transmission RCB is received, the requesting processor begins transmitting data blocks to the other processor. Data blocks can be transmitted until an EOF {End of File} is encountered, at which time a zero length record is transmitted {Section 4.4.2}. In order to transmit more data blocks, on the same device stream, the request to initiate a function transmission RCB sequence of events must be initiated again. If a request to initiate a function transmission RCB is not received before data blocks are received, the data blocks are ignored.

Data blocks are transmitted one block at a time. Before another block can be transmitted, the receiving processor must transmit a positive response. A positive response is an acknowledge control block or a data block.

Console functions {operator messages and operator commands} do not have to follow the request to initiate - permission to initiate sequence. A console function may be initiated anytime that the wait-a-bit bit in the FCS is not set.

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4.0 MULTI-LEAVING BLOCK DESCRIPTIONS

4.1 Control Blocks

Four types of control blocks are used in the MULTI-LEAVING protocol. These control blocks are:

1. Acknowledge block
2. Negative block
3. Enquiry block
4. Idle block

A description of the blocks and the block usage are contained in the following subsections.

4.1.1 Acknowledge Block {ACK}

The acknowledge block {ACK} consists of the following control characters:

SYN, SYN, SYN, DLE, ACK0, PAD

where SYN = synchronization control character

DLE = data link escape control character

ACK0 = affirmative acknowledgement control character

PAD = pad control character {all 1 bits}

The ACK block is transmitted to indicate that the previous block was received without error and no data is available for transmission.

4.1.2 Negative Acknowledge Block {NAK}

The negative acknowledge block {NAK} consists of the following control characters:

SYN, SYN, SYN, NAK, PAD

where SYN = synchronization control character

NAK = negative acknowledgement control character

PAD = pad control character {all 1 bits}

The NAK block is transmitted to indicate that the previous block was received in error and retransmission is necessary.

NOTE: A NAK block is never transmitted as a response to a NAK block.

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4.1.3 Enquiry Block

The enquiry block consists of the following control characters:

SYN, SYN, SYN, SOH, ENQ, PAD
 where SYN = synchronization control character
 SOH = start of header control character
 ENQ = enquiry control character
 PAD = pad control character {all 1 bits}

The enquiry block is transmitted to establish communications with HASP at the central processor. The enquiry block is only used at system loading time.

4.1.4 Idle Block

The idle block is an ACK block which is used to maintain communications and avoid an unprogrammed time-out when neither processor has any data to transmit. The idle block is transmitted at least every two seconds.

4.2 Data Block Control Bytes

The control bytes, that are referenced in Section 3 and are part of each data block, are described in the following subsections.

4.2.1 Block Control Byte {BCB}

The block control byte bit representation is as follows:

Bit no. 0 7
 0XXXCCCC
 where: 0 = 1 {must always be on}
 XXX = 000 = Normal block
 = 001 = Ignore sequence count
 = 010 = Reset expected block sequence to CCCC*
 = 011 or 100 = Reserved
 = 101 or 110 = Available for user modification
 = 111 = Reserved for future expansion
 CCCC = Modulo 16 block sequence count

* Reset block count on first transmission {SIGN-ON, etc.}

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4.2.2 Function Control Sequence {FCS}

The function control sequence bit representation is as follows:

Bit No. 0 78 F
 0SRRABCD0TRRWXYZ

where: 0 = 1 {Must always be on}
 S = 1 = Suspend all stream transmissions {Wait-A-Bit}
 = 0 = Normal state

Note - for the following bits
 - a bit = 1 = continue function transmission
 - a bit = 0 = suspend function transmission
 T = Remote Console stream identifier
 R = Reserved for future expansion
 ABCDWXYZ = Various function stream identifiers*

* These stream identifiers are oriented to the recipient.
 For example: if the central processor sends an FCS to the terminal, then the ABCDWXYZ bits represent card reader function stream identifiers. The card reader function stream identifiers are assigned in the following order:

Card Reader No. 1 = A
 No. 2 = B
 No. 3 = C
 No. 4 = D
 No. 5 = W
 No. 6 = X
 No. 7 = Y
 No. 8 = Z

If the terminal sends an FCS to the central processor, then the ABCDWXYZ bits represent punch and printer function stream identifiers. The card punch and line printer function stream identifiers are assigned in the following order:

Printer No. 1 = A = Punch No. 8
 Printer No. 2 = B = Punch No. 7
 Printer No. 3 = C = Punch No. 6
 Printer No. 4 = D = Punch No. 5
 Printer No. 5 = W = Punch No. 4
 Printer No. 6 = X = Punch No. 3
 Printer No. 7 = Y = Punch No. 2
 Printer No. 8 = Z = Punch No. 1

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4.2.3 Record Control Byte {RCB}

The record control byte bit representation is as follows:

Bit No. 0 7
 0IIITTTT

where: 0 = 0 = End of transmission block {IIITTTT=0}
 = 1 = All other RCB's
 III = Stream identifier if TTTT ≠ 0
 III = Control information if TTTT = 0 {control record}
 = 000 = Reserved for future expansion
 = 001 = Request to initiate a function transmission*
 = 010 = Permission to initiate a function transmission*
 = 011 and 100 = Reserved
 = 101 = Available for local modification
 = 110 = Bad BCB on last block received
 = 111 = General Control Record {type indicated
 in SRCB}
 TTTT = Record type identifier
 = 0000 = Control record
 = 0001 = Operator message display request
 = 0010 = Operator command
 = 0011 = Normal input record
 = 0100 = Print record
 = 0101 = Punch record
 = 0110 = Data set record
 = 0111 = Terminal message routing request
 = 1000-1100 = Reserved for future expansion
 = 1101-1111 = Available for local modification

* The RCB for these functions is contained in the SRCB.

4.2.4 Sub-Record Control Byte {SRCB}

The sub-record control byte bit representation is as follows:

Bit = 0 7
 0SSSSSSS

where: 0 = 1 {Must always be on}
 SSSSSSS = additional record information - dependant
 on record type

If record type is General Control Record:

SSSSSSS=1000001 = Initial terminal SIGN-ON

Other bit representations for General Control Records
 have been assigned but are not now implemented by IBM
 {APPENDIX B}.

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If record type is Request or Permission to initiate a function transmission:

SSSSSSS = Stream identifier and record type identifier as described in RCB.

If record type is Bad BCB on last block received:

SSSSSSS = expect block count modulo 16-right justified.

If record type is Print record:

SSSSSSS = MCCCCCC

where: M = 0 = Normal carriage control
 = 1 = Reserved for future use
 CCCCCC = Carriage control information
 = 1000NN = Space immediately NN spaces
 = 11NNNN = Skip immediately to channel NNNN
 = 0000NN = Space NN lines after print
 = 01NNNN = Skip to channel NNNN after print
 = 000000 = Suppress space

If record type is Punch record:

SSSSSSS = MMBRRSS

where: SS = Punch stacker selection information
 B = 0 = Normal EBCDIC card image
 = 1 = Column binary card image {not now supported}
 MM = 00 = SCB count units = 1
 = 01 = SCB count units = 2 {not now supported}
 = 10 = SCB count units = 4 {not now supported}
 = 11 = Reserved
 RR = Reserved for future expansion

If record type is Input record:

SSSSSSS = MMBRRRR

where: MM = 00 = SCB count units = 1
 = 01 = SCB count units = 2 {not now supported}
 = 10 = SCB count units = 4 {not now supported}
 = 11 = Reserved
 B = 0 = Normal EBCDIC card image
 = 1 = Column binary card image {not now supported}
 RRRR = Reserved for future expansion

If record type is Message routine record:

SSSSSSS = T

where: T = Remote system number {1 ≤ T ≤ 99}
 = Remote system group {100 ≤ T ≤ 127}
 = 0 = Broadcast to all remote systems

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4.2.5 String Control Byte {SCB}

The string control byte bit representation is as follows:

Bit No. 0 7
 0KTCCCCC

where: 0 = 0 = End of record {KTCCCCC=0}
 = 1 = All other SCB's
 K = 0 = Duplicate character string
 T = 0 = Duplicate character is a blank
 = 1 = Duplicate character is a non-blank
 {character follows SCB}
 CCCC = Duplication count
 K = 1 = Non-duplicate character string
 TCCCCC = Character string length

If KTCCCCC = 0 and 0=1, SCB indicates record is continued in the next transmission block.

4.3 Data Block Description

Data blocks consist of data records, the control bytes described in the previous sub-sections and the following test control characters:

SYN = synchronization control character
 DLE = data link escape control character
 SOH = start of header control character - used only if non-transparent mode
 STX = start of test control character
 ETB = end of transmission block control character
 CRC-16 = cyclic redundancy checking control characters {2 bytes}
 PAD = pad control character {all 1 bits}

A typical data transmission block is shown in Figure 4.3.1.

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FIGURE 4.3.1 TYPICAL MULTI-LEAVING DATA TRANSMISSION BLOCK

SYN	
SYN	- Synchronization Characters
SYN	
DLE	- BSC Leader {SOH if no transparency feature}
STX	- BSC START-OF-TEXT
BCB	- Block Control Byte
FCS	- Function Control Sequence {2 bytes}
RCB	- Record Control Byte for record 1
SRCB	- Sub-Record Control Byte for record 1
SCB	- String Control Byte for record 1
DATA	- Character String
SCB	- String Control Byte for record 1
DATA	- Character String
SCB=0	- Terminating SCB for record 1
RCB	- RCB for record 2
SRCB	- SRCB for record 2
SCB	- SCB for record 2
DATA	- Character String
SCB=0	- Terminating SCB for record 2
RCB=0	- Transmission Block Terminator
DLE	- BSC Leader - {SYN if no transparency feature}
ETB	- BSC Ending Sequence
CRC-16	- Cyclic Redundancy Check Characters {2 bytes}
PAD	- All 1 Bits

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The following examples are representative of the various record types. {It is assumed that all of the examples start with SYN, SYN, SYN, DLE, STX, or SOH and end with DLE, or SYN, ETB, CRC-16, PAD. DLE is used if transmission is in transparent mode.}

Example No. 1 - A request to initiate a transmission function for printer no. 2

BCB = 1000XXXX - Normal block - count = XXXX
 FCS = 1000YYYY, 1100YYYY - Normal state, identifiers YYYY, YYYY
 SRCB = 10100100 - Request to initiate a function transmission
 RCB = 0 - Transmission block terminator

Example No. 2 - A permission to initiate a transmission function for card punch no. 1.

BCB
 FCS
 RCB = 10100000 - Permission to initiate a function transmission
 SRCB = 10010101 - Card punch stream no. 1
 RCB = 0 - Transmission block terminator

Example No. 3 - A card reader record from card reader no. 2.

Card record =
 Column No. 1 5 10 15 23 29
 LABEL AAAAAA EXAMPLE

BCB
 FCS
 RCB = 10100011 - Card reader stream no. 2
 SRCB = 10000000 - SCB count unit no. 1, EBCDIC card image
 SCB = 11000101 - Nonduplicate character string-length 5 characters
 L - Data characters
 A
 B
 E
 L
 SCB = 10000100 - Duplicate character string, blank - length 4 characters
 SCB = 10100110 - Duplicate character string, non-blank - length 6 characters

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A	- Duplicated character
SCB = 100001111	- Duplicate character string, blank - length 7 characters
SCB = 110001111	- Nonduplicate character string - length 7 characters
E	- Data characters
X	
A	
M	
P	
L	
E	
SCB = 100111111	- Duplicate character string, blank - length 31 characters
SCB = 10010100	- Duplicate character string, blank - length 20 characters
SCB = 0	- End of record
RCB = 0	- Transmission block terminator

4.4 Short Block Descriptions

There are several blocks that appear to be data blocks but are really special case data blocks. These short blocks are:

- Operator console blocks
- End of file blocks
- FCS change blocks
- SIGN-ON blocks
- BCB error blocks

The SIGN-ON blocks are described in the Terminal Start-Up Section {6.0}, and BCB error blocks are described in the Error Conditions Section {5.0}.

4.4.1 Operator Console Blocks

Blocks which contain operator console messages or commands are special in that no additional records are packed into the data block following a console record.

Example: Assume a card reader is reading cards and the operator keys in a console command to the central processor. The data block contains the data cards read up to the point that the console record is available. From that point on, the data block looks as follows:

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RCB = 10010010	- Operator command - stream no. 1
SRCB = 10000000	
SCB	- SCB describing console character string
DATA	- Data may be divided into many character strings
SCB = 00000000	- End of record
RCB = 00000000	- Transmission block terminator
DLE or SYN	- DLE if transparent mode
ETB	- End of block
CRC-16	- Cyclic redundancy check
PAD	- All 1 bits

A request to initiate a transmission function is not required to transmit console records. The only restriction is that the Wait-A-Bit is not set in the FCS.

4.4.2 End of File Blocks {EOF}

Blocks which contain end of files are special in that no additional records from the same device stream are packed into the data block following an EOF. Data blocks which are terminated by an EOF contain a final record which is as follows: for card reader stream no. 1.

RCB = 10010011	- Card reader stream no. 1
SRCB = 10000000	- SCB count units =1, EBCDIC card images
SCB = 00000000	- EOF
RCB = 00000000	- Transmission block terminator
DLE or SYN	- DLE if transparent mode
CRC-16	
PAD	

In order to transmit more records for a device stream that contained an EOF, the request to initiate a function transmission must be transmitted again {Section 3.2}. If another device stream contains data for transmission, if the device stream has permission available in the data block, then the last RCB in the above example would be a device stream RCB followed by data instead of a transmission block terminator.

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4.4.3 FCS Change Blocks

The FCS change block is transmitted when the status of one or more of the streams has changed, and there is no data to transmit. The FCS change block is as follows:

SYN
SYN
SYN
DLE or SOH - DLE if transparent mode
STX
BCB
FCS - Changed FCS
RCB = 0 - Transmission block terminator
DLE or SYN - DLE if transparent mode
ETB
CRC-16
PAD

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5.0 ERROR CONDITIONS

The error conditions that can occur are dependent in large part on what hardware is used by a terminal. A few error conditions that are not hardware dependent are the following:

- CRC-16 Error
- Illegal Block Make-up
- Unknown response
- Time-out
- BCB Error

5.1 CRC-16 Error {Cyclic Redundancy Checking}

Cyclic redundancy checking is a type of error checking which is employed to help insure error free data transmission. A cyclic redundancy check is a division performed by both the transmitting and receiving processors using the numeric binary value of the message as a dividend and a constant as a divisor. The quotient is discarded, and the remainder serves as a check character. The receiving processor compares the transmitted remainder to its computed remainder. If the two remainders are equal, there is no error. If the two remainders are not equal, an error has occurred. CRC occurs only on data blocks.

If a CRC-16 error occurs, the receiving processor transmits a NAK block {Section 4.1.2} to the transmitting processor which informs the transmitting processor that a retransmission of the last block is required. If the retransmitted block is correct, the processing continues.

5.2 Illegal Block Make-Up Error

A data block must end with an ETB control character. If the data block does not end with an ETB, then an illegal block make-up error occurs. The required error recovery procedure for this error is to have the receiving processor transmit a NAK block {Section 4.1.2} to the other processor. The NAK block informs the other processor that a retransmission of the last block is required. If the retransmitted block is correct, the processing continues.

5.3 Unknown Response Error

An unknown response error occurs when the response received from the transmitting processor is not one of the following:

1. A data block beginning with DLE, STX control characters - transparent mode.

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2. A data block beginning with SOH,STX control characters - non-transparent mode
3. An ACK block {Section 4.1.1}.
4. A NAK block {Section 4.1.2}

If an unknown response error occurs, the receiving processor transmits a NAK block to the other processor which informs the other processor that a retransmission of the last block is required. If the retransmitted block is correct, the processing continues.

5.4 Time-out Error

From the moment communications are established until the last block is transmitted after SIGN-OFF, transmission blocks are expected at one processor or the other at least every two seconds after a block was transmitted by the receiving processor, or time-out error occurs.

If a time-out error occurs, the receiving processor transmits a NAK block to the other processor which informs the other processor that a retransmission of the last block is required. If the retransmitted block is correct, the processing continues.

5.5 BCB Error

Every data block contains a BCB byte {Section 4.2.1}, and in each BCB byte is a block sequence count. The data blocks are transmitted in sequentially ascending order unless an ignore or reset BCB byte is transmitted. If the block sequence count in the data block is not equal to the block sequence count expected by the receiving processor, a BCB error occurs.

If a BCB error occurs and the block sequence count is a duplicate of a block sequence count previously received {expected block sequence count minus received block sequence count ≤ 2 }, the data block is ignored and processing continues as if an FCS change block or an ACK block was received.

If a BCB error occurs and the block sequence count is not a duplicate block sequence count as described in the previous paragraph, a BCB error block is transmitted from the terminal to the central processor. The BCB error block informs the other processor that a block sequence count error occurred, and the processor is to back up the file to the missing block or is to transmit a reset BCB byte. The format of the BCB error block is:

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SYN
SYN
SYN
DLE or SOH - DLE if transparent mode
STX
BCB = 1001XXXX - ignore sequence checking, XXXX = received
block sequence count
FCS
RCB = 11100000 - Bad BCB on last block
SRCB = 1000YYYY - YYYY = expected block sequence count
SCB = 00000000 - End of Record
RCB = 00000000 - Transmission block terminator
DLE or SYN - DLE if transparent mode
ETX
CRC-16
PAD

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6.0 TERMINAL START-UP AND TERMINATION

Terminal start-up is accomplished via a three step process.

1. Terminal initialization
2. Communication line initialization
3. SIGN-ON

6.1 Terminal Initialization

The terminal software is loaded and put into execution. The loading can be via paper tape, cards, mag. tape, or mass storage depending on the terminal hardware. The initialization processor establishes the I/O buffers and other necessary parameters. After all the buffers are set, a card is read from the card reader. If the card is a blank card, the default SIGN-ON parameters are used.* If the card is a /* SIGNON card {Section 6.3}, the parameters on the /* SIGNON card are used instead of the default parameters. If the card is neither a blank card or a /* SIGNON, an error has occurred. The recovery from this error is to be defined by the implementor of the MULTI-LEAVING protocol.

* Default SIGN-ON parameters are assembled into the terminal software.

6.2 Communication Line Initialization

After the terminal is initialized, the communication line is initialized. The line is initialized by enabling the communication adapter and the data set. Communications are then established with HASP via the following procedure:

1. An Enquiry block {Section 4.1.3} is transmitted to the central processor from the terminal.
2. If the central processor can process the terminal data, an ACK block is transmitted from the central processor to the terminal. If the central processor cannot process the terminal data, the Enquiry block is ignored.
3. If an ACK block is received by the terminal, a buffer is constructed and the SIGN-ON record is queued for transmission to HASP {the central processor}.
4. If I/O errors occur or an ACK block is not received, step 1 is repeated.
5. After the SIGN-ON record is transmitted and a response is received, the terminal is ready to do normal processing.

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6.3 SIGN-ON Block

A SIGN-ON block is transmitted to the central processor to inform the central processor that a terminal is signing on. The data portion of the SIGN-ON block is the SIGN-ON record. The format of the SIGN-ON card is:

```

column 1          16          25
      /*SIGNON    REMOTEnn    password
  
```

where nn = a one or two digit decimal number which correlates this remote terminal with information about it in the central computer.

password = blank unless a password is required.

The SIGN-ON block format is:

```

SYN
SYN
SYN
DLE or SOH          - DLE if transparent mode
STX
BCB = 1010XXXX      - reset count to XXXX
FCS
RCB = 11110000      - General Control Record
SRCB= 11000001      - Initial SIGN-ON
SIGN-ON Record
RCB = 00000000      - Transmission block terminator
DLE or SYN          - DLE if transparent mode
ETX
CRC-16
PAD
  
```

6.4 SIGN-OFF Block

Terminal processing termination is accomplished via the /* SIGNOFF card. The /* SIGNOFF card when transmitted to the central processor as a record in a data block has the same effect as an EOF block [Section 4.4.2]. In addition to terminating the input stream, the /* SIGNOFF record causes communications with the terminal to be terminated after completion of the current device streams. The /* SIGNOFF card format is:

```

column 1
      /*SIGNOFF
  
```


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7.0 MULTI-LEAVING COMMUNICATIONS

This section contains examples which show normal MULTI-LEAVING communications and error conditions communications.

Example no. 1 - Terminal number 2 is signed on, and two jobs are transmitted to the central processor. The last card transmitted is a /* SIGNOFF card.

Terminal transmits	Central Processor Transmits
Enquiry Block	ACK block
SIGN-ON block {Terminal 2}	ACK block
Request to initiate card reader function transmission	Permission to initiate card reader function transmission
Data block no. 1 from card reader {Job no. 1}	ACK block
⋮	⋮
Data block no. X from card reader {Job no. 1}	ACK block
EOF block from card reader {Job no. 1} {May contain data prior to EOF}	ACK block
Request to initiate card reader function transmission	Permission to initiate card reader function transmission
Data block no. 1 from card reader {Job no. 2}	ACK block
⋮	⋮
Data block no. N from card reader {Job no. 2}	ACK block
Data block that contains /* SIGNOFF record	Disconnect

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Example no. 2 - Terminal number 1 is signed on, and one job is transmitted to the central processor and two job print streams {only printer} are transmitted to the terminal {Print stream is from previous jobs and is available at SIGN-ON time}. The last input record transmitted is a /* SIGNOFF card. Assume each print stream is 3 blocks long and the job input stream is 4 blocks long.

<u>Terminal transmits</u>	<u>Central Processor Transmits</u>
Enquiry block	ACK block
SIGN-ON block {Terminal 1}	Request to initiate print function transmission
Permission to initiate print function transmission	Print data block no. 1 {Job no. 1}
Request to initiate card reader function transmission	Permission to initiate card reader function transmission
Card reader data block no. 1	Print data block no. 2 {Job no. 1}
Card reader data block no. 2	Print data block no. 3 {Job no. 1} - EOF block
Card reader data block no. 3	Request to initiate print function transmission
Permission to initiate print function transmission	Print data block no. 1 {Job no. 2}
Card reader data block no. 4 {SIGN-OFF card}	Print data block no. 2 {Job no. 2}

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Example no. 3 - Terminal is already signed on and idling. A job is read in and a punch and print stream are transmitted. After completion of the transmissions, return to an idle state. Card reader stream is 4 blocks long, and print and punch streams total 3 blocks long.

<u>Terminal Transmits</u>	<u>Central Processor Transmits</u>
ACK block {2 second interval}	ACK block {2 second interval}
Request to initiate card reader function transmission	Permission to initiate card reader function transmission
Card reader data block no. 1	Request to initiate printer function transmission
Permission to initiate printer function	Request to initiate punch function transmission
Permission to initiate punch function transmission	Data block no. 1 {Printer and punch}
Card reader data block no. 2	Data block no. 2 {Print EOF block and partial punch block}
Card reader data block no. 3	Data block no. 3 {Punch EOF block}
Card reader data block no. 4	ACK block
ACK block	ACK block {2 second interval}
ACK block {2 second interval}	ACK block {2 second interval}

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Example no. 4 - Terminal was previously signed on. A job is read in and transmitted to the central processor. A transmission error occurs {CRC-16 error or illegal block make-up or unknown response error or time-out error} and recovery is accomplished. The card reader stream is 4 blocks long.

<u>Terminal Transmits</u>	<u>Central Processor Transmits</u>
ACK block {3 second interval}	ACK block {2 second interval}
Request to initiate a card reader function transmission	Permission to initiate a card reader function transmission
Data block no. 1	ACK block
Data block no. 2 {Transmission error}	NAK block
Data block no. 2	ACK block
Data block no. 3	ACK block
Data block no. 4 {EOF block}	ACK block
ACK block {2 second interval}	ACK block {2 second interval}

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Example no. 5 - Terminal was previously signed on. A job is read in and transmitted to the central processor while a print job is transmitted to the terminal. A CRC-16 error occurs on one of the print blocks and the NAK block from the terminal is then lost. This causes a BCB error which is recovered from. The card reader stream is 4 blocks long and the printer stream is 5 blocks long.

<u>Terminal Transmits</u>	<u>Central Processor Transmits</u>
ACK block {2 second interval}	ACK block {2 second interval}
ACK block {2 second interval}	Request to initiate print function transmission
Permission to initiate print function transmission	Print data block no. 1
ACK block	Print data block no. 2
Request to initiate card reader function transmission	Permission to initiate card function transmission
Card reader data block no. 1	Print data block no. 3
Card reader data block no. 2	Print data block no. 4 {CRC-16 error}
NAK block {lost}	NAK block
Card reader data block no. 3*	Print data block no. 5
BCB error block	Print data block no. 4
Card reader data block no. 4 {EOF block}	Print data block no. 5 {EOF block}
ACK block	ACK block {2 second interval}
ACK block {2 second interval}	ACK block {2 second interval}

* NAK block is never transmitted in response to a NAK block.

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Example no. 6 - Terminal was previously signed on. A printer stream no. 1 is transmitted to the terminal, but while the transmission is occurring, printer stream no. 1 becomes not available. Printer stream no. 1 is 4 blocks long.

<u>Terminal Transmits</u>	<u>Central Processor Transmits</u>
ACK block {2 second interval}	ACK block {2 second interval}
ACK block {2 second interval}	Request to initiate print function transmission
Permission to initiate print function transmission	Printer stream no. 1 data block no. 1
ACK block	Printer stream no. 1 data block no. 2
FCS change block {printer stream no. 1 not available - bit A=0} See Section 4.2.2	ACK block
ACK block {2 second interval}	ACK block {2 second interval}
Idle until printer is available	
FCS change block {printer stream no. 1 now available - bit A=1}	Printer stream no. 1 data block no. 3
ACK block	Printer stream no. 1 data block no. 4 {EOF block}
ACK block	ACK block {2 second interval}
ACK block {2 second interval}	ACK block {2 second interval}

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Example no. 7 - Terminal was previously signed on. A printer stream is transmitted to the terminal. While the transmission is occurring, a operator console command is keyed in.

<u>Terminal Transmits</u>	<u>Central Processor Transmits</u>
ACK block {2 second interval}	ACK block {2 second interval}
ACK block {2 second interval}	Request to initiate printer function transmission
Permission to initiate printer function transmission	Printer data block no. 1
ACK block	Printer data block no. 2
⋮	⋮
Operator Console Block	Printer data block no. X
ACK block	Printer data block no. X+1
⋮	⋮
ACK block	Printer data block no. N {EOF block}
ACK block	ACK block {2 second interval}
ACK block {2 second interval}	ACK block {2 second interval}

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8.0 GLOSSARY

ACK - Affirmative acknowledgment {Section 4.1.1}

BCB - Block control byte {Section 4.2.1}

Control Block - Transmission block consisting only of control characters {Section 4.1}

CRC-16 - Cyclic redundancy check for 8-bit bytes {2 bytes long} {Section 5.1}

Data Block - Transmission block consisting of control characters, control bytes, and data {Section 4.3}

DCT - Device control table

Device Stream - A stream identifier associated with a specific device

DLE - Data link escape {Section 4.1}

EBCDIC - Extended binary coded decimal interchange code {Appendix C}

ENQ - Enquiry {Section 4.1.3}

EOF - End of file {Section 4.4.2}

ETB - End of transmission block {Section 4.3}

FCS - Function control sequence {Section 4.2.2}

HASP - Houston Automatic Spooling and Priority system

MULTI-LEAVING - Communication protocol that operates under HASP for computer-to-computer communications.

NAK - Negative acknowledgment {Section 4.1.2}

Non-transparent mode - Mode in which data-link line control characters are recognized as that.

PAD - A byte containing all 1 bits {Section 4.2 and 4.3}

RCB - Record control byte {Section 4.2.3}

SCB - String control byte {Section 4.2.5}

SOH - Start of heading {Section 4.3}

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SRCB - Sub-Record control byte {Section 4.2.4}

STX - Start of text {Section 4.3}

SYN - Synchronous idle {Section 4.1 and 4.3}

Transparent mode - A versatile mode that treats the data-link
line-control characters only as specific
bit patterns.

Wait-A-Bit - Suspend all stream transmissions {Section 4.2.2}

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APPENDIX A

IBM 360/20 TERMINAL LISTING

LCC OBJECT CODE ADDR1 ADDR2 STMT SOURCE STATEMENT F150CT70 11/21/72

STMT	ADDR1	ADDR2	STMT	SOURCE STATEMENT	ADDRESS
1507 *					F3024000
1508 *					F3026000
1509 *					F3030000
1510 ERHID	SETA	2		*****REHOTE'S IDENTIFICATION	F3032000
1511 ERHID	SETA	20		MODEL OF REHOTE TERMINAL CPU	F3034000
1512 CSUMOD	SETA	2		SUB-MODEL 2 (MODEL 20)	F3036000
1513 CLINESPD	SETA	2000		COMMUNICATION LINE SPEED	F3039000
1514 CRESIZ	SETA	8		AMOUNT OF CORE AVAILABLE TO PROGRAM	F3040000
1515 EXPARENT	SETC	'YES'		ADAPTER TRANSPARENCY FEATURE	F3042000
1516 CHICORE	SETA	CORESIZ		HIGHEST CORE LOCATION AVAILABLE	F3044000
1517 ERTPORG	SETA	K'80'		ORIGIN OF PROGRAM	F3046000
1518 ERRNSGN	SETA	10		MAX NUM ERROR MSG QUEUED	F3048000
1519 ERRTXTL	SETA	13		LENGTH OF TEXT * BLANK	F3050000
1520 *					F3052000
1521 *				CONFIGURATION	F3054000
1522 *					F3056000
1523 EPDEV (1)	SETA	2303		PRINTER 1 MACHINE NUMBER	F3058000
1524 EPDEV (2)	SETA	0		PRINTER 2 MACHINE NUMBER	F3060000
1525 EPDEV (3)	SETA	0		PRINTER 3 MACHINE NUMBER	F3062000
1526 EPDEV (4)	SETA	0		PRINTER 4 MACHINE NUMBER	F3064000
1527 EPDEV (5)	SETA	0		PRINTER 5 MACHINE NUMBER	F3066000
1528 EPDEV (6)	SETA	0		PRINTER 6 MACHINE NUMBER	F3068000
1529 EPDEV (7)	SETA	0		PRINTER 7 MACHINE NUMBER	F3070000
1530 EPADR (1)	SETC	'4'		PRINTER 1 ADDRESS	F3072000
1531 EPADR (2)	SETC	'00F'		PRINTER 2 ADDRESS	F3074000
1532 EPADR (3)	SETC	'FF'		PRINTER 3 ADDRESS	F3076000
1533 EPADR (4)	SETC	'FF'		PRINTER 4 ADDRESS	F3078000
1534 EPADR (5)	SETC	'FF'		PRINTER 5 ADDRESS	F3080000
1535 EPADR (6)	SETC	'FF'		PRINTER 6 ADDRESS	F3082000
1536 EPADR (7)	SETC	'FF'		PRINTER 7 ADDRESS	F3084000
1537 ERDEV (1)	SETA	2501		READER 1 MACHINE NUMBER	F3086000
1538 ERDEV (2)	SETA	0		READER 2 MACHINE NUMBER	F3088000
1539 ERDEV (3)	SETA	0		READER 3 MACHINE NUMBER	F3090000
1540 ERDEV (4)	SETA	0		READER 4 MACHINE NUMBER	F3092000
1541 ERDEV (5)	SETA	0		READER 5 MACHINE NUMBER	F3094000
1542 ERDEV (6)	SETA	0		READER 6 MACHINE NUMBER	F3096000
1543 ERDEV (7)	SETA	0		READER 7 MACHINE NUMBER	F3098000
1544 ERADR (1)	SETC	'1'		READER 1 ADDRESS	F3100000
1545 ERADR (2)	SETC	'FF'		READER 2 ADDRESS	F3102000
1546 ERADR (3)	SETC	'FF'		READER 3 ADDRESS	F3104000
1547 ERADR (4)	SETC	'FF'		READER 4 ADDRESS	F3106000
1548 ERADR (5)	SETC	'FF'		READER 5 ADDRESS	F3108000
1549 ERADR (6)	SETC	'FF'		READER 6 ADDRESS	F3110000
1550 ERADR (7)	SETC	'FF'		READER 7 ADDRESS	F3112000
1551 EUDV (1)	SETA	1442		PUNCH 1 MACHINE NUMBER	F3114000
1552 EUDV (2)	SETA	0		PUNCH 2 MACHINE NUMBER	F3116000
1553 EUDV (3)	SETA	0		PUNCH 3 MACHINE NUMBER	F3118000
1554 EUDV (4)	SETA	0		PUNCH 4 MACHINE NUMBER	F3120000
1555 EUDV (5)	SETA	0		PUNCH 5 MACHINE NUMBER	F3122000
1556 EUDV (6)	SETA	0		PUNCH 6 MACHINE NUMBER	F3124000

LCC	OBJECT CODE	ADDR1	ADDR2	SRST	SOURCE STATEMENT	F150C570	11/21/72
1557	CODEV (7)	SETA	0		PUNCH 7 MACHINE NUMBER	F3124000	
1558	QUADR (1)	SETC	'3'		PUNCH 1 ADDRESS	F3126000	
1559	QUADR (2)	SETC	'FFF'		PUNCH 2 ADDRESS	F3128000	
1560	QUADR (3)	SETC	'PPP'		PUNCH 3 ADDRESS	F3130000	
1561	QUADR (4)	SETC	'PPF'		PUNCH 4 ADDRESS	F3132000	
1562	QUADR (5)	SETC	'FFF'		PUNCH 5 ADDRESS	F3134000	
1563	QUADR (6)	SETC	'PPP'		PUNCH 6 ADDRESS	F3136000	
1564	QUADR (7)	SETC	'PPF'		PUNCH 7 ADDRESS	F3138000	
1565	WDEV (1)	SETA	0		CONSOLE MACHINE NUMBER	F3140000	
1566	WADR (1)	SETC	'01P'		CONSOLE ADDRESS	F3142000	
1567	WADAPT	SETC	'020'		ADDRESS OF COMMUNICATIONS ADAPTER	F3144000	
1568		AIF			(MICROE GT 32) .HISKP1	F3146000	
1569	6L	SETA	2		LENGTH ATTRIBUTE OF ACOMS	F3148000	
1570		AGO			.HISKP	F3150000	
1571	.HISKP	ANOP				F3156000	
1572	6R (1)	SETB	1		NO NON DUAL READER	F3158000	
1573	6R (2)	SETB	1		NO DUAL 2520	F3160000	
1574	6R (3)	SETB	1		NO DUAL 1442	F3162000	
1575	6R (4)	SETB	1		NO 2540 PUNCH	F3164000	
1576	6R (5)	SETB	1		NO NON DUAL PUNCH	F3166000	
1577	6R (6)	SETB	1		NOT MOD 20	F3168000	
1578	6R (7)	SETB	1		NO DUAL 2560	F3170000	
1579	6R (8)	SETB	1		NO SINGLE POCHEAT 1442	F3172000	
1580	6PRTSIZE	SETA	120		MAXIMUM PRINT LINE SIZE	F3174000	
1581	6WOSIZE	SETA	120		LENGTH OF CONSOLE INPUT AREA	F3176000	
1582	*				INTERNAL VARIABLES	F3178000	
1583	*					F3180000	
1584	*					F3182000	
1585	6NUBUFS	SETA	8		NUMBER OF TP BUFFERS	F3184000	
1586	6TPBSIZ	SETA	512		DEFAULT BUFFER SIZE	F3186000	
1587	6ALBPSIZ	SETA	400		MULTI-LEAVING BUFFER SIZE	F3188000	
1588	6TPBSIZ	SETA	6HLBPSIZ		TRANSMISSION BUFFER SIZE	F3190000	V03.1
1589	6TPBSIZ	SETA	6TPBSIZ		FORCE BUFSIZE E2EM	F3192000	
1590	6NUTANK	SETA	8		NUMBER OF DECOMPRESSION TANKS	F3194000	
1591	6CMTYPE	SETA	3		COMPRESSION TECHNIQUE INDICATOR	F3196000	
1592	*				(0 = NO COMPRESSION)	F3198000	
1593	*				(1 = TRAILING BLANK ELIMINATION)	F3200000	
1594	*				(2 = BLANK COMPRESSION ONLY)	F3202000	
1595					(3 = COMPRESS ALL IDENTICAL CHARS)	F3204000	
1596	6CCT	SETA	4		MINIMUM EQUAL CHARACTERS TO COMPRESS	F3206000	
1597	6LOGCLAS	SETA	0		LOWEST ERROR SEVERITY TO LOG	F3208000	
1598	6GEV	SETC	'HOGEM'		SET TO GEN FOR ALL MACRO PRINT	F3210000	
1599	6PRCONS	SETA	0		PRINT CONSOLE MESSAGES OPTION	F3212000	
1600	6LOCCOH	SETA	0		LOCAL COMMAND OPTION	F3214000	
1601	6HOME	SETA	0		HOME LOOP OFF	F3216000	
1602	*					F3218000	
1603	*				FUNCTION CONTROL MASKS FOR TCAS	F3220000	
1604	*					F3222000	
1605	6PCPS	SETA	X'0000'		CONTROL RECORD PROCESSOR	F3224000	
1606	6PPCS (1)	SETA	X'0800'		PRINTER 1	F3226000	
1607	6PPCS (2)	SETA	X'0400'		PRINTER 2	F3228000	
1608	6PPCS (3)	SETA	X'0200'		PRINTER 3	F3230000	
1609	6PPCS (4)	SETA	X'0100'		PRINTER 4	F3232000	
1610	6PPCS (5)	SETA	X'0008'		PRINTER 5	F3234000	
1611	6PPCS (6)	SETA	X'0004'		PRINTER 6	F3236000	

LOC	OBJECT CODE	ADDR1	ADDR2	STMT	SOURCE STATEMENT	F15OCT70	11/21/72
1612	6PPCS(7)	SETA	X'0002'		PRINTER 7	F3238000	
1613	6VPCS(1)	SETA	X'0001'		PUNCH 1	F3280000	
1614	6VPCS(2)	SETA	X'0002'		PUNCH 2	F3282000	
1615	6VPCS(3)	SETA	X'0004'		PUNCH 3	F3284000	
1616	6VPCS(4)	SETA	X'0008'		PUNCH 4	F3286000	
1617	6VPCS(5)	SETA	X'0100'		PUNCH 5	F3288000	
1618	6VPCS(6)	SETA	X'0200'		PUNCH 6	F3290000	
1619	6VPCS(7)	SETA	X'0400'		PUNCH 7	F3292000	
1620	6WPCS(1)	SPTA	X'0040'		CONSOLE	F3254000	
1621	6VPCS(1)	SETA	X'0000'		OPERATOR COMMAND PCS	F3256000	
1622	6VPCS(1)	SETA	X'0800'		READER FUNCTION 1	F3258000	
1623	6VPCS(2)	SETA	X'0400'		READER FUNCTION 2	F3260000	
1624	6VPCS(3)	SETA	X'0200'		READER FUNCTION 3	F3262000	
1625	6VPCS(4)	SETA	X'0100'		READER FUNCTION 4	F3264000	
1626	6VPCS(5)	SETA	X'0008'		READER FUNCTION 5	F3266000	
1627	6VPCS(6)	SETA	X'0004'		READER FUNCTION 6	F3268000	
1628	6VPCS(7)	SETA	X'0002'		READER FUNCTION 7	F3270000	
1629 *					RECORD CONTROL BYTES	F3272000	
1630 *						F3274000	
1631	6RCB	SETA	X'80'		CONTROL RECORD CONTROL BYTE	F3276000	
1632	6PRCB(1)	SETA	X'94'		STREAM 1 PRINT RECORDS	F3278000	
1633	6PRCB(2)	SETA	X'A4'		STREAM 2 PRINT RECORDS	F3280000	
1634	6PRCB(3)	SETA	X'B4'		STREAM 3 PRINT RECORDS	F3282000	
1635	6PRCB(4)	SETA	X'C4'		STREAM 4 PRINT RECORDS	F3284000	
1636	6PRCB(5)	SETA	X'D4'		STREAM 5 PRINT RECORDS	F3286000	
1637	6PRCB(6)	SETA	X'E4'		STREAM 6 PRINT RECORDS	F3288000	
1638	6PRCB(7)	SETA	X'F4'		STREAM 7 PRINT RECORDS	F3290000	
1639	6URCB(1)	SETA	X'05'		STREAM 1 PUNCH RECORDS	F3292000	
1640	6URCB(2)	SETA	X'A5'		STREAM 2 PUNCH RECORDS	F3294000	
1641	6URCB(3)	SETA	X'B5'		STREAM 3 PUNCH RECORDS	F3296000	
1642	6URCB(4)	SETA	X'D5'		STREAM 4 PUNCH RECORDS	F3298000	
1643	6URCB(5)	SETA	X'C5'		STREAM 5 PUNCH RECORDS	F3300000	
1644	6URCB(6)	SETA	X'E5'		STREAM 6 PUNCH RECORDS	F3302000	
1645	6URCB(7)	SETA	X'F5'		STREAM 7 PUNCH RECORDS	F3304000	
1646	6URCB(1)	SETA	X'91'		MESSAGE TO TERM OPERATOR	F3306000	
1647	6URCB(2)	SETA	X'92'		OPERATOR COMMAND RCB	F3308000	
1648	6URCB(3)	SETA	X'93'		STREAM 1 TO HASP	F3310000	
1649	6URCB(4)	SETA	X'A3'		STREAM 2 TO HASP	F3312000	
1650	6URCB(5)	SETA	X'B3'		STREAM 3 TO HASP	F3314000	
1651	6URCB(6)	SETA	X'C3'		STREAM 4 TO HASP	F3316000	
1652	6URCB(7)	SETA	X'D3'		STREAM 5 TO HASP	F3318000	
1653	6URCB(1)	SETA	X'E3'		STREAM 6 TO HASP	F3320000	
1654	6URCB(2)	SETA	X'F3'		STREAM 7 TO HASP	F3322000	
1655	6URCB(3)	SETA	X'F3'			F3324000	
1656 *						F3326000	
1657 *						F3328000	
1658 *						F3330000	
1659 *						F3332000	
1660 *						F3334000	
1661 *						F3336000	
1662 *						F3338000	
1663 *						F3340000	
1664 *						F3342000	
1665 *						F3344000	
1666 *						F3346000	

THE FOLLOWING VALUES REPRESENT THE LIMIT OF QUEUING FOR EACH INDICATED DEVICE.
 THE 'BL' VALUES REPRESENT THE MAXIMUM NUMBER OF BUFFERS FROM HASP WHICH WILL BE QUEUED ON THE DEVICE.
 THE 'TL' VALUES REPRESENT THE NUMBER OF DECOMPRESSED RECORDS WHICH WILL BE QUEUED, NOTE THAT SINCE ONE DECOMPRESSED RECORD WILL ALWAYS BE IN PROCESS, THE DEVICE WILL REQUIRE ONE TANK MORE THAN THE TANK LIMIT (A 2500 PUNCH REQUIRES 2 MORE FOR ERROR RECOVERY PURPOSES).

LCC	OBJECT CODE	ADDR1	ADDR2	STMT	SOURCE STATEMENT	P150CT70	11/21/72
1668	*				DECOMPRESSION TANK LIMITS		F3350000
1669	*				DECOMPRESSION TANK LIMITS		F3352000
1670	*				PRINTERS		F3354000
1671	*				PRINTERS		F3356000
1672	*			SETA 1	PRINTER 1-DECOMPRESSION LIMIT		F3358000
1673	*			SETA 2	PRINTER 2-DECOMPRESSION LIMIT		F3360000
1674	*			SETA 3	PRINTER 3-DECOMPRESSION LIMIT		F3362000
1675	*			SETA 4	PRINTER 4-DECOMPRESSION LIMIT		F3364000
1676	*			SETA 5	PRINTER 5-DECOMPRESSION LIMIT		F3366000
1677	*			SETA 6	PRINTER 6-DECOMPRESSION LIMIT		F3368000
1678	*			SETA 7	PRINTER 7-DECOMPRESSION LIMIT		F3370000
1679	*			PUNCHES			F3372000
1680	*			SETA 1	PUNCH 1 - DECOMPRESSION LIMIT		F3374000
1681	*			SETA 2	PUNCH 2 - DECOMPRESSION LIMIT		F3376000
1682	*			SETA 3	PUNCH 3 - DECOMPRESSION LIMIT		F3378000
1683	*			SETA 4	PUNCH 4 - DECOMPRESSION LIMIT		F3380000
1684	*			SETA 5	PUNCH 5 - DECOMPRESSION LIMIT		F3382000
1685	*			SETA 6	PUNCH 6 - DECOMPRESSION LIMIT		F3384000
1686	*			SETA 7	PUNCH 7 - DECOMPRESSION LIMIT		F3386000
1687	*			CONSOLE			F3388000
1688	*			SETA 1	CONSOLE DECOMPRESSION LIMIT		F3390000
1689	*			SETA 2	CONTROL RECORD PROCESSOR		F3392000
1690	*			SETA 3	CONTROL DECOMPRESSION LIMIT		F3394000
1691	*			SETA 4			F3396000
1692	*			SETA 5			F3398000
1693	*			SETA 6			F3400000
1694	*			SETA 7			F3402000
1695	*			SETA 1	PRINTER 1-BUFFER QUEUING LIMIT		F3404000
1696	*			SETA 2	PRINTER 2-BUFFER QUEUING LIMIT		F3406000
1697	*			SETA 3	PRINTER 3-BUFFER QUEUING LIMIT		F3408000
1698	*			SETA 4	PRINTER 4-BUFFER QUEUING LIMIT		F3410000
1699	*			SETA 5	PRINTER 5-BUFFER QUEUING LIMIT		F3412000
1700	*			SETA 6	PRINTER 6-BUFFER QUEUING LIMIT		F3414000
1701	*			SETA 7	PRINTER 7-BUFFER QUEUING LIMIT		F3416000
1702	*			PUNCHES			F3418000
1703	*			SETA 1	PUNCH 1 - BUFFER QUEUING LIMIT		F3420000
1704	*			SETA 2	PUNCH 2 - BUFFER QUEUING LIMIT		F3422000
1705	*			SETA 3	PUNCH 3 - BUFFER QUEUING LIMIT		F3424000
1706	*			SETA 4	PUNCH 4 - BUFFER QUEUING LIMIT		F3426000
1707	*			SETA 5	PUNCH 5 - BUFFER QUEUING LIMIT		F3428000
1708	*			SETA 6	PUNCH 6 - BUFFER QUEUING LIMIT		F3430000
1709	*			SETA 7	PUNCH 7 - BUFFER QUEUING LIMIT		F3432000
1710	*			CONSOLE			F3434000
1711	*			SETA 1	CONSOLE BUFFER QUEUING LIMIT		F3436000
1712	*			SETA 2	CONTROL RECORD PROCESSOR		F3438000
1713	*			SETA 3	CONTROL BUFFER QUEUING LIMIT		F3440000
1714	*			SETA 4	CONTROL BUFFER QUEUING LIMIT		F3442000
1715	*			SETA 5	CONTROL BUFFER QUEUING LIMIT		F3444000

LOC OBJECT CODE ADDR1 ADDR2 SMT SOURCE STATEMENT

P15OCT70 11/21/72

1718 AIF (MACHINE EQ 20).LOAD20

1719 .LOAD20 ANOP

1720 PUNCH Y 0 0+ 88 \$ 0 0 N 0+ 8 0 H000 X000E # 00 00 00 XF3458000

1721 .SKIP ANOP

F3450000
F3456000
F3458000
F3460000
F3862000

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LCC OBJECT CODE ADDR1 ADDR2 SHT SOURCE STATEMENT

000000	1724	HASPRTP	START 0			F3468000
	1725 *					F3470000
	1726 *			DEBUGGING VARIABLES		F3472000
	1727 *					F3474000
	1728	EDEBUG	SETA 1			F3476000
	1729	CTRACE	SETA 0	NO TRACE DEGRADATION		F3478000
	1730	EREP	SETA 1	PATCH MODE ON		F3480000
	1731 *					F3482000
	1732 *			CONFIGURATION DEPENDENT EQUATES		F3484000
	1733 *					F3486000
	1734			\$CONFIG		F3488000
	1753	EA	SETA 1			F3490000
	1754 *					F3492000
	1755 *			SYMBOLIC DEFINITIONS USED TO REFER TO TCT LOCATIONS		F3494000
	1756 *			** SEE GLOBAL STORAGE FOR UNIQUE PROCESSORS EQUIVALENT DEFS		F3496000
	1757 *					F3498000
000000	1758	TCDSECT	DSECT			F3500000
	1759			DEFINE TCT SYMBOLS		F3502000
	1779 *					F3504000
	1780 *			TCTSTAT BIT DEFINITIONS		F3506000
000008	1781	TCT1403	EQU X'08'	TCT STATUS FLAGS FOR 1403, SW ON		F3509000
000008	1782	TCT1403	EQU X'08'	TCT STATUS FLAGS FOR 1403, SW ON		F3510000
000008	1783	TCT2203	EQU X'08'	TCT STATUS FLAGS FOR 2203, SW ON		F3512000
000000	1784	TCT2501	EQU X'00'	TCT STATUS FLAGS FOR 2501		F3514000
000000	1785	TCT2520	EQU X'00'	TCT STATUS FLAGS FOR 2520		F3516000
000001	1786	TCT2520	EQU X'01'	TCT STATUS FLAGS FOR 2520 DUAL		F3518000
000008	1787	TCT1442	EQU X'08'	TCT STATUS FLAGS FOR 1442		F3520000
000002	1788	TCT1442	EQU X'02'	TCT STATUS FLAGS FOR 1442 DUAL		F3522000
000004	1790	TCT2540	EQU X'04'	TCT STATUS FLAGS FOR 2540		F3524000
000002	1791	TCT2560	EQU X'02'	TCT STATUS FLAGS FOR 2560 DUAL		F3526000
000010	1792	TCT2560	EQU X'10'	TCT STATUS FLAGS FOR 2560		F3528000
000010	1793	TCT1052	EQU X'10'	TCT STATUS FLAGS FOR 1052		F3530000
000010	1794	TCT2152	EQU X'10'	TCT STATUS FLAGS FOR 2152		F3532000
000004	1795	TCTREL	EQU X'04'	INTERLOCK RELEASE REQ FOR CONSOLE		F3534000
000003	1796	TCTDUAL	EQU X'03'	EITHER DUAL DEVICE		F3536000
000080	1797	TCTOPEN	EQU X'80'	TCT OPEN BIT		F3538000
000040	1798	TCTACT	EQU X'40'	ACTION REQUIRED ON THIS TCT		F3540000
000008	1799	TCTREQ	EQU X'08'	CONSOLE REQUEST BIT		F3542000
000008	1800	TCTPRISM	EQU X'08'	PRINTER AVAILABLE FOR CONSOLE		F3544000
000020	1801	TCTSTOP	EQU X'20'	TCT IS IN STOPPED STATE		F3546000
	1802 *			.S COMMAND REQUIRED TO START UP		F3548000
	1803 *			TCTUCB BIT DEFINITIONS		F3550000
000080	1805	TCTNPOINT	EQU X'80'	NO INTERRUPTS ON DEVICE ALLOWED		F3556000
	1807 *			DUMMY TCT DEVICE DEFINITIONS FOR CCTLCT		F3560000
000000	1809	CO	EQU X'00'	DUMMY OPCODE FOR CONTROL		F3564000

HE2 GLOBAL SYMBOL DEFINITIONS

LCC	OBJECT CODE	ADDR1	ADDR2	SYMT	SOURCE STATEMENT	ADDRESS
000000		1810	C01	EQU	X'00'	DUMMY OPCODE FOR CONTROL
000000		1811	CF0	EQU	X'00'	DUMMY FLAGS FOR CCF
000000		1812	TCT0	EQU	0	DUMMY DEVICE STATUS
000010		1814	***	TCTECB	BIT DEFINITIONS	
000040		1816	TCTBUSY	EQU	X'10'	DEVICE BUSY BIT
		1818	***	TCTSENSE	BIT DEFINITIONS (FIRST BYTE)	
		1820	TCTINREQ	EQU	X'40'	INTERVENTION REQUIRED BIT

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F356000
F356800
F357000

F3574000

F3578000

F3582000

F3586000

LOC	OBJECT CODE	ADDR1	ADDR2	STMT	SOURCE STATEMENT	
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000000 1824 BUFDSECT DSSECT
 000000 1825 BUFBEGIN DS OF
 000002 1826 BUFBEGIN SACOM 0
 000004 1828 BUFCOUNT DS H
 000005 1829 BUFBSTAT DS C
 000007 1830 BUFBSTART DS CL2
 000008 1831 BUFBFCB DS C
 00000A 1832 BUFBPCS DS CL2
 000198 1833 BUFBDATA DS (6TPBFSIZ-5)C
 1834 BUFBEND DS OF

TRANSMISSION BUFFER DSSECT
 BEGINNING OF THE BUFFER
 BUFFER CHAIN FIELD
 COUNT OF BYTES TO TRANSMIT
 BUFFER STATUS BYTE
 TRANSMISSION CONTROL BYTES
 BLOCK CONTROL BYTE
 FUNCTION CONTROL SEQUENCE
 DATA PORTION OF TP BUFFER
 FULL ALIGNMENT FOR THE NEXT

F3594000
 F3596000
 F3598000
 F3600000
 F3602000
 F3604000
 F3606000
 F3608000
 F3610000

1836 *
 1837 *
 1838 *

BUFFER STATUS BIT DEFINITIONS

F3616000
 F3618000
 F3620000

000001 1840 BUFBPAKE EQU X'01'
 000002 1841 BUFBRESP EQU X'02'
 000004 1842 BUFBPAK EQU X'04'
 000008 1843 BUFBTEXT EQU X'08'
 000010 1844 BUFBUCHEK EQU X'10'

DUMMY BUFFER INDICATOR
 RESPONSE ONLY IN BUFFER
 WAK RESPONSE BEING SENT
 BUFFER CONTAINS TEXT INFORMATION
 UNIT CHECK EXPECTED

F3624000
 F3626000
 F3628000
 F3630000
 F3632000

LCC	OBJECT CODE	ADDR1	ADDR2	STMT	SOURCE STATEMENT	P15OCT70	11/21/72
000000							
000000							
000001							
000002							
000004							
000010							
1857	LOGDSECT	DSECT			LOG DSECT		F3656000
1858	LOGID	DS	C		LOG IDENTIFICATION		F3658000
1859	LOGCLASS	DS	C		SEVERITY CODE OF MESSAGE		F3660000
1860	LOGCOUNT	DS	H		COUNT OF TOTAL ERRORS		F3662000
1861	LOGTEXT	DS	CL(6ERRTEXTL-1)		TEXT OF MESSAGE		F3664000
			CL(13-1)		TEXT OF MESSAGE		
1862	LOGEND	DS	OC		END OF DSECT		F3666000

LCC	OBJECT CODE	ADDR1	ADDR2	STMT	SOURCE STATEMENT	
000000					1865 HASRTP CSECT	F3672000
					1866 AIP	F3674000
					1867 .ORG20 (6MACHINE PQ 20).ORG20	F3766000
000000					1868 HASRTP TO BEGINNING OF CSECT	F3768000
					1869 DS (6HRTFORG)C PROGRAM ORIGIN	F3770000
000000					DS (128)C PROGRAM ORIGIN	

HR2 H A S P / R J E *SYMBOLIC REGISTER DEFINITIONS*

IC	OBJECT CODE	ADDR1	ADDR2	STRT	SOURCE STATEMENT	PC
1						
2						
3	000080			1871	HASPRJE EQU *	P15OCT70 11/21/72
4				1872	AIP	F3774000
5				1873	.EIGHTH ANOP	F3776000
6	000008			1874	R8 EQU 8	F3794000
7	000009			1875	R9 EQU 9	F3796000
8	00000A			1876	R10 EQU 10	F3798000
9	00000B			1877	R11 EQU 11	F3800000
10	00000C			1878	R12 EQU 12	F3802000
11	00000D			1879	R13 EQU 13	F3804000
12	00000E			1880	R14 EQU 14	F3806000
13	00000F			1881	R15 EQU 15	F3808000
14	000007			1882	TCTR EQU 7	F3810000
15					TCT BASE REGISTER	F3812000
16						
17						
18				1884 *		F3816000
19				1885 *		F3818000
20				1886 *		F3820000
21					REGISTER ASSIGNMENTS AND/OR TYPICAL ASSIGNMENTS	
22						
23				1888 *	R15 = WORK REG (SECONDARY LINK OR PARAMETER REGISTER)	F3824000
24				1889 *	R14 = LINK REG	F3826000
25				1890 *	R13 = BUFFER POINTER	F3828000
26				1891 *	R12 = RESERVED	F3830000
27				1892 *	R11 = WORK	F3832000
28				1893 *	R10 = WORK	F3834000
29				1894 *	R9 = WORK	F3836000
30				1895 *	R8 = PARAMETER REGISTER	F3838000
31				1896	AIF (MACHINE EQ 20).Y1	F3840000
32				1897 .X1	AIP (MACHINE EQ 20).Y1	F3840000
33				1898 .BASOK1	ANOP (MACHINE EQ 20).BASOK1	F3858000
34				1899	ANOP	F3864000
35					USING HASPRJE-CHRTORG,0,1,2,3 PROVIDE FOR 16K	F3866000
36	000000				USING HASPRJE-128,0,1,2,3 PROVIDE FOR 16K	F3870000
37	000080			1900	DS (X*24)C	F3872000
38				1901 .BASOK2	ANOP (CONSOL EQ 0 AND 6PRCONS EQ 0).ERRLOG	F3902000
39				1902	AIP	
40				1903 .ERRLOG	ANOP	

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LCC OBJECT CODE ADDR1 ADDR2 STMT SOURCE STATEMENT

1906					PRINT GEN	F3908000
1907					\$START \$CONCOH	F3910000
1908					\$START DS OH	
00258	0000A4	4700	0258			
0089E	0000A8	4700	089E		CONTROL RECORD PROCESSOR	
00354	0000AC	4700	0354		INPUT BUFFER HANGER	
00432	0000B0	47F0	0432		ENTRY POINT TO PRINT	
0054C	0000B4	4700	054C		ENTRY POINT TO READ CARD	
00DC2	0000B8	4700	0DC2		ENTRY TO PUNCH CARD	
000A4	0000BC	47F0	00A4		COMMUNICATIONS SUPERVISOR	
1915					\$START	
1916					PRINT GEN	F3912000
					PRINT NOGEN	

1916					\$CHTEMP \$ACON 0	DISABLED TEMPORARY WORK	F3916000
1920					\$LOGINFO DC A(0)	INFORMATION TO BE LOGGED	F3918000
1921					\$LOGSAVE \$ACON 0	\$LOG REG SAVE AREA	F3920000
1923					\$COHDCOH DS 0C		F3922000
1924					\$INPCOH DS 0C		F3924000
1925					\$DATCOH DS 0C		F3926000
1926					\$ROUCOH DS 0C		F3928000

LOC	OBJECT CODE	ADDR1 ADDR2	STMT	SOURCE STATEMENT	DATE	PRINT GRH
				F150CCT70	11/21/72	F3932000
1930	6B		SETA 1	SET SYMB TO CHAIN TCTS TOGETHER		F3936000
1931	6A		SETA 1			F3938000
1932	6D		SETA 1	SIGNAL ORG TO GENTCT		F3940000
1933	6C		SGENTCT C,6CPCS,6CRCB,6FP,0,6CTLH,6CBLH,6CPCS,6CRCB	CONT TCT		F3942000
1934	6C		DS OH	ORIGIN OF TOTAL CONTROL TABLE		
1935	**					
1936	**			SYMBOLIC DEFINITIONS USED BY PROCESSOR TO REFER TO TCT		
1937	**					
1938	6C		DS OH			
1939	6C		DS H			
1940	6C		AL2	FUNCTION CONTROL SEQUENCE MASK		
1941	6C		DS CL1	RECORD CONTROL BLOCK MASK		
1942	6C		DS CL1	STATUS FLAGS		
1943	6C		DS H			
1944	6C		EQU CCTRCB	READER RCB IDENTIFICATION		
1945	**					
1946	**			NORMAL DEVICE EXTENSION		
1947	**					
1948	6C		DS H			
1949	6C		DS H			
1950	**					
1951	**			TNKLH, TNKCT AND BUPLH, BUPTCT MUST APPEAR IN SEQ AND STRT		
1952	**			OH HALF WORD BOUNDARIES		
1953	6C		DS CL1	MAX NUM OF TANKS ASSIGNABLE TO DEV		
1954	6C		DS CL1	CURRENT NUM ASSIGNED		
1955	6C		DS CL1	MAX NUM OF BUFFERS ASSIGNABLE TO DV		
1956	6C		DS CL1	CURRENT NUM ASSIGNED		
1957	6C		ORG \$TCT+			
1958	**					
1959	**			STORAGE ALLOCATION AND INITIAL VALUES FOR TCT		
1960	**					
1961	6C		DS OH			
1962	6C		Y(\$TCT2)			
1963	6C		AL2(0)			
1964	6C		AL1(128)			
1965	6C		AL1(\$TCT0)			
1966	6C		Y(\$CCOMH)			
1967	**			NORMAL EXTENSION TO TOTAL CONTROL TABLE		
1968	6C		Y(0)			
1969	6C		Y(0)			
1970	6C		AL1(8)			
1971	6C		X'00'	CURRENT NUMBER OF TANKS IN Q		
1972	6C		AL1(8)			
1973	6C		X'00'	CURRENT NUMBER OF BUFFERS IN Q		

1	LCC	OBJECT CODE	ADDR1	ADDR2	STRT	SOURCE STATEMENT	F150CT70	11/21/72
2					1974 6A	SETA 1		
3					1975 6D	SETA 6CONSOLE	SIGNAL DSECT OR ORG TO GENMCT	F3948000
4					1976	AIF (6CONSOLE EQ 0 AND 6PRTCONS EQ 0).TCTWTO		F3948000
5					1977 .TCTWTO	ANOP		F3956000
6					1978 6D	SETA 6NUHPRM	SIGNAL DSECT OR ORG TO GENMCT	F3958000
7					1979 .TCTPRTL AIF	(6A GT 6NUHPRM).TCTPRT		F3960000

LOC	OBJECT CODE	ADDR1	ADDR2	SRHT	SOURCE STATEMENT	7150CT70	11/21/72
1981					SGENCT P,GPPCS(GA),CPRCB(GA),SPADR(GA),SPDEV(GA),SETLH(GA),CP3964000		
1982+STCT2	DS				CPBLM(GA),GPPCS(GA),SPRCB(GA),PRINTER TCT		F3966000
1983**					ORIGIN OF TOTAL CONTROL TABLE		
1984**					SYMBOLIC DEFINITIONS USED BY PROCESSOR TO REFER TO TCT		
1985**							
1986+PTCT	DS						
1987+PTNEXT	DS						
1988+PTPCS	DS				FUNCTION CONTROL SEQUENCE MASK		
1989+PTRCB	DS				RECORD CONTROL BLOCK MASK		
1990+PTSTAT	DS				STATUS FLAGS		
1991+PTCOH	DS						
1992+PTINRCB	DS				READER RCB IDENTIFICATION		
1993**							
1994**					NORMAL DEVICE EXTENSION		
1995**							
1996+PCTANK	DS						
1997+PCTBUFER	DS						
1998**							
1999**					TNKLN,TNKCT AND BUFLN,BUFCT MUST APPEAR IN SEQ AND START		
2000**					ON HALF WORD BOUNDARIES		
2001+PCTTKLN	DS				MAX NUM OF TANKS ASSIGNABLE TO DEV		
2002+PCTTKCT	DS				CURRENT NUM ASSIGNED		
2003+PCTBUFLM	DS				MAX NUM OF BUFFERS ASSIGNABLE TO DV		
2004+PCTBUFCT	DS				CURRENT NUM ASSIGNED		
2005*	ORG				STCT2		
2006**							
2007**					STORAGE ALLOCATION AND INITIAL VALUES FOR TCT		
2008**							
2009*	DS						
2010*	DC				Y(STCT3)		
2011*	DC				AL2(2048)		
2012*	DC				AL1(148)		
2013*	DC				AL1(TCT2203)		
2014*	DC				Y(SPCOMH)		
2015**					NORMAL EXTENSION TO TOTAL CONTROL TABLE		
2016*	DC				Y(0)		
2017*	DC				Y(0)		
2018*	DC				AL1(1)		
2019*	DC				X'00*		
2020*	DC				AL1(2)		
2021*	DC				X'00*		
2022 SA	SETA				SA+1		F3968000
2023	AGO				.TCTPRTL		F3970000
2024	.TCTPRTL AIP				(GA GT 6NUMPRT).TCTPRT		F3960000
2025	.TCTPRT ANOP				SETA 1		F3972000
2026 SA	SETA						F3974000
2027 ED	SETA				ENUMRDR		F3976000
2028	.TCTDRAL AIP				(GA GT 6NUMRDR).TCTDRD		F3978000

HR2 READER TOTAL CONTROL TABLES

LCC	OBJECT CODE	ADDR1	ADDR2	STMT	SOURCE STATEMENT	11/21/72
				2030	AIP (6S(6A) NE 0).TCTDR1	F3982000
				2031	\$GERTCT R,8RPCS(6A),SRCB(6A),CHADR(6A),8BDEV(6A),0,0,0,	CP3984000
	0000EA			2032+\$RCT3	DS OH	F3986000
				2033**	ORIGIN OF TOTAL CONTROL TABLE	
				2034**	SYMBOLIC DEFINITIONS USED BY PROCESSOR TO REFER TO TCT	
				2035**		
	0000EA			2036+RCT	DS OH	
	0000EA			2037+RCTEXT	DS H	
	0000EC			2038+RCTFCS	DS AL2	FUNCTION CONTROL SEQUENCE MASK
	0000EE			2039+RCTRCB	DS CL1	RECORD CONTROL BLOCK MASK
	0000EF			2040+RCTSTAT	DS CL1	STATUS FLAGS
	0000FO			2041+RCTCOM	DS H	
	0000EE			2042+RCTINRCB EQU	RCTRCB	READER RCB IDENTIFICATION
				2043**		
				2044**	TANK EXTENSIONS FOR READER AND CONSOLE PROCESSORS	
				2045**		
	0000F2			2046+RCTTANK1	DS H	
	0000F4			2047+RCTTRCB1	DS CL1	RCB IDENTIFICATION
	0000F5			2048+RCTTSRC1	DS CL1	SCB IDENTIFICATION
	0000F6			2049+RCTTCT1	DS H	NUMBER OF DATA CHARACTERS
	0000F8			2050+RCTTDF1	DS CL(RSIZE+4)	
	00014C			2051+RCTTANK2	DS H	
	00014E			2052+RCTTRCB2	DS CL1	RCB IDENTIFICATION
	00014F			2053+RCTTSRC2	DS CL1	SCB IDENTIFICATION
	000150			2054+RCTTCT2	DS H	NUMBER OF DATA CHARACTERS
	000152			2055+RCTTDF2	DS CL(RSIZE+4)	
	0000EA			2056+	ORG \$TCT3	
				2057**		
				2058**	STORAGE ALLOCATION AND INITIAL VALUES FOR TCT	
				2059**		
	0000EA			2060+	DS OH	
	0000EA 01A6			2061+	DC Y(\$TCT4)	
	0000EC 0800			2062+	DC AL2(2048)	
	0000EE 93			2063+	DC AL1(147)	
	0000EF 00			2064+	DC AL1(TCT2501)	
	0000FO 00B0			2065+	DC Y(SRCOHM1)	
				2066**	INPUT TANKS FOR READER AND CONSOLE	
	000CF2 0000			2067+	DC Y(0)	
	0000F4 93			2068+	DC AL1(147)	
	0000F5 80			2069+	DC X'80'	
	0000F6 0050			2070+	DC H'80'	SRCB DATA LENGTH
	0000F8 40404040404040			2071+	DC CL(RSIZE+4) *	
	00014C 0000			2072+	DC Y(0)	
	00014E 93			2073+	DC AL1(147)	
	00014F 80			2074+	DC X'80'	
	000150 0050			2075+	DC H'80'	SRCB DATA LENGTH
	000152 40404040404040			2076+	DC CL(RSIZE+4) *	
				2077	AGO .TCTDR2	
				2078	.TCTDR2 ANOP	F3988000
				2079 EA	SETA 6A*1	F4000000
				2080	AGO .TCTDR1	F4002000
				2081	.TCTDEL AIP (6A GT 6NUMBER).YCTDR	F4004000
				2082	.TCTDR ANOP	F3978000
				2083 EA	SETA 1	F4006000
						F4008000

LOC	OBJECT CODE	ADDR1	ADDR2	SYMT	SOURCE STATEMENT
					P15OCT70 11/21/72
2087					SGENRCT D,5UPCS(6A),6UADR(6A),6UDEY(6A),6UHLH(6A), CF4016000 6UBLH(6A),6URCB(6A),6UPCH TCT P4018000
2088+STCTA	DS	OH			ORIGIN OF TOTAL CONTROL TABLE
2089**					
2090**					SYMBOLIC DEFINITIONS USED BY PROCESSOR TO REFER TO TCT
2091**					
2092+UTCT	DS	OH			
2093+UCTNEXT	DS	H			FUNCTION CONTROL SEQUENCE MASK
2094+UCTPCS	DS	AL2			RECORD CONTROL BLOCK MASK
2095+UCTRCB	DS	CL1			STATUS FLAGS
2096+UCTSWAT	DS	CL1			
2097+UCTCON	DS	H			READER RCB IDENTIFICATION
2098+UCTIRCB	DS	UCTRCB			
2099**					
2100**					NORMAL DEVICE EXTENSION
2101**					
2102+UCTANK	DS	H			
2103+UCTBUFER	DS	H			
2104**					
2105**					TANKLH, TANKCT AND BUPLH, BUPTCT MUST APPEAR IN SEQ AND STRT ON HALF WORD BOUNDARIES
2106**					MAX NUM OF TANKS ASSIGNABLE TO DEV
2107+UCTTKLML	DS	CL1			CURRENT NUM ASSIGNED
2108+UCTTKNCT	DS	CL1			MAX NUM OF BUFFERS ASSIGNABLE TO DV
2109+UCTBUFLH	DS	CL1			CURRENT NUM ASSIGNED
2110+UCTBUFCT	DS	CL1			
2111+					ORG STCTH
2112**					
2113**					STORAGE ALLOCATION AND INITIAL VALUES FOR TCT
2114**					
2115+	DS	OH			
2116+	DC	Y(STCT5)			
2117+	DC	AL2(1)			
2118+	DC	AL1(149)			
2119+	DC	AL1(TCT1442)			
2120+	DC	Y(SUCOHH)			
2121**					NORMAL EXTENSION TO TOTAL CONTROL TABLE
2122+	DC	Y(0)			
2123+	DC	Y(0)			
2124+	DC	AL1(1)			
2125+	DC	X'00'			CURRENT NUMBER OF TANKS IN Q
2126+	DC	AL1(2)			
2127+	DC	X'00'			CURRENT NUMBER OF BUFFERS IN Q
2128 GA	SETA	EA+1			
2129	AGO	.TCTPUNL			P4020000
2130 .TCTPUNL	AIF	(6A GT 6UHPUN) .TCTPUN			P4020000
2131 .TCTPUN	ANOP				P4024000

LOC	OBJECT CODE	ADDR1	ADDR2	SRHT	SOURCE STATEMENT	
000000				2133	STCTED EQU 0 STCTS EQU 0	LAST ICT POINTS TO ZERO LAST ICT POINTS TO ZERO
				2134	PRINT 6GEN PRINT HOGEN	
				2136	.NOGCTS AWOP	
				2137 *		
				2138 *		
				2139 *		
000186				2140	LOGTABLE DS OH	ERROR LOG TABLE
000248				2141	ORG **X'92'	ORG OVER THE LOG TABLE
000186				2142	ERRTAB EQU LOGTABLE-LOGEND%LOGSECT	LOGTABLE-LOGEND%LOGSECT ERROR LOG TABLE FOR CE
				2144 *		
				2145 *		
				2146 *		
						BEGINNING OF QUEUE CHAINS
000248 0000				2148	STEMP DC H'0'	GLOBAL TEMPORARY WORK
				2149	\$BUPPOOL SACOH \$1STBUP	SUPER POOL CHAIN CONTROL WORD
				2151	\$TANKPOL SACOH \$1STANK	TANK QUEUE CONTROL WORD
				2153	\$OUTBUP SACOH 0	MISSION BUFFER CHAIN CTL WORD
				2155	\$SCORESIZ SACOH 0	SYSTEM MEMORY SIZE
000252				2157	\$PCSOUT DS OH	OUTGOING FUNCTION CONTROL SEQUENCE
000252 8PCF				2158	DC X'8FCF'	ALL FUNCTIONS PERMITTED
000254 8PCF				2159	\$PCFIN DC X'8FCF'	INCOMING PCS FROM HASP
				2160	\$WAITCON AIF (MACHINE WE 20).N20CON	
000256 00A0				2161	\$WAITCON DC H'160'	DELAYING CONSTANT FOR MOD 20
				2162	AGO .N30CON	
				2163	.N30CON AHOP	

HR2 S C O N T R O L -- INPUT CONTROL RECORD PROCESSOR

1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40		
LCC	OBJECT CODE	ADDR1	ADDR2	STRT	SOURCE STATEMENT																																				
				2165 *																																					
				2166 *																																					
	0000A4			2167	SCRPH1			NULL																																	
	000000			2169	SCCORH1	EQU		\$CONTOR																																	
	000000			2170	CPLAGS	EQU	0																																		
				2171	COPCODE	EQU	0																																		
				2172 *																																					
				2173	SCONTROL	NULL	*																																		
	000000			2175	SLA			R13,SCITICT																																	
				2177				USING TCTDSECT,R13																																	
	00026E	4770	027A	2178				\$CHAIN TCTANK,RB,NOSSH																																	
	000272	9200	00A5	2183				ENZ HPROCESS																																	
	000276	47F0	00A8	2184				RVI SCONTCOR*1,0																																	
				2185				B SCONTCOR*4																																	
				2186 *																																					
				2187 *																																					
				2188 *																																					
				2190	HPROCESS	NULL	*																																		
				2192	\$TCPOST	T,R10	*																																		
				2198	DROP	R1J																																			
	000000			2199	USING	TANKDSEC,R8																																			
	00028E	F300	030F	8002	0030F	00002																																			
	000294	9403	030F	0030F																																					
	000298	48A0	030E	0030E																																					
	00029C	7AAA																																							
	0002A2	7A9A																																							
	0002A4	4890	9000																																						
	0002A8	4090	02AE	002AE																																					
	0002AC	47F0	0000	00000																																					

LOC	OBJECT CODE	ADDR1	ADDR2	STMT	SOURCE STATEMENT	PC
2211 *						F4154000
2212 *					SUBROUTINE TO FIND TCT CORRESPONDING TO SRCE FUNCTION	F4156000
2213 *					R14=RETURN , CC NE 0 -R13 CONTAINS TCT,CC=0-NOT FOUND	F4159000
2215	HTCFIND NULL *					F4162000
2217	SLA R13,STCT1				ENTRY POINT	F4164000
2219	USING TCTDSECT,R13				FIRST TCT	F4166000
2220	HNEXTTCT NULL *				ADDRESSABILITY	F4168000
2222	CLC TCTRCB,TANKSECB	00003	00008	00003	IS THIS CORRECT TCT	F4170000
2223	BE HTCTOK	002CC		002CC	BR IF YES	F4172000
2224	AIF (6P(2) AND 6R(3) AND 6R(7)).HDUAL				BR IF YES	F4174000
2225	.HDUAL ANOP				AND 6R(7) .HDUAL	F4180000
2226	SLOAD R13,TCRNEIT				NO... TO NEXT	F4182000
2228	SLTR R13,R13				IS THIS ALL...	F4184000
2230	BNZ HNEXTTCT	002B4		002B4	BR IF MORE	F4186000
2231	BR R14				RETURN WITH COND. CODE = 0	F4188000
2232	HTCTOK SLTR R14,R14				SET COND. CODE NON-ZERO	F4190000
2234	BR R14				AND RETURN	F4192000

RR2 S C O N T R O L -- INPUT CONTROL RECORD PROCESSOR

LOC	OBJECT CODE	ADDR1	ADDR2	STMT	SOURCE STATEMENT	
					SUBROUTINE TO \$PPUT AN ANSWERING CTL RECORD	F4198000
					RB = TANKADDR	F4200000
						F4202000
2237 *						
2238 *						
2239 *						
2241	HPPT				NULL *	F4206000
2243					BAS R14,\$STPPUT	F4208000
2247		002F4			BHZ \$EXIT	F4210000
2248		0002DA	D201 00A6 0310 000A6 00310		HVC \$CONTCOH*2(2),HREPUTA SRT COMUTATOR RE-ENTRY	F4212000
2249					\$STO R8,NTANK SAVE TANK ADDR	F4214000
2251		000A8			B \$CONTCOH*4 EXIT TO COMUTATOR	F4216000
2252	HREPUT				NULL *	F4218000
2254					\$LOAD R8,NTANK RETRY PUTTING RECORD	F4220000
2256					BAS R14,\$STREPUT TRY IT	F4222000
2260		000A8			BZ \$CONTCOH*4	F4224000
2261	NEXIT				NULL *	F4226000
2263					\$FREE \$TANKPOL,R8,NOENB FREE TANK	F4228000
2267		000302	D201 00A6 0312 000A6 00312		HVC \$CONTCOH*2(2),HCONTROL RESET COMUTATOR	F4230000
2268		000308	47F0 0258		B \$CONTROL AND TRY NEXT TANK	F4232000
2269	NTANK				\$ACON 0 TANK REG STORAGE	F4234000
2271	HTEHP				DC H'0' TEMP STORAGE (HI-BYTE ALWAYS ZERO)	F4236000
2272	HREPUTA				DC S(HREPUT) COMUTATOR ADJUSTMENT ADDR	F4238000
2273	HCONTROL				DC S(\$CONTROL) COMUTATOR ADJUSTMENT ADDR	F4240000
2274	HCONTTAB				NULL * CONTROL TYPE BRANCH TABLE	F4242000
2276 *					DC S(HC0) RESERVED	F4244000
2277					DC S(HC1) 001 START FUNCTION REQUEST	F4246000
2278					DC S(HC2) 010 START FUNCTION PERMISSION	F4248000
2279					DC S(HC3) 011 RESERVED	F4250000
2280					DC S(HC4) 100 RESERVED	F4252000
2281					DC S(HC5) 101 RESERVED	F4254000
2282					DC S(HC6) 110 RESERVED	F4256000
2283					DC S(HC7) 111 GENERAL CONTROL TYPE	F4258000

F15OCT70 11/21/72

LOC	OBJECT CODE	ADDR1	ADDR2	SPRT	SOURCE STATEMENT	
1					F150CT70 11/21/72	
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HB2 \$ C O N T R O L -- INPUT CONTROL RECORD PROCESSOR

1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20	21	22	23	24	25	26	27	28	29	30	31	32	33	34	35	36	37	38	39	40
LCC	OBJECT CODE	ADDR1	ADDR2	STMT	SOURCE STATEMENT																																		
0002F4		2336	HC3	EQU	HEXIT																																		
		2338 *																																					
		2339 *	HC4		CONTROL RECORD, TYPE = 100 (RESERVED)																																		
		2340 *																																					
0002F4		2342	HC4	EQU	HEXIT																																		
		2344 *																																					
		2345 *	HC5		CONTROL RECORD, TYPE = 101 (RESERVED)																																		
		2346 *																																					
0002F4		2348	HC5	EQU	HEXIT																																		
		2349 *																																					
		2351 *																																					
		2352 *	HC6		CONTROL RECORD, TYPE = 110 (RESERVED)																																		
		2353 *																																					
		2355 *																																					
		2356 *																																					
		2357 *																																					
		2358 *																																					
0002F4		2359	HC6	EQU	HEXIT																																		
		2361 *																																					
		2362 *	HC7		CONTROL RECORD, TYPE = 111 (GENERALIZED CONTROL)																																		
		2363 *																																					
		2364 *																																					
		2366	HC7	EQU	HEXIT																																		
		2367 *																																					
		2368 *																																					
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		2370 *																																					
		2371 *																																					
		2372 *																																					
		2373 *																																					
		2374 *																																					

LCC	OBJECT CODE	ADDR1	ADDR2	STMT	SOURCE STATEMENT	F150CT70	11/21/72
2383	EA				SETA 0	F4442000	
2384	*					F4444000	
2385	*				BASE REGISTER ASSIGNMENT	F4446000	
2386	*					F4448000	
2387					USING TANKDSEC,RB	F4450000	
2388					AIP (6NUHPRT EQ 1).PSK1	F4452000	
2389	.PSK1				ANOP	F4456000	
2390	\$PRTN1				HULL	F4458000	
2392	PNEXT				EQU *	F4460000	
2393					\$GETNK P,RB,R9,EMPTY=HAI GET NEXT TANK TO PRINT	F4462000	
2406					CLI TANKCNT+1,0 TEST FOR END OF JOB	F4464000	
2407					AIP (6PRTCONS EQ 0).PSK5	F4466000	
2408	.PSK5				ANOP	F4502000	
2409					BE PPRE	F4504000	
2410	.PSK5A				ANOP	F4506000	
2411					CLI TANKSRCB,X'8E' LOOK FOR CHANGE IN FORMS	F4508000	
2412					AIF (6CONSOLE EQ 0).PSK5B	F4510000	
2413	.PSK5B				BNE **8	F4516000	
2414					HVI TANKSRCB,X'91' SKIP SRCB MODIFICATION IF NO	F4518000	
2415	.PSK5C				ANOP	F4520000	
2416					HVC PCTCCHCT+1(1),TANKCNT+1 SET THE LINE IN CCH	F4522000	
2417	*					F4524000	
2418	*				SET UP CARRIAGE CONTROL	F4526000	
2419	*					F4528000	
2420					AIP (6MACHINE EQ 20).PSK2	F4530000	
2421	.PSK2				ANOP	F4562000	
2422					HVC PCIO+3(1),TANKSRCB PICK UP CARRIAGE CONTROL INFO	F4564000	
2423					HVI PCIO+1,X'44' RESET CONTROL OPCODE	F4566000	
2424					TR PCIO+3,PSK1NH CK FOR SKIP IMMEDIATE	F4568000	
2425					BO PSKIM IF YES GO AROUND DELAY SETTING	F4570000	
2426					OI PCIO+1,X'02' SET SKIP AFTER PRINT 44=46	F4572000	
2427	PSKIM				TR PCIO+3,X'10' CK FOR SKIP NOT SPACE	F4574000	
2428					BZ PSK1NS IF OFF SPACE	F4576000	
2429					OI PCIO+1,X'01' SET SKIP 44=45, 46=47	F4578000	
2430	PSK1NS				AH RB,=(Y(TANKDATA-TANKDSEC) GET TO DATA ADDRESS	F4580000	
2431					STH RB,PCTCCH+2 SAVE ADDRESS IN XIO INST	F4582000	
2432					OI PCTCCH+1,X'01' SPACE SUPPRESS BIT ON	F4584000	
2433					TR PCIO+3,X'3F' DO WE SPACE SUPPRESS	F4586000	
2434					BZ PLINE IF SO SKIP OVER CARRIAGE CONTROL	F4588000	
2435					BI PCTCCH+1,255-X'01' TURN SPACE SUPPRESS OFF	F4590000	
2436					3DELAY P,TYPE=SERUP PREPARE FOR WAITING	F4592000	
2439	PCIO				CIO *-8,-*- PERFORM CARRIAGE CONTROL OPERATION	F4594000	
2444					BC 7,\$PCONHI+4 WAIT AND TRY LATER IF NO START	F4596000	
2445					CHECK P WAIT FOR FINISH	F4598000	
2451	.PSK3				ANOP	F4600000	
2452	PLINE				WRITE P,S=PCTCCN PRINT THE LINE	F4602000	
2461					CHECK P CHECK FOR FINISHED	F4604000	
2467					SLOAD RB,PCTCCH+8-6L PICK UP AREA FROM CCH	F4606000	
2469					SH RB,=(Y(TANKDATA-TANKDSEC) GET TO START OF TANK	F4608000	
2470	PPREZ				SPRZE \$TANKPOL,RB,ENBL RELEASE THE TANK ENABLED	F4610000	
2475					B PNEXT PRINT NEXT LINE	F4612000	
2476					AIP (6NUHPRT EQ 1).PRTN1	F4614000	
2477	.PRTN1				ANOP	F4618000	
2478					SENDPROC.P END OF PRINT PROCESSOR	F4620000	
2484	*					F4622000	

LCC	OBJECT CODE	ADDR1	ADDR2	STRT	SOURCE STATEMENT	F150CT70	11/21/72
				2485 #	EQUATES FOR PRINT PROCESSOR		F4624000
				2486 #			F4626000
000020				2487 PSKTRM EQU X'20'	SRCB FLAG TO SKIP IMMEDIATE		F4628000
				2488 AIP (MACHINE EQ 20).PSK#			F4630000
				2489 .PSK# ANOP			F4642000
000084				2490 PCT EQU 132	LINE LENGTH AS ASSEMBLED		F4644000
0003FE				2491 PCTCCWCT EQU PCTCCW+4	LOCATION OF COUNT FIELD		F4646000
000080				2492 PCTRTN EQU \$PCOMN1+4	LOCATION OF NEXT COMPARATOR ENTRY		F4648000
				2493 .PRNSKP ANOP			F4650000
				2494 AIP (ENUMDR EQ 0).RTNSKP			F4652000

LOC	OBJECT CODE	ADDR1	ADDR2	START	SOURCE STATEMENT	11/21/72
2496	6A				SETA 0	F4656000
2497	*				INPUT SERVICE PROCESSOR	F4658000
2498	*					F4660000
2499	*					F4662000
2500	*				GENERATION LOGIC VARIABLE HEAVINGS	F4664000
2501	*				ER(1) = 0 FOR 1442,2501,2520,2540,2560 READ DEVICES	F4666000
2502	*				ER(2) = 0 FOR 2520 READ/PUNCH DEVICES	F4668000
2503	*				ER(3) = 0 FOR 1442 READ/PUNCH DEVICES	F4670000
2504	*				ER(6) = 0 FOR MOD 20	F4672000
2505	*				ER(7) = 0 FOR 2560 READ/PUNCH DEVICE	F4674000
2506					AIP (CNURDR EQ 1).RRTN1	F4676000
2507	.RRTN1				(6R(7)).RRTN2	F4680000
2508	.RRTN2				ANOP	F4684000
2509	SRRTN1				NULL	F4686000
2511	RDSTART				DS OH	F4688000
2512					AIP (6P(2) AND 6R(3) OR NOT 6R(6)).RRTN18	F4690000
2513	.RRTN18				ANOP	F4694000
2514	READ1				SRAD R, RCTDTA1	F4696000
2522	RCK1				S\$CHECK R, RDERR1, RDEOP1 MAKE SURE IN OK	F4698000
2534					AIP (6R(2) AND 6R(3) OR 6R(1)).RRTN3	F4700000
2535	.RRTN3				AIP (6R(2) AND 6R(3)).RRTN4	F4706000
2536	.RRTN4				AIP (6R(2) OR 6R(3)).RRTN5	F4712000
2537	.RRTN5				AIP (6R(2) OR NOT 6R(6)).RRTN6	F4718000
2538	.RRTN6				ANOP	F4724000
2539	ROPEN				\$LA RB, RCTANK1 LOCATE TANK IN PARAMETER REG	F4726000
2541					AIP (6H0E EQ 1).RH0E	F4728000
2542					R14, \$TPOPE	F4730000
2546					BZ RREOPEN	F4732000
00045C	4780 0522				AIP (6LOCCOH EQ 0).RRTN7A	F4734000
2547					AIP (6MACHINE EQ 20).RRTN7	F4738000
2548	.RRTN7A				ANOP	F4748000
2549	.RRTN7				ANOP	F4750000
2550					\$DELAY R, TIME=LONG, TYPE=BRANCH WAIT FOR HASP TO RESPOND	F4752000
2555	.RRTN8				AIP (6LOCCOH EQ 0).RRTN8A	F4756000
2556	.RRTN8A				ANOP	F4758000
2557	*				WE MUST GO BACK TO COMMUTATOR FOR GATE TO BE OPENED	F4760000
2558	.RH0E				ANOP	F4762000
2559	RDLOOP				DS OH	F4764000
2560					\$READ R, RCTDTA2	F4766000
2568					MVC RCTTCT1,=H'80'	F4768000
2569					\$PUT R, RCTANK1	F4770000
2581					\$CHECK R, RDERR, RDEOP	F4772000
2593					\$READ R, RCTDTA1	F4774000
2601					MVC RCTTCT2,=H'80'	F4776000
2602					\$PUT R, RCTANK2	F4778000
2614	RCHECK				DS OH	F4780000
2615					\$CHECK R, BDERR, RDEOP	F4782000
2627					B RDLOOP	F4784000
2628	RDOP				MVC RCTTCT1,=F'0'	F4786000
2629					\$DELAY R, TYPE=SETUP	F4788000
2632					\$LA RB, RCTANK1	F4790000
2634					BAS R14, \$TTPUT	F4792000
2638					RCTRN	F4794000
2639					AIP (6P(2) AND 6R(3)).RRTN9	F4800000
2680	.RRTN9				AIP (6MACHINE EQ 20).RRTN10	F4814000
2641	.RRTN10				AIP (6P(7)).RRTN14	

-(6+7) 7

LCC	OBJECT CODE	ADDR1	ADDR2	SPRT	SOURCE STATEMENT	F15CCY70	11/21/72
				2642	.RRTN14 ANOP		F4820000
	000506	47F0	0432	2643	B RDSTART	-7	F4822000
				2644	.RRTN15 ANOP	GO BACK TO START OF PROCESSOR	F4824000
				2645	RDERR	1+2+3+6+7	F4826000
	00051E	47F0	04D8	2648	\$READ R,RCTTDWAY	PREPARE TO WAIT ON OPERATOR	F4828000
				2656	B RCHECK	READ AGAIN	F4830000
				2657	RREOPEN	SEE IP IN PROPERLY	F4832000
				2651	RDERR1	\$DELAY R, TIME=SHORT, TYPE=BRANCH, PREVLOC=OPEN WAIT, TRY AGAIN	F4834000
				2655	AIF	\$INTREQ R,RDR, PREVLOC=READ1 PREPARE TO WAIT ON OPERATOR, READ	F4836000
				2666	.RRTN12 AIF	(CR(7)).RRTN12	F4908000
				2667	.RRTN12A ANOP	(NOT CR(6)).RRTN12A	F4910000
	000432			2668	RDEOF1 EQU	RDSTART	F4912000
				2659	.RRTN13 AIF	(CR(2) AND CR(3)).RRTN16	F4914000
				2670	.RRTN16 ANOP		F4916000
				2671	AIF	(ENDRDR EQ 1).RRTN17	F4918000
				2672	.RRTN17 ANOP		F4920000
				2673	SENDPROC R	END OF READ PROCESSOR	F4980000
				2686	AIF	(MACHINE NE 20).RRTNSKP	F4982000
				2687	*		F4984000
				2688	*	INPUT PROCESSOR EQUATES	F4986000
				2689	*		F4988000
	000050			2690	RCT EQU	80	F4990000
	0000B4			2691	RCTRTH EQU	SRCONH1+4	F4992000
				2692	.RRTNSKP ANOP	LOCATION OF NEXT COMMUTATOR ENTRY	F4994000
				2693	AIF	(ENDMPUN EQ 0).RRTNSKP	F4996000

LCC	OBJECT CODE	ADDR1	ADDR2	STMT	SOURCE STATEMENT	F150CT70	11/21/72
				2695 6A	SETA 0		F5000000
				2696 *			F5002000
				2697 *	PUNCH SERVICE PROCESSOR		F5004000
				2698 *			F5006000
				2699 *	GENERATION LOGIC VARIABLE HEATINGS		F5008000
				2700 *	ER(2) = 0 FOR 2520 READ/PUNCH DEVICES		F5010000
				2701 *	ER(3) = 0 FOR 1442 READ/PUNCH DEVICES		F5012000
				2702 *	ER(4) = 0 FOR 2540 PUNCH DEVICES		F5014000
				2703 *	ER(5) = 0 FOR 2520,2560 PUNCH DEVICES		F5016000
				2704 *	ER(6) = 0 FOR MOD 20		F5018000
				2705 *	ER(7) = 0 FOR 2560 READ/PUNCH DEVICE		F5020000
	000000			2706 *	ER(9) = 0 FOR 1442 SINGLE POCKET PUNCH DEVICES		F5022000
				2707	USING TANKDSEC,R8		F5024000
				2708	AIP (GNURPUN EQ 1).URTN1		F5026000
				2709	ANOP		F5030000
				2710	START		F5032000
	00054C			2712	USRTM1		F5034000
				2713	DS OH		F5036000
	000580	9500	8005	2713	GETTNK U,R8,R9,EMPTY=HALT		F5038000
	000584	4780	058C	2726	LOOP ENTRY TO CONTINUE PUNCHING		F5040000
	000588			2727	CLI TANKCNT+1,0		F5042000
				2727	TEST FOR END OF JOB		F5044000
				2728	BE UPFREE		F5046000
				2729	IF SO FREE TANK		F5048000
				2729	PUNCH THE CARD		F50504000
				2741	WRITE U,TANKDATA,TANKDSEC,OP=(UCTALTOP,D),CTADDR=TANKCNT,		F5046000
				2742	S=UCTCCX		F5048000
				2743	PUNCH THE CARD		F50504000
				2744	ER(4) OR ER(2) AND ER(3) AND ER(5) AND ER(8) AND ER(8).URTN2		F5096000
				2744	ER(4).URTN3		F5100000
				2753	CHECK U,USER		F5102000
				2755	SLOAD R8,UCTCCX+4-6L		F5108000
	000588	4880	1316	2755	R8,=Y(TANKDATA-TANKDSEC) GET TO START OF TANK		F5116000
				2756	ANOP		F5120000
				2757	UPFREE		F5126000
				2762	FREE STANKPOL,R8,ENBL		F5108000
				2763	ER(2) AND ER(3) OR ER(5) AND ER(8) AND ER(4).URTN5		F5106000
				2764	ER(2) AND ER(3).URTN6		F5116000
				2765	ER(4) OR ER(2) AND ER(3) AND ER(3) AND ER(5) AND ER(8).URTN7		F5120000
				2766	ER(4).URTN8		F5126000
	0005CA	47F0	054C	2767	NOT(ER(2) AND ER(3)) AND ER(4) AND ER(5) AND ER(8).UR9		F5130000
				2768	UR9		F5136000
				2769	NOT(ER(8) AND ER(2) AND ER(3) OR ER(5)).URTN10		F5138000
	0005CE			2770	URR		F5152000
				2771	URTN11		F5154000
				2772	ER(2) AND ER(3) OR ER(5) OR ER(8).URTN12		F5156000
				2773	ER(2) AND ER(3) AND ER(5) OR ER(8).URTN13		F5162000
				2774	ER(8).URTN14		F5172000
				2777	SIMREQ U,PUN,PREVIC=OPUNCH		F5174000
				2778	ER(2) AND ER(3) OR ER(5) OR NOT ER(8).URTN14		F5176000
				2779	ER(2) AND ER(3).URTN15		F5182000
				2780	(GNURPUN EQ 1).URTN16		F5224000
				2781	ANOP		F5228000
				2785	SENDPROC U		F5230000
				2786 *	AIP (SMACHINE HE 20).URTN17		F5232000
				2787 *	PUNCH PROCESSOR EQUATES		F5234000
				2788 *			F5236000
	000050			2789	UCT		F5240000
	000088			2790	UCTRN		F5242000

LENGTH OF CARD
LOCATION OF NEXT COORDINATOR ENTRY

LOC	OBJECT CODE	ADDR1	ADDR2	STRT	SOURCE STATEMENT	
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P15OCT70 11/21/72
 F2244000
 F5246000
 F5624000
 F5626000
 F5780000
 F5782000

2791 .URTSKP ANOP
 2792 .AIF (SCORSOLE EQ 0 AND PERSONS EQ 0).PRTSKP
 2793 .PRTSKP ANOP
 2794 .AIF (CLOCCH EQ 0).WXX1
 2795 .WXX1 ANOP
 2796 .PRINT GEN

LOC	OBJECT CODE	ADDR1	ADDR2	SINH	SOURCE STATEMENT	PC
2798 *						F5786000
2799 *					SPUT ROUTINE	F5788000
2800 *					INTERFACE WITH TTPUT	F5790000
2801 6A						F5792000
2802 0XDUH					SETA 0	F5794000
2803					DSECT	F5796000
2804 .0XB					AIF (6MACHINE EQ 20).0XB	F5802000
2805 0XTNK					ANOP	F5804000
2806 0XTCT					DS H	F5806000
2807 0XBADRET					DS H	F5808000
2808 0XGDRET					DS 2H	F5810000
2809 .0XAR					ANOP	F5812000
2810 HASRPTP					CSECT	F5814000
2811					USING 0XDUH,R14	F5816000
2812					USING TANKDSEC,R8	F5818000
2813					AIF (6MACHINE EQ 20).0X1	F5820000
2814 .0X1					ANOP	F5836000
2815 *					R14 POINTS TO USER CALLING SEQ WITH TANK AND TCT ADDRESS	F5838000
2816					USING TCTDSECT,R9	F5840000
2817 SPUT					NULL	F5842000
2818 SPUT					DS OH	F5844000
2819	0131C				AH R14,=H'4'	F5846000
2820	00500				STH R14,0XSAY	F5848000
2821	00000				LH R8,0XTNK	F5850000
2822	00002				LH R9,0XTCT	F5852000
2823 .0XAY					AIF (6CONSOLE EQ 0).0X2	F5854000
2824 .0X2					ANOP	F5906000
2825 0XPUT					BAS	F5908000
2826 0XPUT					DS OH	F5910000
2827+					DC X'4D'	F5912000
2828+					DC AL1(R14*16)	F5914000
2829+					DC S(TTPUT)	F5916000
2830					\$LOAD R14,0XSAY	F5918000
2831+	00600				LH R14,0XSAY	F5920000
2832					AIF (6MACHINE EQ 20).0X7	F5932000
2833	00002				LH R9,0XTCT	F5946000
2834 .0X7					ANOP	F5948000
2835	00008				BNE 0XGDRET	F5950000
2836					AIF (6CONSOLE EQ 0).0X8	F5952000
2837 .0X8					AIF (6MACHINE EQ 20).0X9	F5954000
2838 .0X9					ANOP	F5956000
2839	00004				B	F5946000
2840 0XSAY					DC R'0'	F5948000
2841 SPUTA					STH R14,0XSAY	F5950000
2842	00600				LH R8,0XTNK	F5952000
2843	00000				BAS R14,STPREPUT	F5954000
2844					DC Y'4D'	F5956000
2845					DC AL1(R14*16)	F5946000
2846+					DC S(TPREPUT)	F5948000
2847	00600				LH R14,0XSAY	F5950000
2848	00004				BE 0XBADRET	F5952000
2849 .0XA9					ANOP	F5954000
2850	00008				B	F5956000
2851					DROP R14,RE	F5958000
2852					AIF (6MACHINE EQ 20).0XC	F5960000
					RETURN	F5962000
						F5964000
						F5966000
						F5968000

LCC OBJECT CODE		ADDR1	ADDR2	STMT	SOURCE STATEMENT	F15OCT70	11/21/72
000000				2859	USING LOGDSECT,R15		F6018000
				2860	NULL		F6020000
00061A				2861+\$LOG	DS OR		
				2862	STO R14,LOG14SAV		F6022000
00061A	40E0	0648		2863+	STH R14,LOG14SAV	SAVE R14	
				2864	\$STO R15,LOGRTN	STORE RTN REG	F6024000
00061E	4CP0	064A		2865+	LH R15,0(0,R15)	PICK UP ERROR ENTRY OFFSET	F6026000
000622	48P0	0000		2866	\$AA R15,ERRTAB	ADD ERROR TABLE ORIGIN	F6028000
				2867	AH R15,=Y(SENRTAB)		
000626	4AF0	131E		2868+	LH R14,LOGCOUNT	LOAD ERROR COUNT	F6030000
00062A	48E0	0002		2869	AH R14,=H'1'	UP COUNT BY ONE	F6032000
00062E	4AE0	1320		2870	STH R14,LOGCOUNT	STORE NEW COUNT	F6034000
000632	40E0	0002		2871	AIP (6TRACE EQ 0).LNOTRC		F6036000
				2872	AIP (6CONSOLE EQ 0 AND 6PRCONS EQ 0).LOGNOT		F6056000
				2873	.LNOTRC		F6089000
				2874	.LOGNOT	RESTORE R14	F6090000
000636	48E0	0648		2875	LRTN		
				2876+LRTN	\$LOAD R14,LOG14SAV	PICK UP CALLER	F6092000
00063A	48P0	064A		2877	LH R14,LOG14SAV		F6094000
00063E	D203	00C4	1308	2878+	LH R15,LOGRTN	ZERO OUT LOG INFO FOR NEXT LOG	F6096000
000644	47F0	0002		2879	HVC \$LOGINFO,=F'0'	RETURN	F6098000
				2880	B 2(0,R15)		
000648	0000			2881	LOG14SAV \$ACON		F6100000
				2882+LOG14SAV	DC Y(0)		
00064A	0000			2883	LOGRTN \$ACON		F6102000
				2884+LOGRTN	DC Y(0)		
000000				2885	\$LOGCLAS EQU 6LOGCLAS		F6104000
					\$LOGCLAS EQU 0		
				2886	DROP R15		F6106000

LOC	OBJECT CODE	ADDR1	ADDR2	STMT	SOURCE STATEMENT	PI5OCT70	11/21/72
2888 *					NULL		F61090000
2889 *					ENTRY - \$TTPUT		F61100000
2890 *					REGISTERS - R9=RECORD TANK 2 (R8)=RCH,3 (R8)=SECB		F61120000
2891 *					R14=RETURN ADDR, CC=0 - RECORD NOT TAKEN		F61140000
2892 *					CC=BE,0-RECORD ACCEPTED		F61160000
2893 *					R15 IS CONSIDERED VOLITILE		F61180000
2894 *							F61200000
2895 *							F61220000
2896	\$TTPUT				NULL		F61240000
2897	\$TTPUT				DS		F61260000
2898	\$TRACE				SSH=YES		F61280000
2899	\$STO	R14,OSAVR14			SAVE RETURN		F61300000
2900	\$STH	R14,OSAVR14					F61320000
2901	\$STH	R10,OSAVR10					F61340000
2902	\$STH	R9,OSAVR9			SAVE INPUT TANK ADDR		F61360000
2903	\$STO	R8,0INADD					F61380000
2904	\$STO	R8,0INADD					F61400000
2905	LH	R15,=H*1			CONSTANT FOR SPEED		F61420000
2906	LLOAD	R10,0INADD			COMPRESSION WORK AREA		F61440000
2907	LH	R10,0INADD					F61460000
2908	AH	R10,=Y(TANKRCB-TANKDSEC)			SKIP TO CTL BYTES		F61480000
2909	USING	TANKDSEC,R8					F61500000
2910	LH	R9,TANKCNT			TANK DATA COUNT		F61520000
2911	CH	R9,=H*0			IS THIS A NULL RECORD		F61540000
2912	BE	0E0INPUT			BR IF YES TO ADD TO BUFFER		F61560000
2913	AIF	(EXCTYPE LE 1).0NOCOMP			TEST NONE OR TRAILING COMP		F61580000
2914	AR	R9,R8			INCLUDE TANK ADDR		F61600000
2915	\$STO	R9,0INEND			TO SAFE STORAGE		F61620000
2916	\$STH	R9,0INEND					F61640000
2917	CLI	OTS (R8),2			IS THIS A TEXT CARD		F61740000
2918	BNE	0G0A			BR IF NO		F61760000
2919 *					SKIP ATTEMPTING TO COMPRESS A TEXT CARD		F61780000
2920	LH	R9,TANKCNT			INPUT COUNT		F61800000
2921	\$LTR	R11,R8			INPUT ADDR		F61820000
2922 *	SR	R11,R11					F61840000
2923 *	AR	R11,B8					F61860000
2924	AR	R8,R9			END OF RECORD		F61880000
2925	AH	R9,0D200			COUNT FORMAT		F61900000
2926	B	OSQUEEZE			GO PROCESS RECORD		F61920000
2927	0G0A						F61720000
2928	0G0A						F61740000
2929	NVI	OTS (R9),0			SETUP ENDING CHARACTER		F61760000
2930	AIP	(EXCTYPE EQ 2).0H88					F61780000
2931	CLI	OTS-1(R9),0			DOES ENDING MATCH LAST DATA CHAR		F61800000
2932	BNE	*+8			BR IF NOT		F61820000
2933	NVI	OTS (R9),255			YES...USE ANOTHER		F61840000
2934	AVOP						F61860000
2935	HVC	OTS+1(6CCT-1,R9),OTS (R9)			PROPAGATE FOR DUPLICATION		F61880000
	HVC	OTS+1(R-1,R9),OTS (R9)			PROPAGATE FOR DUPLICATION		F61900000
2936	0G0						F61920000
2937	0G0						F61940000
2938	SLA	R14,0G01			LOAD FOR SPEED		F61960000
2939 *	LH	R14,=Y(0G01)					F61980000
2940	SLA	R13,OSQUEEZE			LOAD FOR SPEED		F62000000
2941 *	LH	R13,=Y(OSQUEEZE)					F62020000

LOC	OBJECT CODE	ADDR1	ADDR2	STRT	SOURCE STATEMENT	F150CTY70
1	0006B2	4890	089A	2942	LH R9,OD200	11/21/72
2	0006B6	18BB		2943	\$LTR R11,R8	F6194000
3	0006B8	18BB		2944*	SR R11,R11	F6196000
4				2945*	AR R11,R8	
5	0006BA			2946	OG01 NULL	F6198000
6				2947*	OG01 DS OH	
7				2948	AIP (ECHTYPE NE 2).ONB1	F6200000
8				2949	.ONB1 ANOP	F6206000
9				2950		F6208000
10	0006BA	D502	8006	8007	00006	00007
11	0006C0	078D		2951	CLC OTS*(SCCT-1,R8),OTS*(R8) CHECK FOR COMPRESSABILITY	F6210000
12	0006C2	1A8F		2952	BCR B,R13 BR IF COMPRESSABLE (TO OSQUEEZE)	F6212000
13	0006C4	1A9F		2953	AR R8,R15 UP DATA PTR	F6214000
14	0006C6	07PE		2954	AR R9,R15 AND CHAR COUNT	F6216000
15				2955	BR R14 CONTINUE (TO OG01)	F6218000
16				2956	.ONB2 AYP	F6238000
17				2957	* ANOP	F6240000
18				2958	* OSQUEEZE - 8CCT IDENTICAL CHARACTERS FOUND	F6242000
19				2959	*	F6244000
20				2960	*	F6246000
21	0006C8	4990	089A	2961	OSQUEEZE NULL	F6248000
22	0006CB	4780	06F4	2962	* OSQUEEZE DS OH	
23	0006D0	4990	089C	2963	CH R9,OD200	F6250000
24	0006D4	4720	076C	2964	BE OCHPST IS A CHARACTER STRING ACTIVE	F6252000
25	0006D8	4090	06DC	2965	CH R9,OD263	F6254000
26	0006DC	D200	A003	8006	00003	8006
27	0006E2	D200	A002	06DD	00002	06DD
28	0006E8	96C0	A002	2967	OH R9,*+4	F6256000
29	0006EC	4890	089A	2968	HVC 3(*,*,R10),OTS (R11) MOVE CHAR STRING (*1)	F6258000
30	0006F0	1A89		2970	OI 2(R10),X*CO	F6260000
31	0006F2	1A8F		2971	SH R9,OD200	F6262000
32				2972	AR R10,R9	F6264000
33				2973	AR R10,R15	F6266000
34	0006F4	4980	0892	2974	OCHPST NULL *	F6268000
35	0006F8	4790	0786	2975	*DCHPST DS OH	F6270000
36	0006FC			2976	CH R8,OIBEND	F6272000
37				2977	BFL OBOINPUT	F6274000
38				2978	OCOMP DS OH	F6276000
39	0006FC	48E0	1328	2979	*OCOMP SLA R14,OCOMP1	F6278000
40				2980	LH R14,*Y(OCOMP1) FOR LOOP SPEED	F6280000
41	000700	48D0	132A	2981	SLA R13,OCHPSTOP FOR LOOP SPEED	F6282000
42	000704	4890	132C	2982	LH R13,*Y(OCHPSTOP)	
43				2983	LH R9,*Y(SCCT) START COMPRESSION COUNTER	F6284000
44	000708			2984	LH R9,*Y(4) START COMPRESSION COUNTER	F6286000
45				2985	OCOMP1 NULL *	
46				2986	*OCOMP1 DS OH	
47				2987	AIP (ECHTYPE NE 2).ONB3	F6288000
48				2988	.ONB3 ANOP	F6294000
49	000708	D500	8009	800A	00009	0000A
50	00070E	077D		2989	CLC (SCCT+OTS-1)(T,R8),(SCCT+OTS)(R8) DOES HATCH CONTINUE	F6296000
51	000710	1A9F		2990	.ONB6 ANOP	
52	000712	1A8F		2991	BCR R13 BR IF NO (TO CHPSTOP)	F6298000
53				2992	AR R9,R15 ANOTHER MATCH... COUNT IT	F6300000
54				2993	AR R8,R15 UP TO NEXT CHAR	F6302000
55						F6304000

LOC	OBJECT CODE	ADDR1	ADDR2	STRT	SOURCE STATEMENT	F150CT70	11/21/72
00078C	4880	0890	00890	3044	SLOAD R8, OINADD	STARTING ADDR OF COMPRESSED REC	F6862000
000790	18A8			3045+	LH R8, OINADD		
000792	48A0	1334	01334	3046	SR R10, R8	REDUCE TO ACTUAL COUNT	F6864000
000796	40A0	8000	00000	3047	SH R10, *Y(L, TANKCHR-2)	COMPENSATE FOR FULL CHAIN WORD	F6866000
00079A				3048	STH R10, TANKCHR	SAVE COUNT IN TANK FOR \$TREPBUY	F6868000
00079B				3049	OREENT NULL	BE-ENTRY POINT FROM \$TREPBUY	F6870000
00079C				3050+	OREENT DS		
00079D				3051	SLOAD R9, OBUFPTR	GET ADDR OF ACTIVE BUFFER	F6872000
00079E	4890	0896	00896	3052+	LH R9, OBUFPTR		
0007A0	4990	1314	01314	3053	CH R9, =H'0'	IS THERE ONE	F6874000
0007A2	4780	07F4	007F4	3054	BE OGETBUY	BR IF NO	F6876000
0007A6				3055	OBUFOK NULL	VALID BUFFER	F6878000
0007A8				3056+	OBUFOK DS		
0007A9	49A0	0898	00898	3057	CH R10, OBUFCNT	WILL THIS RECORD YIF...	F6880000
0007AA	4720	0846	00846	3058	BH OBUFULL	BR IF NO	F6882000
0007AE	40A0	0248	00248	3059	STH R10, \$TEMP	FOR STC	F6884000
0007B2	D200	07B9	00249	3060	HVC **7(1), \$TEMP+1	SET IN MOVE	F6886000
0007B8	D200	9000	8002	3061	NVC O!*-*, R9), L, TANKCHR(R8)	MOVE RECORD	F6888000
0007BE	1A9A		00002	3062	AR R9, R10	UPDATE CURRENT PTR	F6890000
0007C0	4090	0896	00896	3063	\$STO R9, OBUFPTR	AND RESET	F6892000
0007C4	4890	0898	00898	3064+	STH R9, OBUFPTR	REMAINING COUNT	F6894000
0007C8	1B9A			3066	SR R9, OBUFCNT	REDUCE BY THIS RECORD	F6896000
0007CA	4090	0898	00898	3067	STH R9, OBUFCNT	AND RESET	F6898000
0007CE	49A0	1336	01336	3068	CH R10, =H'3'	WAS THIS A NULL RECORD	F6900000
0007D2	4780	0842	00842	3069	BF OPLUSH	BR IF YES TO WRITE BUFFER	F6902000
0007D6	9592	8002	00002	3070	CLI TANKFCB, \$ORCB(1)	IS THIS OPER CHD TO HASP	F6904000
0007DA	4780	0842	00842	3071	CLI TANKRCB, 146	IS THIS OPER CHD TO HASP	F6906000
0007DE				3072	ORETOK NULL	BR IF YES TO SEND BUFFER	F6908000
0007DE	96F0	0867	00867	3073+	ORETOK DS	POSITIVE RETURN ENTRY	F6910000
0007E2	4880	0890	00890	3074	OI OPLSH*1, X'F0'	OPEN NORMAL GATE AND SET COND CODE	F6912000
0007E6	4890	088A	0088A	3075	ORETURN NULL	RETURN--COND. CODE ALREADY SET	F6914000
0007EA	48A0	088C	0088C	3076+	ORETURN DS		F6916000
0007EE	48E0	088E	0088E	3077	ORETURN LH	RESTORE TANK ADDR	F6918000
0007F2	07FE			3078	ORETURN LH	RESTORE	F6920000
0007F4	1899			3079	ORETURN LH	RESTORE	F6922000
0007F6	4700	07E2	007E2	3080	ORETURN LH	RESTORE	F6924000
0007FA	000007FE			3081+	ORETURN LH	RESTORE	F6926000
0007FA	8100			3082	ORETURN LH	RESTORE	F6928000
0007FE	00000802			3083	ORETURN LH	RESTORE	F6930000
000802	9500	024A	0024A	3084+	ORETURN LH	RESTORE	F6932000
000806	4780	0814	00814	3085	ORETURN LH	RESTORE	F6934000
00080A	4890	024A	0024A	3086	ORETURN LH	RESTORE	F6936000
00080E	D201	024A	9000	3087	ORETURN LH	RESTORE	F6938000
000814	9231	0824	00824	3088+	ORETURN LH	RESTORE	F6940000
000814				3089+	ORETURN LH	RESTORE	F6942000
000814				3090+	ORETURN LH	RESTORE	F6944000
000814				3091+	ORETURN LH	RESTORE	F6946000
000814				3092+	ORETURN LH	RESTORE	F6948000
000814				3093+	ORETURN LH	RESTORE	F6950000
000814				3094+	ORETURN LH	RESTORE	F6952000
000814				3095+	ORETURN LH	RESTORE	F6954000
000814				3096+	ORETURN LH	RESTORE	F6956000
000814				3097+	ORETURN LH	RESTORE	F6958000
000814				3098+	ORETURN LH	RESTORE	F6960000
000814				3099+	ORETURN LH	RESTORE	F6962000
000814				3100+	ORETURN LH	RESTORE	F6964000
000814				3101+	ORETURN LH	RESTORE	F6966000
000814				3102+	ORETURN LH	RESTORE	F6968000
000814				3103+	ORETURN LH	RESTORE	F6970000
000814				3104+	ORETURN LH	RESTORE	F6972000
000814				3105+	ORETURN LH	RESTORE	F6974000
000814				3106+	ORETURN LH	RESTORE	F6976000
000814				3107+	ORETURN LH	RESTORE	F6978000
000814				3108+	ORETURN LH	RESTORE	F6980000
000814				3109+	ORETURN LH	RESTORE	F6982000
000814				3110+	ORETURN LH	RESTORE	F6984000
000814				3111+	ORETURN LH	RESTORE	F6986000
000814				3112+	ORETURN LH	RESTORE	F6988000
000814				3113+	ORETURN LH	RESTORE	F6990000
000814				3114+	ORETURN LH	RESTORE	F6992000
000814				3115+	ORETURN LH	RESTORE	F6994000
000814				3116+	ORETURN LH	RESTORE	F6996000
000814				3117+	ORETURN LH	RESTORE	F6998000
000814				3118+	ORETURN LH	RESTORE	F7000000
000814				3119+	ORETURN LH	RESTORE	F7002000
000814				3120+	ORETURN LH	RESTORE	F7004000
000814				3121+	ORETURN LH	RESTORE	F7006000
000814				3122+	ORETURN LH	RESTORE	F7008000
000814				3123+	ORETURN LH	RESTORE	F7010000
000814				3124+	ORETURN LH	RESTORE	F7012000
000814				3125+	ORETURN LH	RESTORE	F7014000
000814				3126+	ORETURN LH	RESTORE	F7016000
000814				3127+	ORETURN LH	RESTORE	F7018000
000814				3128+	ORETURN LH	RESTORE	F7020000
000814				3129+	ORETURN LH	RESTORE	F7022000
000814				3130+	ORETURN LH	RESTORE	F7024000
000814				3131+	ORETURN LH	RESTORE	F7026000
000814				3132+	ORETURN LH	RESTORE	F7028000
000814				3133+	ORETURN LH	RESTORE	F7030000
000814				3134+	ORETURN LH	RESTORE	F7032000
000814				3135+	ORETURN LH	RESTORE	F7034000
000814				3136+	ORETURN LH	RESTORE	F7036000
000814				3137+	ORETURN LH	RESTORE	F7038000
000814				3138+	ORETURN LH	RESTORE	F7040000
000814				3139+	ORETURN LH	RESTORE	F7042000
000814				3140+	ORETURN LH	RESTORE	F7044000
000814				3141+	ORETURN LH	RESTORE	F7046000
000814				3142+	ORETURN LH	RESTORE	F7048000
000814				3143+	ORETURN LH	RESTORE	F7050000
000814				3144+	ORETURN LH	RESTORE	F7052000
000814				3145+	ORETURN LH	RESTORE	F7054000
000814				3146+	ORETURN LH	RESTORE	F7056000
000814				3147+	ORETURN LH	RESTORE	F7058000
000814				3148+	ORETURN LH	RESTORE	F7060000
000814				3149+	ORETURN LH	RESTORE	F7062000
000814				3150+	ORETURN LH	RESTORE	F7064000
000814				3151+	ORETURN LH	RESTORE	F7066000
000814				3152+	ORETURN LH	RESTORE	F7068000
000814				3153+	ORETURN LH	RESTORE	F7070000
000814				3154+	ORETURN LH	RESTORE	F7072000
000814				3155+	ORETURN LH	RESTORE	F7074000
000814				3156+	ORETURN LH	RESTORE	F7076000
000814				3157+	ORETURN LH	RESTORE	F7078000
000814				3158+	ORETURN LH	RESTORE	F7080000
000814				3159+	ORETURN LH	RESTORE	F7082000
000814				3160+	ORETURN LH	RESTORE	F7084000
000814				3161+	ORETURN LH	RESTORE	F7086000
000814				3162+	ORETURN LH	RESTORE	F7088000
000814				3163+	ORETURN LH	RESTORE	F7090000
000814				3164+	ORETURN LH	RESTORE	F7092000
000814				3165+	ORETURN LH	RESTORE	F7094000
000814				3166+	ORETURN LH	RESTORE	F7096000
000814				3167+	ORETURN LH	RESTORE	F7098000
000814				3168+	ORETURN LH	RESTORE	F7100000
000814				3169+	ORETURN LH	RESTORE	F7102000
000814				3170+	ORETURN LH	RESTORE	F7104000
000814				3171+	ORETURN LH	RESTORE	F7106000
000814				3172+	ORETURN LH	RESTORE	F7108000
000814				3173+	ORETURN LH	RESTORE	F7110000
000814				3174+	ORETURN LH	RESTORE	F7112000
000814				3175+	ORETURN LH	RESTORE	F7114000
000814				3176+	ORETURN LH	RESTORE	F7116000
000814				3177+	ORETURN LH	RESTORE	F7118000
000814				3178+	ORETURN LH	RESTORE	F7120000
000814				3179+	ORETURN LH	RESTORE	F7122000
000814				3180+	ORETURN LH	RESTORE	F7124000
000814				3181+	ORETURN LH	RESTORE	F7126000
000814				3182+	ORETURN LH	RESTORE	F7128000
000814				3183+	ORETURN LH	RESTORE	F7130000
000814				3184+	ORETURN LH	RESTORE	F7132000
000814				3185+	ORETURN LH	RESTORE	F7134000
000814				3186+	ORETURN LH	RESTORE	F7136000
000814				3187+	ORETURN LH	RESTORE	F7138000
000814				3188+	ORETURN LH	RESTORE	F7140000
000814				3189+	ORETURN LH	RESTORE	F7142000
000814				3190+	ORETURN LH	RESTORE	F7144000
000814				3191+	ORETURN LH	RESTORE	F7146000
000814				3192+	ORETURN LH	RESTORE	F7148000
000814				3193+	ORETURN LH	RESTORE	F7150000
000814				3194+	ORETURN LH	RESTORE	F7152000
000814				3195+	ORETURN LH	RESTORE	F7154000
000814				3196+	ORETURN LH	RESTORE	F7156000
000814				3197+	ORETURN LH	RESTORE	F7158000
000814				3198+	ORETURN LH	RESTORE	F7160000
000814				3199+	ORETURN LH	RESTORE	F7162000
00							

HR2 \$ T P U T -- BUILDS BUFFERS FOR TRANSMISSION TO HASP

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LCC	OBJECT CODE	ADDR1	ADDR2	STMT	SOURCE STATEMENT
00087E	48F0 1320		01320	3152	LH R15,=R11
000882	48A0 8000		00000	3153	LH R10,TANKCHN
000886	47F0 079A		0079A	3154	B ORCENT
					CONSTANT
					COMPRESSED COUNT
					ENTER FLOW
					F6598000
					F6600000
					F6602000

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LCC	OBJECT CODE	ADDR1	ADDR2	STMT	SOURCE STATEMENT	ADDRESS
				3156 *		F6606000
				3157 *		F6608000
				3158 *		F6610000
				3159 *		F6612000
				3160 OSAVR9	SACON 0	F6614000
00088A	0000			3161 OSAVR9	DC Y(0)	
				3162 OSAVR10	SACON 0	F6616000
00088C	0000			3163 OSAVR10	DC Y(0)	
				3164 OSAVR14	SACON 0	F6618000
00088E	0000			3165 OSAVR14	DC Y(0)	
				3166 OINADD	SACON 0	F6620000
000890	0000			3167 OINADD	DC Y(0)	
				3168 OINEND	SACON 0	F6622000
000892	0000			3169 OINEND	DC Y(0)	
				3170 OACTBUF	SACON 0	F6624000
000894	0000			3171 OACTBUF	DC Y(0)	
				3172 OBUFPTR	SACON 0	F6626000
000896	0000			3173 OBUFPTR	DC Y(0)	
000898	0000			3174 OBUFPTR	DC H'0'	F6628000
00089A				3175 OD200	DS OH	F6630000
00089A	D200			3176	DC X'D200'	F6632000
00089C				3177 OD263	DS OH	F6634000
00089C	D23P			3178	DC X'D23P'	F6636000
000006				3179 OTS	EQU TANKDATA-TANKDSEC	F6638000
				3180	DROP R8	F6640000

HR2 \$ T P G E T --DEBLOCKS BUFFERS RECEIVED FROM HASP

F15OCT70 11/21/72

ICC	OBJECT CODE	ADDR1	ADDR2	STMT	SOURCE STATEMENT	
				3182 *		F6654000
				3183 *		F6656000
	00089E			3184 STPGET	NULL *	F6658000
	00089E	9200 00A9	000A9	3185 STPGET	DS OH	
				3186	MVI STGETCH+,0	ENTERED FROM COHUTATOR
	0008A2	48D0 1312	01312	3187	SLA R13,STCT1	CLOSE COHUTATOR
	000000			3188+	LH R13,=(STCT1)	BEGINNING OF TCT'S
				3189	USING TCTDSECT,R13	**
	0008A6			3190 GTEST	NULL *	F6678000
	000EA6	9140 D005	00005	3191 GTEST	DS OH	F6680000
	0008AA	4710 08BE	008BE	3192	TH TCTSTAT,TCTACT	IS ACTION REQUESTED
				3193	BO GSERVICE	BR IF YES
	0008AE			3194 GNEXTTCT NULL		F6682000
				3195 GNEXTTCT DS	OH	F6684000
				3196	SLOAD R13,TCTNEXT	F6686000
	0008AE	48D0 D000	00000	3197+	LH R13,TCTNEXT	TO NEXT TCT
				3198	S1TR R13,R13	
	0008B2	4AD0 1314	01314	3199+	AH R13,=H'0'	IS THIS END
	0008B6	4770 08A6	008A6	3200	BVZ GTEST	BR IF NO
				3201 *		F6690000
				3202 *		F6692000
				3203 *		F6694000
	0008BA	47F0 00AC	000AC	3204 GVAIT B	3TPGETCH*4	F6696000
					EXIT	F6698000
						F6700000

LCC	OBJECT CODE	ADDR1	ADDR2	SRHT	SOURCE STATEMENT	
3206 *					SERVICE TCT WITH ACTION BIT ON	F6704000
3207 *						F6706000
3208 *						F6708000
3209	CSERVICE NULL *					F6710000
3210	*GSERVICE DS					
3211	OH					
3212	TCTBUPCT,0				ARE ANY BUFFERS AVAILABLE	F6712000
3213	BRE				BR IF YES	F6714000
3214	GNOACT				NO... TURN OFF ACTION	F6716000
3215	GTTANK				AND CONTINUE	F6718000
3216	*GTTANK				A BUFFER IS PRESENT	F6720000
3217	DS					
3218	OH					
3219	TCTFNKCT,TCTMHL				ARE SUFFICIENT TANKS QUEUED	F6722000
3220	BHL				BR IF YES	F6724000
3221	GNOACT					F6726000
3222	OH					
3223	A DECOMPRESSION IS REQUIRED					F6728000
3224	DS					
3225	OH					
3226	SCHAIN \$TANKPOL,R10,HOENB GET A TANK					
3227	CLI STANKPOL,0					
3228	B2					
3229	R10,STANKPOL					
3230	STANKPOL(2),0(R10)					
3231	GWAIT				BR IF NONE	F6730000
3232	OH					
3233	USING TANKDSEC,R10					F6732000
3234	SLOAD R8,TCTBUPER				CURRENT BUFFER	F6734000
3235	R8,TCTBUPER					
3236	RR,(BUPCOUNT-BUFDSECT),0(R8) TO DATA					F6736000
3237	RR, (BUPCOUNT-BUFDSECT),0(R8)				SAVE TANK ADDR.	F6738000
3238	SSTO					
3239	R10,GTANK					
3240	STH					
3241	R10,GTANK					
3242	HVC TANKRCB(2),0(R8)				MOVE RCB AND SRCB	F6740000
3243	LH R15,=H,1				CONSTANT FOR SPEED	F6742000
3244	NULL *				PROCESS AN SCB	F6744000
3245	GDECOHP					
3246	OH					
3247	GSCB(1),2(R8)				SET SCB	F6746000
3248	LH GSCB,X,7F				TURN OFF HIGH-BIT	F6748000
3249	NI				END-OF-RECORD	F6750000
3250	BZ				IS THIS A CHAR STRING...	F6752000
3251	TH				BR IF NOT	F6754000
3252	BZ				TURN OFF STRING BIT	F6756000
3253	HVC TANKDATA(*-*),3(R8)				MOVE STRING (+1)	F6758000
3254	LH *-5				SCB AND COUNT	F6760000
3255	R9,GSCB-1				GET MOVE COUNT	F6762000
3256	SH R9,GD200				REMOVE MOVE	F6764000
3257	AR R8,R9				COUNT INPUT STRING	F6766000
3258	AR R10,R9				COUNT OUTPUT STRING	F6768000
3259	AR R8,R15				COUNT SCB	F6770000
3260	B GDECOHP				CONTINUE WITH RECORD	F6772000
3261	NULL *				PROPAGATION REQUIRED	F6774000
3262	DS					
3263	OH					
3264	GSCB,X,20F				IS THIS BLANKS...	F6776000
3265	TH				BR IF YES	F6778000
3266	BZ				NO .. REMOVE INDICATOR	F6780000
3267	HI				SET SAMPLE CHARACTER	F6782000
3268	HVC TANKDATA(1),3(R8)				SET COUNT	F6784000
3269	HVC GHOU+1(1),GSCB				SET DATA	F6786000
3270	HVC TANKDATA+1(*-*),TANKDATA				PROPAGATE COUNT (+2)	F6788000
3271	LH R9,GSCB-1				PROPAGATION COUNT	F6790000
3272	SH R9,GD200				LESS MOVE	F6792000
3273	AR R8,P15				COUNT SAMPLE CHAR	F6794000

LOC	OBJECT CODE	ADDR1	ADDR2	STMT	SOURCE STATEMENT	P150CF70	11/21/72
1	00095E	47F0	092E	3261	AND ENTER FLOW	F6794000	
2				3262	BLANK PROPAGATION REQUIRED	F6796000	
3	000562	9240	A006	00006	OH TANKDATA,C'	F6798000	
4	000966	D200	096D	0091F	SET COUNT	F6800000	
5	00095C	D200	A007	00006	TANKDATA PROPAGATE BLANKS	F6802000	
6	000972	4890	091E	0091E	GET HOVE COUNT	F6804000	
7	000976	4890	089A	3268	LESS NYC	F6806000	
8	00097A	47F0	092E	0092E	ENTER FLOW	F6808000	
9	00097E			3270	END OF LOGICAL RECORD	F6810000	
10				3271	TANK ADDR	F6812000	
11	00097E	4890	0A28	00A28	FROM END PIR	F6814000	
12	000982	7BA9		3274	SET COUNT IN TANK	F6816000	
13	000982	7BA9		3275	SET COUNT IN TANK	F6818000	
14	000982	7BA9		3276	SET COUNT IN TANK	F6820000	
15	000984	40A0	9004	00004	SET COUNT IN TANK	F6822000	
16	000588	48A0	133E	0133E	R10 = ABSOLUTE TANK CHAIN PIR	F6824000	
17	00098C	7AAD		3280	ADD TO TANK QUEUE	F6826000	
18	00098E	9500	A000	00000			
19	000992	4780	099E	0099E			
20	000996	48A0	A000	00000			
21	00099A	47F0	098E	0098E			
22	00099E	4090	A000	00000			
23	0009A2	D201	9000	1314			
24	0009A8	4A80	1336	01336			
25	0009AC	48A0	D00C	0000C			
26	0009B0	7A8F		3290			
27	0009B2	40A0	D00C	0000C			
28	0009B6	48A0	D006	00006			
29	0009BA	92F0	A001	00001			
30	0009BE	4890	D00A	0000A			
31	0009C2	D500	D004	8000			
32	0009C8	4770	09D6	009D6			
33	0009CC	1889		3299			
34	0009CE	4080	9002	00002			
35	0009D2	47F0	08CE	008CE			
36	0009D6			3304			
37	0009D6			3305			
38	0009D6	000009DA		3306+			
39	0009D6	8100		3307+			
40	0009DA			3309+			
41	0009DA			3310+			
42	0009DE	D201	D00A	9000			
43	0009E4	D201	9000	02A4			
44	0009EA	4090	02A4	002A4			
45	0009EE	4880	D00E	0000E			

LOC	OBJECT CODE	ADDR1	ADDR2	STMT	SOURCE STATEMENT	REDUCE COUNT
3316				\$BCTR RB,0		F6862000
3317*	0130E			AR,=H-1*		
3318	0009F6 4080 D00E			STH RB,TCBUPFLH	AND RESET	F6866000
3319	0009FA D500 D00F D00E			CLC TCTBUFT,TCBUPFLH	IS ANOTHER BUFFER REQUIRED	F6866000
3320	000A00 4780 0A1C			BVL GENABLE	ENABLE INTERRUPTS	V0J.1 F686E000
3321				SOC SPCSOUL,TCFPCS	SHOW NEXT BUFFER PERMITTED	F6870000
3322+*				NOTE THAT THIS IS A VERY RESTRICTIVE		
3323+*				SIMULATION OF THE 'OC' INSTRUCTION FOR THE MODEL 20		
3324+	000A04 D200 0A0B D002 00A0B 00002			HVC *+7(1),TCFPCS		
3325+	000A0A 9600 0252			OI SPCSOUL,*-*		
3326+	000A0E D200 0A15 D003 00A15 00003			HVC *+7(1),TCFPCS+1		
3327+	000A14 9600 0253			OI SPCSOUL+1,*-*		
3328	000A18 92F0 0B5B			MVI SPCSHOT+1,X'PO'	SHOW PCS CHANGE	F6872000
3329	GENABLE			SSH FF	ALLOW INTERRUPTS	V0J.1 F6873000
3330+GENABLE				DS OH		
3331+	000A1C 0000A20			DC ALQ (**4) SPSWSPSWSPSWSPSWSPSWSPSWSPSWSPSWS		
3332+	000A1C 8100			ORG *-4 SPSWSPSWSPSWSPSWSPSWSPSWSPSWSPSWS		
3333+	000A20 0100			DC X'8100' SPSWSPSWSPSWSPSWSPSWSPSWSPSWSPSWS		
3334+	000A20 0100			ORG *-2 SPSWSPSWSPSWSPSWSPSWSPSWSPSWSPSWS		
3335+	000A22 0A24			DC AL2 (X'0100')		
3336+	000A24 47F0 08BE			Y(*+2)		
3337				B GSERVICE	AND CONTINUE	F6874000
3338	GTANK			SACON 0	TANK ADDR	F6970000
3339+GTANK				DC Y(0)		
3340	GD200			EQU OD200	SHARE CONSTANT	F6974000
3341				DROP R9	DISCONTINUE TANK REG	F6976000

LCC	OBJECT CODE	ADDR1	ADDR2	SRHT	SOURCE STATEMENT	F150CT70	11/21/72
000A2A	4080 0A50	00A50		3343	STPOEN NULL *	F6980000	
				3344	STPOEN DS OH		
				3345	STRACE SSH=YES		
				3346	SSTO R14,TSAVA	SAVE CALLER'S	
				3347	STH R14,TSAVA	REGS	
				3348	SSTO R8,TSAVB		
				3349	STH R8,TSAVB		
				3350	HVC TTANK+TANKSRCB-TANKDSEC (1),TANKRCB-TANKDSEC (R8) SET YCH	F6980000	
				3351	SLOAD R8,TANKCON	FOR STPUT	
				3352	LH R8,TANKCON		
				3353	BAS R14,STPUT	GO PUT RECORD	
				3354	DC X'4D'		
				3355	DC AL1(R14*16)		
				3356	DC S(STPUT)		
				3357	SLOAD R8,TSAVB	CALLER'S	
				3358	LH R8,TSAVB	REGS	
				3359	SLOAD R14,TSAVA		
				3360	LH R14,TSAVA		
				3361	BR R14	RETURN TO CALLER	
				3362	*	DUNNY TANK	
				3363	TANK SACON 0	CHAIN	
				3364	TTANK DC Y(0)		
				3365	DC X'90'	RCB FOR FUNCTION CTL RECORD	
				3366	DC X'00'	USER'S SRCB (FUNCTION TYPE)	
				3367	DC H'0'	TANK COUNT	
				3368	TSAVA SACON 0		
				3369	TSAVA DC Y(0)		
				3370	TSAVB SACON 0		
				3371	TSAVB DC Y(0)		
				3372	TANKCON SACON TTANK		
				3373	TANKCON DC Y(TTANK)		
				000A4A	0000		
				000A4C	90		
				000A4D	00		
				000A4E	0000		
				000A50	0000		
				000A52	0000		
				000A54	0A4A		

LINE	LCC	OBJECT CODE	ADDR1	ADDR2	STMT	SOURCE STATEMENT	PI50CT70	DATE
3375	*				NULL			11/21/72
3376	*				DS OH			
3377	*				USING BUFDETECT,R13	BUFFER ADDR IS IN R13		
					AIF ('EXPARENT' EQ 'YES').NONXP1			
3379	S	SEXTP			ANOP			
3380	S	SEXTP			ANOP			
3381	S	SEXTP			ANOP			
3382	S	SEXTP			ANOP			
3383	S	SEXTP			ANOP			
3384	S	SEXTP			ANOP			
3385	S	SEXTP			ANOP			
3386	S	SEXTP			ANOP			
3387	S	SEXTP			ANOP			
3388	S	SEXTP			ANOP			
3389	S	SEXTP			ANOP			
3390	S	SEXTP			ANOP			
3391	S	SEXTP			ANOP			
3392	S	SEXTP			ANOP			
3393	S	SEXTP			ANOP			
3394	S	SEXTP			ANOP			
3395	S	SEXTP			ANOP			
3396	S	SEXTP			ANOP			
3397	S	SEXTP			ANOP			
3398	S	SEXTP			ANOP			
3399	S	SEXTP			ANOP			
3400	S	SEXTP			ANOP			
3401	S	SEXTP			ANOP			
3402	S	SEXTP			ANOP			
3403	S	SEXTP			ANOP			
3404	S	SEXTP			ANOP			
3405	S	SEXTP			ANOP			
3406	S	SEXTP			ANOP			
3407	S	SEXTP			ANOP			
3408	S	SEXTP			ANOP			
3409	S	SEXTP			ANOP			
3410	S	SEXTP			ANOP			

HR2 C O R S U P -- INTERRUPT PROCESSOR (DEFINITIONS)

F150CF70 11/21/72

LCC OBJECT CODE ADDR1 ADDR2 START SOURCE STATEMENT

F7092000

CONTROL CHARACTERS

3413 *

000001	EQU	X'01'	START OF HEADING	F7096000
000002	EQU	X'02'	START OF TEXT	F7098000
000003	EQU	X'03'	END OF TEXT	F7100000
000010	EQU	X'10'	DATA LINK ESCAPE	F7102000
000026	EQU	X'26'	END OF TEXT BLOCK	F7104000
000026	EQU	X'26'	ENQUIRY	F7106000
00002D	EQU	X'2D'	SYNCHRONIZATION	F7108000
000032	EQU	X'32'	LOST BLOCK ALARM	F7110000
000037	EQU	X'37'	NEGATIVE ACKNOWLEDGEMENT	F7112000
00003D	EQU	X'3D'	NEGATIVE ACKNOWLEDGEMENT-CONDITIONAL	F7114000
000061	EQU	X'61'	POSITIVE ACKNOWLEDGEMENT	F7116000
000070	AIF	(%EXPARNT% EQ %YES%).XPARA	POSITIVE ACKNOWLEDGEMENT	F7118000
3426	ANOP			F7128000
3427	.XPARA		TRANSPARENT HEADER	F7130000
3428	.XLDL	XOLE	TRANSPARENT TRAILER	F7132000
3429	XTRL	XOLE	TRANSPARENT CCH CHAINING BITS	F7134000
3430	XCHN	EQU X'60'		F7135000
3431	.XPARB	ANOP		

F7140000

3433 AIF (%MACHINE HE 20).D1

F7164000

COMMUNICATIONS ADAPTER ADDRESSES

3435 *

000C56	EQU	X'56'	BSCA INTERRUPT IDENTIFIER	F7168000
000C55	EQU	X'55'+1	BSCA RECEIVE CODE	F7150000
000C59	EQU	X'59'+1	BSCA TRANSMIT CODE	F7152000
000C57	EQU	X'57'	BSCA SENSE CODE	F7154000
000C50	EQU	X'50'	BSCA ERROR TEST CODE	F7156000
000C56	EQU	X'56'	BSCA STORE CURRENT ADDRESS	F7158000
000C52	EQU	X'52'	BSCA ENABLE CODE	F7160000
000052	EQU	X'51'+1	BSCA RECEIVE INITIAL CODE	F7162000
000051	EQU	X'50'+1	BSCA TRANSMIT-RECEIVE CODE	F7164000
3446	AGO	.D2		F7166000
3447	.D2			F7184000
3449	*		BLOCK CONTROL BYTE INDICATORS	F7188000

F7192000

IGNORE BLOCK COUNT INDICATOR
RESET BLOCK COUNT INDICATOR

3451 BCBIGREZ EQU X'10'

000010
000020

LCC	OBJECT CODE	ADDR1	ADDR2	STMT	SOURCE STATEMENT	ADDR3
000A86				3454 \$IOINT	NULL	F7198000
				3455+\$IOINT	DS	
				3456	AIP	
000A86	9556 0091	00091		3457	CLL	F7200000
000A8A	4770 0EEB			3458	BRE	F7204000
000A8E	47F0 0A88			3459 \$CONBUSY B	CEXIT	F7206000
				3460	TIOB	F7208000
				3461+	DC	
000A92	5A			3462+	DC	
000A93	50			3463+	DC	
000A94	0E90			3464 CREADREQ B	CENDREAD	F7210000
000A96	47F0 0ABC			3465 CDRRESP	XIO	F7212000
				3466+CDRESP	DS	
000A9A				3467+	HVC	
000A9A	D254 0000 0190 00000	00190		3468+	ORG	
000A9A				3469+	DC	
000A9A	D0			3470+	ORG	
000AA0				3471 CREADCT	EQU	
000A9E				3472	BC	
000AA0	4770 0A9A	00A9A		3473	HVI	
000AA4	92F0 0A97	00A97		3474	SPSW	
				3475+	DC	
000AA8	00000090			3476+	ORG	
000AA8				3477+	DC	
000AA8	8100			3478+	ORG	
000AAC				3479 CREDIT	NULL	
				3480+CREXIT	DS	
000AAC				3481	AIP	
				3482 .ITRA	ANOP	
				3483	\$RESTORE R13,R15,CREGS	
				3484+	LH	
000AAC	48D0 0ED8	00ED8		3485+	LH	
000AB0	48E0 0EDA	00EDA		3486+	LH	
000AB4	48F0 0EDC	00EDC		3487 .ITRB	ANOP	
				3488 CEXIT	NULL	
				3489+CEXIT	DS	
000AB8				3490	SPSW	
000AB8	00000090			3491+	DC	
000AB8				3492+	ORG	
000AB8	8100			3493+	DC	
000AB8				3494+	ORG	
000ABC				3495	AGO	
				3496 .X3	ANOP	

ALL INTERRUPTS ENTER HERE

IS THIS BSCA
BR IF NO
RETURN IF NO ACTIVITY
BR IF ANY ERROR

BR IF READ ENDING
OTHERWISE START READ

READ COUNT FIELD
WAIT FOR READ TO TAKE
SHOW READ ACTIVE
AND EXIT

RESTORE INTERRUPTED REGS

RETURN TO INTERRUPTED LOCATION

LCC OBJECT CODE ADDR1 ADDR2 SINT SOURCE STATEMENT

LCC	OBJECT CODE	ADDR1	ADDR2	SINT	SOURCE STATEMENT	EXTERNAL ENTRY POINT
000ABC	3499				\$ENDREAD NULL *	F7268000
000ABC	3500				\$ENDREAD DS OH	
000ABC	3501				CENDREAD NULL *	F7270000
000ABC	3502				CENDREAD DS OH	
000ABC	3503				AIF (MACHINE WE 20).X21	F7272000
000ABC	3504				AIF (TRACE EQ 0 OR MACHINE WE 20).ITRC	F7274000
000ABC	3505				.ITRC ANOP	F7282000
000ABC	3506				\$SAVE R13,R15,CREGS	F7284000
000ABC	40D0	0ED8			STH R13,CREGS+0	F2860000
000AC0	40E0	0EDA			STH R14,CREGS+2	F2880000
000ACA	40F0	0EDC			STH R15,CREGS+4	F2900000
000ACB	48D0	0EC6			ANOP	F2920000
000ACC	48D0	0EC6			\$LOAD R13,CBUFFER	F2920000
000ACC	48D0	0EC6			LH R13,CBUFFER	
000ACC	48D0	0EC6			ANOP	
000ACC	48D0	0EC6			X21	
000ACC	48D0	0EC6			USING BUFDSECT,R13	
000ACC	47F0	0ADC			ANOP LOG OVERHEAD	F2960000
000ACC	47F0	0ADC			CNOLOGAL	
000ACC	47F0	0ADC			AVOID LOG OVERHEAD	F2960000
000AD0	D202	00C4	D006	000C4	00006	
000AD6	4D				HVC \$LOGINFO(3),BUFSTART+1	F7304000
000AD7	4D				DC XTPREAD	F7306000
000AD8	061A				LOG RECEIVED DATA	
000ADA	0090				AL1(R15+16)	
000AD6	4D				DC S(SLOG)	
000AD7	4D				DC AL2(XTPREAD*(LOGEND-LOGDSECT))	
000AD8	061A				ENTRY TO SKIP LOGGING EVERYTHING	F7308000
000ADA	0090				OH	
000ADC	D200	0ED4	D005	00ED4	00005	
000AE2	9510	0ED4			HVC CRESP,BUFSTART	F7310000
000AE6	4770	0AFO			CLI CRESP,XDLE	F7312000
000AEA	D200	0ED4	D006	00ED4	00006	
000AF0	9501	0ED4			BHE **10	F7314000
000AF4	4780	0B9A			HVC CRESP,BUFSTART+1	F7316000
000AF8	9502	0ED4			CLI CRESP,XSOH	F7318000
000AFC	4780	0B9A			BE CIMBUF	F7320000
000E00	9570	0ED4			CLI CRESP,XSTY	F7322000
000E04	4780	0B14			BE CIMBUF	F7324000
000E08	9530	0ED4			CLI CRESP,RACKO	F7326000
000E0C	4780	0D54			B2 CACKED	F7328000
000E10	47F0	0DPC			CLI CRESP,INAK	F7330000
000E14	47F0	0DPC			B2 CACKED	F7332000
000E18	47F0	0DPC			B CRESPRAD	F7334000
000E1C	47F0	0DPC			UNKNOWN RESPONSE RECEIVED	

NOP THE ABOVE BRANCH TO LOG EVERY BLOCK RECEIVED

F7300000

LCC	OBJECT CODE	ADDR1	ADDR2	SINT	SOURCE STATEMENT	
				3592 *	WAIT-A-BIT SEQUENCE RECEIVED FROM HASP	F7420000
				3593 *		F7422000
000B92				3594	CHAITBIT NULL *	F7424000
				3595*	CHAITBIT DS OH	
000E92	92P0 07F7	007F7		3596	STOPPHONE+I,X,YOV STOP ALL BUFFERING	F7426000
				3597	STRACE SSH=NO	F7428000
000E96	47P0 0B5A	00B5A		3598	B \$PCSHOT GO IDLE	F7430000

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LCC OBJECT CODE ADDR1 ADDR2 STMT SOURCE STATEMENT

LCC	OBJECT CODE	ADDR1	ADDR2	STMT	SOURCE STATEMENT	STATEMENT
000B9A	3601				CIMBUF NULL *	F7436000
	3602				CIMBUF OH	
	3603				AIP (SMACHINE EQ 20).CCOH20	F7438000
	3604				.CCOH20 ANOP	F7446000
	3605				CIO CALASTPO,CASCA LAST ADDR USED *1	F7448000
	3606				DC X'9B'	
	3607				DC AL1(CASCA)	
	3608				S(CALASTPO)	
	00EB4	00EB4			LH R15,CALASTPO TO REG	F7450000
	00EA2	4AF0 130E			AH R15,=H'-1' REDUCE TO LAST ADDR	F7452000
	00BA6	9526 F000			CL1 0(R15),XEBP WAS ENDING SEQUENCE CORRECT (E1-B)	F7454000
	00EAA	4770 ODAC			BNE CRESPAD BR IF LOG AND NAK	F7456000
	00BAE	D201 0254 D008			HVC SFCSIN,BUFFCS SET NEW FUNCTION CONTROL	F7458000
	00BB4	D200 0BB8 D007			MVC CBCB(1),BUFFCB VERIFY BLOCK CONTROL BYTE COUNT	F7460000
	00BB8	9500 0ED1			CLI EQU COUNTR BYTE	F7462000
	00BBE	4770 0C7E			BNE CBCBCHK DOES RECEIVED MATCH EXPECTED	F7464000
	00BEC	48F0 0E00			LH R15,CBCBCHK BR IF NO	F7466000
	00EC6	4AF0 1320			AH R15,CBCBCHK-1 GET CURRENT COUNT	F7470000
	00ECA	40F0 0E00			STH R15,=H'1' TO NEXT EXPECTED	F7474000
	00ECE	948F 0ED1			NI CBCBCHK,X'80',R15 AND RESET	F7476000
	00BD2	9101 D004			DS COUNTR,=H'1' MODULO 16	F7478000
	00BD6	4710 0B19			TH BUFBSTAT,BUFFAKE IS THIS DUMMY BUFFER (E1-B)	F7480000
	00EDA	48E0 1312			BO CRYPTOK BR IF YES TO IGNORE	F7484000
	00000				LH R14,STCT1 POINT TO FIRST TCT	V03.1 F7484100
	00BDE	D500 E004 D00A			USING TCTDSECT,R14 DOES BUFFER AND TCT CBBS MATCH	V03.1 F7484200
	00BE4	4780 0C08			CLC TCTRCB,BUFDATA IF SO ASSIGN BUFFER	V03.1 F7484300
	00BE8	48E0 E000			BE CASSIGN GO TO NEXT TCT	V03.1 F7484400
	00BEC	4AE0 1314			LH R14,=H'0' TEST FOR END	V03.1 F7484500
	00BEO	4770 0BDE			STTR R14,R14	
	00EF4	9500 D00A			ANOP R14,=Y(SCT1TCT) COMPARE AGAINST TCT	V03.1 F7484700
	00EF8	4780 0B18			BE BUFBSTAT,0 ZERO RCBT	V03.1 F7484750
	00BFC	910F D00A			TH CRYPTOK FREE BUFFER	V03.1 F7484800
	00C00	4770 0B18			ANOP BUFBSTAT,15 IS IT A CONTROL RECORD	V03.1 F7484900
	00C04	48E0 130C			STTR R14,=Y(SCT1TCT) POINT TO CONTROL TCT	V03.1 F7485100
	00C08	48F0 1344			LH R15,=Y(SCT1TCT) POINT TO CONTROL TCT	V03.1 F7485200
	00C0C	1AFE			AR R15,=Y(TCTRCB-TCTDSECT) GET BUFFER POINTER OFFSET	V03.1 F7485300
	00C0E	92F0 00A9			HVI STPGTCH+1,X'F0' OPEN TRGTS GATE	V03.1 F7485400
	00C12	9200 D00A			HVI BUFBSTAT,0 RESET BUFFER STATUS BITS	F7486000
	00C16	9500 F000			CLI 0(P15),0 QUEUE FOR PROCESSING	F7492000

LCC	OBJECT CODE	ADDR1	ADDR2	STMT	SOURCE STATEMENT	11/21/72
000C1A	4780 0C26	00C26	00C26	3653+	DE *+12	V03.1 F7492100
000C1E	48F0 F000	00000	00000	3654+	LH R15,0(0,R15)	V03.1 F7492200
000C22	47F0 0C16	00C16	00C16	3655+	B *+12	V03.1 F7492300
000C26	40D0 F000	00000	00000	3656+	STH R13,0(0,R15)	V03.1 F7492400
000C2A	D201 D000	1314 00000	1314	3657+	HVC 0(2,R13),=H*0*	
000C30	48F0 1338	01338	01338	3659+	SLA R15,BUFDATA-BUFDSECT GET RCB OFFSET	
000C34	40F0 D002	00002	00002	3660	LH R15,=Y(BUFDATA-BUFDSECT)	
000C38	48F0 E00E	0000E	0000E	3661	STH R15,BUFPCOUNT SAVE OFFSET TO FIRST RCB	
000C3C	4AF0 1345	01345	01345	3662	LH R15,TCIBUFLH PICK LIMIT AND COUNT	
000C40	40F0 E00E	0000E	0000E	3663+	AAA R15,1 ADD 1 TO COUNT	
000C44	9840 E005	00005	00005	3664	AH R15,=Y(1)	
000C48	D500 E00F	E000F	E000F	3665	STH R15,TCIBUFLH SAVE	
000C4E	4740 0C76	00C76	00C76	3666	OI TCIRSTAT,TCIRACT SHOW ACTION REQUIRED ON TCT	
000C52	D200 0C59	E002	00C59	3667	AIP (PHONE EQ 0),HOMASGH	
000C58	9600 0252	00252	00252	3668	CLC TCIBUFCT,TCIBUFLH IS BUFFER MAXIMUM EXCEEDED	
000C5C	D200 0C63	E003	00C63	3669	BL CASHORE IF NOT ALLOW MORE	
000C62	9600 0253	00253	00253	3670	NOTE THAT THIS IS A VERY RESTRICTIVE	
000C66	48F0 0252	00002	00002	3671+*	SIMULATION OF THE 'OC' INSTRUCTION FOR THE MODEL Z0	
000C6A	48F0 0002	00002	00002	3672+*	HVC *+7(1),TCIFPCS	
000C6E	40F0 0252	00252	00252	3673+	HVC *+7(1),TCIFCS	
000C72	47F0 0B2E	00B2E	00B2E	3674+	OI *+7(1),TCIFCS	
000C76	92F0 0B5B	00B5B	00B5B	3675+	HVC *+7(1),TCIFCS+1	
000C7A	47F0 0B2E	00B2E	00B2E	3676+	OI *+7(1),TCIFCS+1	
000C7E	9110 0BBB	00BBB	00BBB	3677+	OH *+7(1),TCIFCS+1	
000C82	4710 0BD2	00BD2	00BD2	3678+	LH R15,\$PCSOULT	
000C86	9120 0BBB	00BBB	00BBB	3679+	SH R15,TCIFCS	
000C8A	4780 0C9A	00C9A	00C9A	3680	STH R15,\$PCSOULT	
000C8E	D100 0ED1	00ED1	00ED1	3681	DROP R14	
000C94	47F0 0BD2	00BD2	00BD2	3682	B CURTNEXT	
000C98	D100 00C4	00BBB	00C4	3683	B CASHORE	
000C9E	D100 00C5	0ED1	00C5	3684	HVI SPCSHOT+1,X*PO*	
000CA4	4D			3685	B CURTNEXT	
000CA5	F0			3686	B CASHORE	
000CA6	061A			3687	B CURTNEXT	
000CAA	D200 0ECB	00ECB	00ECB	3688	B CURTNEXT	
000CC0	48F0 0ED0	00ED0	00ED0	3689	B CURTNEXT	
000CC4	48F0 0ECA	00ECA	00ECA	3690	B CURTNEXT	
000CC8	4720 0CC0	00CC0	00CC0	3691	B CURTNEXT	
000C7E				3687	CBCBCHK NULL *	
000C7E	9110 0BBB	00BBB	00BBB	3688	CBCBCHK DS	
000C82	4710 0BD2	00BD2	00BD2	3689	TH CBCB,CBIGHRE	
000C86	9120 0BBB	00BBB	00BBB	3690	BO CBCBOK	
000C8A	4780 0C9A	00C9A	00C9A	3691	TH CBCB,CBRESSET	
000C8E	D100 0ED1	00ED1	00ED1	3692	BZ CBCBBAD	
000C94	47F0 0BD2	00BD2	00BD2	3693	HVN CBCBCNT(1),CBCB	
000C98	D100 00C4	00BBB	00C4	3694	B CBCBOK	
000C9E	D100 00C5	0ED1	00C5	3695	B CBCBOK	
000CA4	4D			3696	NULL *	
000CA5	F0			3697	DS	
000CA6	061A			3698	DS	
000CAA	D200 0ECB	00ECB	00ECB	3699	HVN \$LOGINFO(1),CBCB	
000CC0	48F0 0ED0	00ED0	00ED0	3700+	HVN \$LOGINFO+1(1),CBCBCNT(1) SET FOR LOG	
000CC4	48F0 0ECA	00ECA	00ECA	3701+	SLOG XBCRCHK	
000CC8	4720 0CC0	00CC0	00CC0	3702+	DC X*4D,	
000C7E				3687	AL1(R15*16)	
000C7E	9110 0BBB	00BBB	00BBB	3688	DC S(\$LOG)	
000C82	4710 0BD2	00BD2	00BD2	3689	DC	
000C86	9120 0BBB	00BBB	00BBB	3690	DC	
000C8A	4780 0C9A	00C9A	00C9A	3691	DC	
000C8E	D100 0ED1	00ED1	00ED1	3692	DC	
000C94	47F0 0BD2	00BD2	00BD2	3693	DC	
000C98	D100 00C4	00BBB	00C4	3694	DC	
000C9E	D100 00C5	0ED1	00C5	3695	DC	
000CA4	4D			3696	DC	
000CA5	F0			3697	DC	
000CA6	061A			3698	DC	
000CAA	D200 0ECB	00ECB	00ECB	3699	DC	
000CC0	48F0 0ED0	00ED0	00ED0	3700+	DC	
000CC4	48F0 0ECA	00ECA	00ECA	3701+	DC	
000CC8	4720 0CC0	00CC0	00CC0	3702+	DC	
000C7E				3687	AL2(XBCBCHK*(LOGEND-LOGDSECT))	
000C7E	9110 0BBB	00BBB	00BBB	3688	HVC CTRMP+1(1),CBCB	
000C82	4710 0BD2	00BD2	00BD2	3689	LH R15,CBCBCNT+1	
000C86	9120 0BBB	00BBB	00BBB	3690	SH R15,CTRMP	
000C8A	4780 0C9A	00C9A	00C9A	3691	BP *+8	
000C8E	D100 0ED1	00ED1	00ED1	3692		
000C94	47F0 0BD2	00BD2	00BD2	3693		
000C98	D100 00C4	00BBB	00C4	3694		
000C9E	D100 00C5	0ED1	00C5	3695		
000CA4	4D			3696		
000CA5	F0			3697		
000CA6	061A			3698		
000CAA	D200 0ECB	00ECB	00ECB	3699		
000CC0	48F0 0ED0	00ED0	00ED0	3700+		
000CC4	48F0 0ECA	00ECA	00ECA	3701+		
000CC8	4720 0CC0	00CC0	00CC0	3702+		

HE2 C O H S U P -- CIMBUF (PROCESS INPUT BUFFER FROM HASP)

LOC	OBJECT CODE	ADDR1	ADDR2	STMT	SOURCE STATEMENT	F150CT70	11/21/72
1	000CBC	4470	1349	AH	R15, H'16:		
2	000CC0	4470	08CC	CH	R15, CHXDUP.3		F7532000
3	000CC4	4720	08CC	BR	CHLKLOST		F7534000
4	000CC8	4770	0818	B	CHRTOK		F7536000
5							F7538000
6							
7							
8							
9	000CCC			3713	CHLKLOST NULL *		F7542000
10	000CCC	D100	0CED	08BB	3714+CHLKLOST DS OH		
11	000CD2	D100	0CF1	08E1	HVN		F7544000
12	000CD8	D208	0D02	08E8	HVN		F7546000
13	000CDE	D200	0E45	08ED	HVC		F7548000
14	000CE4	4770	0DF4	B	HVC		F7549000
15					CWRITE		F7550000
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3713 CHLKLOST NULL *
 3714+CHLKLOST DS OH
 3715 HVN
 3716 HVN
 3717 HVC
 3718 HVC
 3719 B

ONE OR MORE BLOCKS ARE LOST
 SET RECEIVED BLOCK COUNT
 SET EXPECTED BLOCK COUNT
 SET RESTORE CLOSTBLK *CLOSTBLK SET BAD BLOCK
 SET RESTORE BCB INSTRUCTION #03.1
 GO TELL HASP ABOUT BAD BCB

3721 * PROTOTYPE CTL RECORD TO TELL HASP THAT BLOCKS ARE LOST F7554000
 3723 CLOSTBLK NULL * START F7558000
 3724+CLOSTBLK DS OH
 3725 DC AL2(CLOSTEND-CLOSTBS) BUFCOUNT F7560000
 3726 DC AL1(BUFTXT) BUFCOUNT F7562000
 3727 CLOSTBS DC AL1(XLDR, XSTX) BUFCOUNT F7564000
 3728 CLOSTBCB DC AL1(X'80'+BCBIGNRE) BUFCB(RECEIVED BLOCK CT ADDED) F7566000
 3729 CLOSTFCS DC AL2(0) FCS F7568000
 3730 CLOSTFCS DC AL1(X'80') RCB (CTL REC, TYPE=LOST DATA) F7570000
 3731 CLSTRRCB DC AL1(X'80') SCB(EXPECTED BLK CT ADDED) F7572000
 3732 DC AL1(0) SCB (NULL RECORD) F7574000
 3733 DC AL1(0) RCB (END OF BLOCK) F7576000
 3734 CLOSTEND EQU * END OF PROTOTYPE F7578000

LOC	OBJECT CODE	ADDR1	ADDR2	STMT	SOURCE STATEMENT	PREPARE TO REFRANSHIP
3779 *						F7644000
3780 *					A NEGATIVE RESPONSE RECEIVED FROM HASP	F7646000
3781 *						F7648000
3782 *						
3783 *						F7652000
3784 *						F7654000
3785 *						
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3900 *						

HR2 C O M S U P -- CNAKED (PROCESS ERROR REPLY FROM HASP)

F150CT10 11/21/72

F7714000

SOURCE STATEMENT

AND GO WRITE IT

B CNWRITE

00DP4 3826

000IA6 47FO 0DP4

1 LOC OBJECT CODE ADDR1 ADDR2 START STMT SOURCE STATEMENT
2 000IA6 47FO 0DP4 00DP4 3826 B CNWRITE
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11/21/72

LCC OBJECT CODE ADDR1 ADDR2 STAT SOURCE STATEMENT

Object Code	Addr1	Addr2	Stat	Source Statement	Address
000DF4				3865 CWRITE NULL	F7768000
				3866+CWRITE DS OH	
				3867 AIF (EMACHINE NE 20),X4	F7770000
000DF8	48F0	1352		R15,-Y (BUFSTART-BUFDSCT) DATA DISPLACEMENT	F7772000
				R15,R13 R15 = BEGINNING OF DATA	F7774000
000DF8	1APD			3868 AR R15,R13	F7776000
000DF8	40F0	0A9C		3869 STR R15,CRESPT+2 SET RETURN READ	F7778000
000DFE	40F0	0E84		3870 STR R15,CWRITE+2 SET WRITE ADDR	F7780000
000E02	9102	0004		3871 TH BUFSTAT,BUFRESP IS THIS A RESPONSE SEQUENCE...	F7782000
000E06	4780	0E14		3872 BZ CSETEND BR IF NO	F7784000
000E0A	D201	0E86		3873 RVC CWRITE+4(2),-H'2: YES...SET RESPONSE COUNT	F7786000
000E10	47F0	0E5A		3874 B CWRITE+4(2),-H'2: YES...SET RESPONSE COUNT	F7788000
				3875 CWRITE+4(2),-H'2: YES...SET RESPONSE COUNT	
				3876 CSETEND NULL *	
				3877+CSETEND DS OH	
000E14	48F0	0002		3878 LH R15,BUPCOUNT DATA COUNT	F7790000
000E18	4AF0	1356		3879 AH R15,-H'11: FIX FOR X-PARENTHESIS	F7792000
000E1C	40F0	0E86		3880 SH R15,CWRITE+4 SET COUNT	F7794000
000E20	1APD			3881 AR R15,R13 INCLUDE BUFFER	F7796000
000E22	4AF0	1352		3882 AH R15,-Y (BUFSTART-BUFDSCT) ADJUST FOR HEADING	F7798000
000E26	4BFO	1356		3883 SH R15,-H'11: AVOIDS ASSEMBLER ERROR	F7800000
000E2A	D201	00E8		3884 RVC 0(2,R15),XETBSEQ SET ENDING	F7802000
				3885 CWRITE NULL *	F7804000
				3886+CWRITE DS OH	
000E30	9102	0004		3887 TH BUFSTAT,BUFRESP IS THIS A RESPONSE	F7806000
000E34	4710	0E5A		3888 BO CWRITE+4 BR IF YES TO SKIP PCS SET	F7808000
000E38	D201	0008		3889 RVC BUFPCS,\$FCSOUT SET CURRENT FCS	F7810000
000E3E	D201	0E8C		3890 RVC CPCSOUT,\$FCSOUT SAVE LAST PCS SENT	F7812000
000E44	9200	0007		3891 CSETBCB HVI BUFBCB,-* SET BCB INTO BUFFER	F7814000
000E48	D200	0E77		3892 TH COLDCB+1(1),BUFDATA SAVE RCR	F7815000
000E4E	9101	0004		3893 RVC BUFSTAT,BUFFAKE IS THIS A DUMMY BUFFER	F7816000
000E52	4780	0E5A		3894 BZ *-8 BR IF NO	F7818000
000E56	9640	0008		3895 OI BUFPCS,X'40: YES...SET WAIT-A-BIT	F7820000
				3896 CWRITE NULL *	F7822000
				3897+CWRITE DS OH	
000E5A	9200	0A97		3898 HVI CREADREQ+1,0 SHOW READ REQUIRED	F7824000
000E5E	4000	0E86		3899 STR R13,CBUFFER SET BUFFER ADDR	F7826000
000E62	47F0	0E72		3900 B CWLOG NOP THIS TO LOG EVERY WRITE	F7828000
000E66	D202	00C4		3901 RVC SLOGINPO(3),BUFSTART+1 SET LOG INFO	F7830000
000E6C	4D			3902 STLOG AND DO IT	F7832000
000E6E	F0			3903 DC X'4D: AND DO IT	
000E70	0080			3904 DC S(\$LOG)	
				3905+ AL1(R15*16)	
				3906+ DC AL2(ATPWRITE*(LOGEND-LOGDSCT))	
				3907+CWLOG DS OH	
				3908+CWLOG DS OH	
				3909 AIF (TRACE EQ 0 OR EMACHINE NE 20),TRX	F7834000
				3910 ANOP	
				3911 BRESTORE R13,R15,CREGS RESTORE BEFORE I/O IN CASE 4088	F7836000
000E72	48D0	0E88		3912+ LH R13,CREGS+0	F7842000
000E76	48E0	0E8A		3913+ LH R14,CREGS+2	F7844000
000E7A	48F0	0E8C		3914+ LH R15,CREGS+4	
				3915+ ANOP	
				3916+ TRX	
000E7E	9200	0A8F		3916 HVI \$COMBUSY+1,0 SHOW ACTIVITY	F7846000

1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
LCC	OBJECT CODE	ADDR1	ADDR2	STMT	SOURCE STATEMENT	AIP	ANOP	CL	CH	CH	CH	CH	CH	CH	CH	CH	CH	CH	CH
				3917															
				3918	.CL														
				3919	COMWRITE XIO														
				3920	*COMWRITE DS														
	000882			3921		HVC													
	000882	D258	0000	00000	00000														
	000882			3922		ORG													
	000882	D0		3923		DC													
	000888			3924		ORG													
	000888	4770	0882	3925		BC													
				3926		SPSW	104												
	00088C	000000090		3927		DC													
	00088C			3928		ORG													
	00088C	8100		3929		DC													
	000890			3930		ORG													
				3931		AGO													
				3932	.X5	AMOP													

F150CT70 11/21/72

703.1 P7850000
P7854000
P7856000

(MACHINE NE 20 OR 6LMBSPD LT 19200).CL

ISSUE WRITE

WAIT UNTIL ACCEPTED
EXIT (QUICKLY)

7.8-6

F7858000
F7860000

F7862000
F7942000

LCC	OBJECT CODE	ADDR1	ADDR2	STRT	SOURCE STATEMENT	F156CT70	11/21/72
				3934 *			F7946000
				3935 *			F7948000
				3936 *			F7950000
000E90				3938 CERRR	MULL *		F7954000
				3939+CERRR	DS CH		
				3940	AIF (6MACHINE NE 20).X10		F7956000
				3941	AIF (6TRACE EQ 0 OR 6MACHINE NE 20).ITRE		F7958000
				3942 .ITRE	ANOP		F7966000
				3943	SSAVE R13,R15,CREGS		F7968000
000E90	40D0 0ED8			3944+	STH R13,CREGS*0		
000E94	40E0 0EDA			3945+	STH R14,CREGS*2		
000E98	40F0 0EDC			3946+	STH R15,CREGS*4		
				3947 .ITRF	ANOP		
				3948	SLOAD R13,CBUFFER		F7970000
000E9C	48D0 0EC6			3949+	LH R13,CBUFFER		F7972000
				3950	CIO CENSE,CASENSE		
000EA0	9B			3951+	DC X'9B'		F7974000
000EA1	57			3952+	DC AL1(CASENSE)		
000EA2	0EA5			3953+	DC S(CENSE)		
000EA5				3954 CENSE	EQU **1		
000EA4	9200 00C4			3955	HVI SLOGINFO,**		F7976000
				3956	SLOG XUNITCHK		F7978000
				3957+	DC X'4D'		F7980000
000EA8	4D			3958+	DC AL1(R15*16)		
000EA9	F0			3959+	DC S(\$LOG)		
000EAA	061A			3960+	DC AL2(XUNITCHK*(LOGEND-LOGDSXCT))		
000EAC	0050			3961	DC CENSE,X'7F'		F7982000
000EAE	957F 0EA5			3962	BHW CWRITE		F7984000
000EB2	47D0 0E30			3963 *	PROCESS READ ERROR		F7986000
000EB6	9500 000A			3964 COLDRCB	CLI BUFDATA,**		F7987500
000EBA	4780 0D9C			3965	BE CSENDRK		F7987500
000EBE	94F7 0004			3966	NI BUFSRAT,253-BUFFTEXT OTHERWISE FORGET TEXT		F7987500
000EC2	47F0 0D9C			3967	B CSENDRK		F7987500
				3968	AGO		F7990000
				3969 .X1Z	ANOP		F8148000

LOC	OBJECT CODE	ADDR1	ADDR2	STMT	SOURCE STATEMENT	PC
3972	000EC6 0000			3972	CBUFFER \$ACON 0	F815A000
3973	000EC6 0000			3973	CBUFFER DC Y(0)	F815A000
3974	000EC8 0000			3974	\$BUFFER EQU CBUFFER	F8156000
3975	000ECA 0000			3975	CFCOUT DC H'0'	F8158000
3976	000ECB 0000			3976	CTEMP DC H'0'	F8160000
3977	000ECC 0000			3977	CHADUP DC H'3'	F8162000
3978	000ECE 00			3978	DC AL1(0)	F8164000
3979	000ECF 80			3979	CBCBNT0 DC AL1(X'80')	F8166000
3980	000ED0 00			3980	DC AL1(0)	F8168000
3981	000ED1 80			3981	CBCBNTI DC AL1(X'80')	F8170000
3982	000ED2 0000			3982	DC H'0'	F8172000
3983	000ED3			3983	\$INQCM EQU *-1	F8174000
3984	000ED4 00			3984	CRESP DC AL1(0)	F8176000
3985				3985	AIP (TRACE EQ 0 OR \$MACHINE NE 20).TRG	F8178000
3986				3986	.TRG ANOP	F8184000
3987				3987	CREGS \$WCON (0,0,0)	F8186000
3988				3988	CREGS DC A(0,0,0)	F8188000
3989				3989	.TRH ANOP	F8190000
3990				3990	CALASTPO DC H'0'	F8190000
3992	000EE6 1002			3992	* CONTROL SEQUENCES	F8194000
3993	000EE8 1026			3993	XSTXSEQ DC AL1(XLDR,XSTM)	F8196000
3994	000EEA 1070			3994	YETBSEQ DC AL1(ATRL,XETB)	F8198000
3995	000EEC 323D			3995	XACKSEQ DC AL1(KDLE,XACK)	F8200000
3996				3996	XNACKSEQ DC AL1(XSYN,XNACK)	F8202000
3997				3997	AIP (\$MACHINE EQ 20).S1	F8204000
3998				3998	.S1 ANOP	F8204000
3999				3999	DROP R13	F8247000
4000				4000	AIP (\$MACHINE EQ 20).YOSRP1	F8247000
4001				4001	.YOSRP1 ANOP	F8247000
4002				4002	YOSRP1 ANOP	F8247000
4003				4003	YOSRP1 ANOP	F8247000
4004				4004	YOSRP1 ANOP	F8247000
4005				4005	YOSRP1 ANOP	F8247000
4006				4006	YOSRP1 ANOP	F8247000
4007				4007	YOSRP1 ANOP	F8247000
4008				4008	YOSRP1 ANOP	F8247000
4009				4009	YOSRP1 ANOP	F8247000
4010				4010	YOSRP1 ANOP	F8247000
4011				4011	YOSRP1 ANOP	F8247000
4012				4012	YOSRP1 ANOP	F8247000
4013				4013	YOSRP1 ANOP	F8247000
4014				4014	YOSRP1 ANOP	F8247000
4015				4015	YOSRP1 ANOP	F8247000
4016				4016	YOSRP1 ANOP	F8247000
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4073				4073	YOSRP1 ANOP	F8247000
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4096				4096	YOSRP1 ANOP	F8247000
4097				4097	YOSRP1 ANOP	F8247000
4098				4098	YOSRP1 ANOP	F8247000
4099				4099	YOSRP1 ANOP	F8247000
4100				4100	YOSRP1 ANOP	F8247000

LCC	OBJECT CODE	ADDR1	ADDR2	STMT	SOURCE STATEMENT	P150CT70	11/21/72
	000IF2			4016	SSVC NULL *		F8620000
				4017	SSVC DS OH		F8622000
				4018	AIF (STRACE EQ 0).TRASKIP		F8766000
				4019	.TRASKIP ANOP		

F15OCT70 11/21/72

FB770000

LCC	OBJECT CODE	ADDR1	ADDR2	SIHT	SOURCE STATEMENT	
000EP2	0274			4021	\$1STANK \$GENTANK	TANK RCB
000EF4	00			4022	*STANK1 DC Y(\$TANK2)	TANK SRCB
000EP5	00			4023	+ AL1(0)	TANK COUNT
000EP6	0000			4024	+ AL1(0)	TANK DATA
000EP8	4040404040404040			4025	+ DC AL2(0)	TANK WORK SPACE
000F70	40404040			4026	+ DC 120C	TANK RCB
000F74	02F6			4027	+ DC (4)C	TANK SRCB
000F76	00			4028	*\$TANK2 DC Y(\$TANK3)	TANK COUNT
000F77	00			4029	+ DC AL1(0)	TANK DATA
000F78	0000			4030	+ DC AL1(0)	TANK WORK SPACE
000F7A	4040404040404040			4031	+ DC AL2(0)	TANK RCB
000EP2	40404040			4032	+ DC 120C	TANK SRCB
000EP6	1078			4033	+ DC (4)C	TANK COUNT
000EP8	00			4034	*\$TANK3 DC Y(\$TANK4)	TANK DATA
000EP9	00			4035	+ DC AL1(0)	TANK WORK SPACE
000EFA	0000			4036	+ DC AL1(0)	TANK RCB
000EFC	4040404040404040			4037	+ DC AL2(0)	TANK SRCB
001C74	40404040			4038	+ DC 120C	TANK COUNT
001C78	10FA			4039	+ DC (4)C	TANK DATA
001C7A	00			4040	*\$TANK4 DC Y(\$TANK5)	TANK WORK SPACE
001C7B	00			4041	+ DC AL1(0)	TANK RCB
001C7C	0000			4042	+ DC AL1(0)	TANK SRCB
00107E	4040404040404040			4043	+ DC AL2(0)	TANK COUNT
0010P6	40404040			4044	+ DC 120C	TANK DATA
0010FA	117C			4045	+ DC (4)C	TANK WORK SPACE
0010FC	00			4046	*\$TANK5 DC Y(\$TANK6)	TANK RCB
0010FE	0000			4047	+ DC AL1(0)	TANK SRCB
001100	4040404040404040			4048	+ DC AL1(0)	TANK COUNT
001178	40404040			4049	+ DC AL2(0)	TANK DATA
00117C	11FE			4050	+ DC 120C	TANK WORK SPACE
00117E	00			4051	+ DC (4)C	TANK RCB
00117F	00			4052	*\$TANK6 DC Y(\$TANK7)	TANK SRCB
001180	0000			4053	+ DC AL1(0)	TANK COUNT
001182	4040404040404040			4054	+ DC AL1(0)	TANK DATA
0011FA	40404040			4055	+ DC AL2(0)	TANK WORK SPACE
0011FE	1280			4056	+ DC 120C	TANK RCB
001200	00			4057	+ DC (4)C	TANK SRCB
001201	00			4058	*\$TANK7 DC Y(\$TANK8)	TANK COUNT
001202	0000			4059	+ DC AL1(0)	TANK DATA
001204	4040404040404040			4060	+ DC AL1(0)	TANK WORK SPACE
00127C	40404040			4061	+ DC AL2(0)	TANK RCB
001280	0000			4062	+ DC 120C	TANK SRCB
001282	00			4063	+ DC (4)C	TANK COUNT
001283	00			4064	*\$TANK8 DC Y(\$TANK9)	TANK DATA
001284	0000			4065	+ DC AL1(0)	TANK WORK SPACE
001286	4040404040404040			4066	+ DC AL1(0)	TANK RCB
0012FE	40404040			4067	+ DC AL2(0)	TANK SRCB
000000				4068	+ DC 120C	TANK COUNT
000EP2				4069	+ DC (4)C	TANK DATA
4070	*\$TANK9 EQU			4070	+ EQU	END CHAIN
4071	*\$1STANK EQU			4071	+ EQU	STANK1
4072	*\$1STANK AIP			4072	+ AIP	EXTERNAL NAME
4073	*\$TANKSIP ANOP			4073	+ ANOP	EXTERNAL EQ 0 AND 6PRTCONS EQ 0

FB772000
FB782000

11	LCC	OBJECT CODE	ADDR1	ADDR2	STRT	SOURCE STATEMENT	11/21/72
1	001308	00000000			4075	LTORG	P8786000
2	001308	00000000			4076	=P'0'	
3	00130C	00CA			4077	=Y(\$CLTCT)	
4	00130E	FFFF			4078	=H'1'	
5	001310	0312			4079	=Y(\$CONTTAB-2)	
6	001312	00CA			4080	=Y(\$CTC1)	
7	001314	0000			4081	=H'0'	
8	001316	0006			4082	=Y(TANKDATA-TANKDSEC)	
9	001318	00F2			4083	=Y(RCTANK1)	
10	00131A	0050			4084	=H'80'	
11	00131C	0004			4085	=H'4'	
12	00131E	01A6			4086	=Y(\$ERRTAB)	
13	001320	0001			4087	=H'1'	
14	001322	0002			4088	=Y(TANKRCH-TANKDSEC)	
15	001324	068A			4089	=Y(OG01)	
16	001326	06C8			4090	=Y(OSQUEZE)	
17	001328	0708			4091	=Y(OCOMP1)	
18	00132A	0716			4092	=Y(OCHESTOP)	
19	00132C	0004			4093	=Y(4)	
20	00132E	001F			4094	=H'31'	
21	001330	0040			4095	=H'64'	
22	001332	003F			4096	=H'63'	
23	001334	0000			4097	=Y(L'TANKCHN-2)	
24	001336	0003			4098	=H'3'	
25	001338	000A			4099	=Y(BUFDATA-BUFSECT)	
26	00133A	0189			4100	=Y(400-BUFDATA-BUFSECT-2)	
27	00133C	0004			4101	=Y(BUFSTART-BUFSECT-1)	
28	00133E	0008			4102	=Y(TCTANK-TCTDSECT)	
29	001340	024E			4103	=Y(\$OUTBUF)	
30	001342	0190			4104	=Y(400)	
31	001344	000A			4105	=Y(TCTBUFER-TCTDSECT)	
32	001346	0001			4106	=Y(1)	
33	001348	0010			4107	=H'16'	
34	00134A	0D46			4108	=Y(CDUMHY)	
35	00134C	0008			4109	=Y(CDURCT)	
36	00134E	0006			4110	=AL2(CDUMEND-CDUMSTRT)	
37	001350	00BC			4111	=Y(\$CONCOH-4)	
38	001352	0005			4112	=Y(BUFSTART-BUFSECT)	
39	001354	0002			4113	=H'2'	
40	001356	000B			4114	=H'11'	
41	001358	AD			4115	=X'AO'	
42	00135A				4116	\$END DS OH	P8786000

LOC	OBJECT CODE	ADDR1	ADDR2	STMT	SOURCE STATEMENT	DS	OF	
3	00135C			A178 *	\$1STBUF			F8792000
4				A119 *				F8794000
5				A120 *				F8796000
6				A121 *				F8798000
7				A122 *				F8800000
8				A123 *				F8802000
9				A124 *				F8804000
10				A125				F8806000
<p>THE FOLLOWING SECTION WILL DYNAMICALLY BUILD THE COMMUNICATIONS BUFFER POOL OVER THE INITIALIZATION INSTRUCTIONS. BUFFERS WILL BE CONSTRUCTED TO THE EXTENT OF MEMORY OR UNTIL %NUMBUFS% HAS BEEN BUILT. THE LOCATION OF THE BUFFERS SO GENERATED WILL BE AS GIVEN BELOW.</p>								
00135C				A127+&BUF1	EQU			\$1STBUF*(1-1)*(BUFEND-BUFBEGIN)
0014F4				A128+&BUF2	EQU			\$1STBUF*(2-1)*(BUFEND-BUFBEGIN)
00168C				A129+&BUF3	EQU			\$1STBUF*(3-1)*(BUFEND-BUFBEGIN)
001824				A130+&BUFA	EQU			\$1STBUF*(4-1)*(BUFEND-BUFBEGIN)
0019BC				A131+&BUFB	EQU			\$1STBUF*(5-1)*(BUFEND-BUFBEGIN)
001B54				A132+&BUFC	EQU			\$1STBUF*(6-1)*(BUFEND-BUFBEGIN)
001CFC				A133+&BUFD	EQU			\$1STBUF*(7-1)*(BUFEND-BUFBEGIN)
001E84				A134+&BUFE	EQU			\$1STBUF*(8-1)*(BUFEND-BUFBEGIN)

HB2 \$ I N I T I 8 -- DYNAMIC BUFFER POOL CONSTRUCTION

LCC	OBJECT CODE	ADDR1	ADDR2	START	SOURCE STATEMENT	F150CT70	11/21/72
001361				4136	ORG *(BUFSTART-BUFBEGIN) ORG AFTER BUF CMTBL INFO	F8808000	
001361	00			4137	P\$1STBUF \$ACON \$1STBUF	F8810000	
001362	135C			4138	P\$1STBUF DC Y(\$1STBUF)	F8812000	
001364	0178			4139	OH'0',AL2(BUFEND-BUFBEGIN)	F8814000	
001366	0330			4140	OH'0',AL2(2*(BUFEND-BUFBEGIN))	F8816000	
001368	00000000			4141	P'0'	F8818000	
00136C	1FF			4142	OH'0',AL2(1024*8HICORE-1)	F8820000	
00136E	0001			4143	OH'0',AL2(1024*8-1)	F8822000	
001370	0008			4144	H'ENUMBUFS'	F8824000	
001372				4145	BNUMBUFS DC H'8'	F8826000	
001372				4146	IBLDBUFS NULL	F8828000	
001372	4800 1362			4147	SLOAD R11,P\$1STBUF LOAD ADDR OF 1ST BUFFER	F8830000	
001376	48A0 1362			4148	LH R11,P\$1STBUF LOAD ADDE OF 1ST BUFFER	F8832000	
00137A	4890 1370			4149	LH R10,P\$1STBUF LOAD MAX NUMBER OF BUFFERS	F8834000	
00137E	4AA0 1366			4150	LH R9,ENUMBUFS	F8836000	
001382	49A0 136C			4151	BULDHORE NULL	F8838000	
001386	4720 13A8			4152	BULDHORE DS	F8840000	
00138A	4890 136E			4153	AH R10,BUFLN2 INCREASE R10 BY 2 BUFLNGTHS	F8842000	
00138E	4780 13A8			4154	SCA R10,BCOREND IS THERE ROOM FOR 2 MORE EUPS	F8844000	
001392	48A0 1364			4155	CH R10,BCOREND BR IF NO	F8846000	
001396	40A0 B000			4156	BH BUFSDONE SUB ONE FROM BUFFER COUNT	F8850000	
00139A	D202 8002 1368 00002 01368			4157	SH R9,BONE BR IF LAST BUFFER	F8852000	
0013A0	48B0 B000			4158	BZ BUFSDONE BACK UP TO NEXT BUFP ADDR	F8854000	
0013A4	47F0 137E			4159	SH R10,BUFLN1 STORE POINTER IN PREV BUFP	F8856000	
0013A8				4160	SSTO R10,0(R11)	F8858000	
0013A8	48A0 1368			4161	STH R10,0(R11) INITIAL VALUES	F8860000	
0013AC	40A0 F000			4162	HVC L'BUFCHAIN((BUFSTART-BUFCOUNT),R11),BUFZEROS MOVE IN	F8862000	
0013B0	D202 8002 1368 00002 01368			4163	* UPDATE R11 TO NEXT BUFFER	F8864000	
				4164	LH R11,0(R11) BR TO BUILD ANOTHER BUFP	F8866000	
				4165	B BULDHORE	F8870000	
				4166	BUFSDONE NULL		
				4167	BUFSDONE DS		
				4168	SLOAD R10,BUFZEROS LOAD CHAIN TERMINATOR		
				4169	LH R10,BUFZEROS STORE IT IN THE LAST BUFFER		
				4170	SSTO R10,0(R11)		
				4171	STH R10,0(R11)		
				4172	HVC L'BUFCHAIN((BUFSTART-BUFCOUNT),R11),BUFZEROS MOVE IN		
				4173	* INITIAL VALUES		
				4174	AIF (&HOME EQ 0),IHOME		
				4175	IHOME ANOP		

LOC	OBJECT CODE	ADDR1	ADDE2	STMT	SOURCE STATEMENT	PC
4177					\$DCHAIN \$BUFPPOOL,R13,NOENB GET A BUFFER	F8878000
4178*	0013B6 9500 024A	0024A			CLI \$BUFPPOOL,0	F8878000
4179*	0013BA 4780 13C8	013C8			BE **14	
4180*	0013BE 48D0 024A	0024A			LH R13,\$BUFPPOOL	
4181*	0013C2 D201 024A D000 0024A 00000	00000			HVC \$BUFPPOOL(2),0(R13)	
4182	000000				USING BUFDSECT,R13 *	
4183	0013C8 D25A D002 1414 00002 01414	00002			HVC \$CHAIN \$OUTSUP,R13,NOENB STAGE FOR WRITE	F8882000
4184	0013CE 4080 00C0	000C0			\$CHAIN \$OUTSUP,R13,NOENB STAGE FOR WRITE	F8882000
4185*	0013D2 4880 1470	000C0			STH R8,\$SCHTENP	
4186*	0013D6 9500 8000	01470			LH R8,=Y(\$OUTBUF)	
4187*	0013DA 4780 13E6	00000			CLI 0(R8),0	
4188*	0013DE 4880 8000	013E6			BE **12	
4189*	0013E2 47F0 13F6	00000			LH R9,0(0,R8)	
4190*	0013E6 40D0 8000	013D6			B **12	
4191*	0013EA D201 D000 1472 00000 01472	00000			STH R13,0(0,R8)	
4192*	0013F0 4880 00C0	000C0			HVC 0(2,R13),=H'0'	
4193*					LH R9,\$SCHTENP	
4194					\$DCHAIN \$BUFPPOOL,R13,NOENB GET ANOTHER	F8884000
4195*	0013F4 9500 024A	0024A			CLI \$BUFPPOOL,0	
4196*	0013F8 4780 1406	01406			BE **14	
4197*	0013FC 48D0 024A	0024A			LH R13,\$BUFPPOOL	
4198*	001400 D201 024A D000 0024A 00000	00000			HVC \$BUFPPOOL(2),0(R13)	
4199					STO R13,\$BUFFER	F8885000
4200*	001406 40D0 0EC6	00EC6			STH R13,\$BUFFER	
4201	00140A D201 D005 00EA 00005 00ABC	00005			HVC \$START,XACKSEQ	F8886000
4202	001410 47F0 0ABC	00ABC			B \$ENDREAD	F8890000
4203 ICTLS	001414				DS OH	F8892000
4204	001414 0058				DC AL2(ICTLE*-3)	F8894000
4205	001416 00				DC X'00'	F8896000
4206	001417 1002				DC AL1(X'80'+BCBRESSET)	F8898000
4207	001419 A0				DC AL2(0)	F8900000
4208	00141A 0000				DC X'F0'	F8902000
4209	00141C F0				DC C'A'	F8904000
4210	00141E C1				DC OCL80	F8906000
4211 ICTXT	00141E 615CE2C9C7D5D685				DS	F8908000
4212	001433 F240408040804080				DC CL21'/SIGNON	F8910000
4213	00146E 00				DC CL59'ERTID'	F8912000
4214	00146F				DC CL59'2' REMOTE IDENTIFICATION	
4215 ICTLE	001470				DC AL1(0)	
4216	001470 024E				DC EQU *	F8914000
4217	001472 0000				DC LTORG	F8916000
4218					DC =Y(\$OUTBUF)	F8918000
					DC =H'0'	

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1	LCC	OBJECT CODE	ADDR1	ADDR2	STMT	SOURCE STATEMENT	P15GCT70	11/21/72
1	001474				4220 \$REPCARD NULL	OH		
2	001474				4221 \$REPCARD DS	OH		
3	001474				4222 ZEP1 NULL *			
4	001474				4223 ZEP1 DS OH			
5	001474				4224 AIR (MACHINE EQ 20).REP20			
6	001474				4225 AROP			
7	001474				4226 XIO			
8	001474				4227 HVC			
9	001474				4228 DC			
10	001474				4229 DC			
11	001474				4230 DC			
12	001474				4231 BC			
13	001474				4232 ZEP2 NULL			
14	001474				4233 ZEP2 DS OH			
15	001474				4234 TI0B *RBUSY	WAIT		
16	001474				4235 DC X'9A'			
17	001474				4236 DC			
18	001474				4237 DC			
19	001474				4238 ZEP3 NULL			
20	001474				4239 ZEP3 DS OH			
21	001474				4240 TI0B ZEP1 REOR	ERROR		
22	001474				4241 DC X'9A'			
23	001474				4242 DC			
24	001474				4243 DC			
25	001474				4244 ZEP4 NULL			
26	001474				4245 ZEP4 DS OH			
27	001474				4246 TI0B ZEPEND, REOP	EOF		
28	001474				4247 DC X'9A'			
29	001474				4248 DC			
30	001474				4249 DC			
31	001474				4250 XIO			
32	001474				4251 HVC			
33	001474				4252 DC			
34	001474				4253 DC			
35	001474				4254 DC			
36	001474				4255 BC			
37	001474				4256 AGO			
38	001474				4257 .REP20A AROP			
39	001474				4258 CLC			
40	001474				4259 BE			
41	001474				4260 CLC			
42	001474				4261 BE			
43	001474				4262 CLC			
44	001474				4263 BNE			
45	001474				4264 CLC			
46	001474				4265 LH			
47	001474				4266 BE			
48	001474				4267 LH			
49	001474				4268 BAS			
50	001474				4269 DC			
51	001474				4270 DC			
52	001474				4271 DC			
53	001474				4272 STH			
54	001474				4273 LH			
55	001474				4274 LH			

LOC	OBJECT CODE	ADDR1	ADDR2	STMT	SOURCE STATEMENT	GO DECODE	ADDRESS
4275	Z1			BAS	R9,ZTRAN		F9020000
4276	Z1			DS	OH		F9020000
4277	+			DC	X'4D'		
4278	+			DC	ALI(P9=16)		
4279	+			DC	SIZTRAN		
4280		00000		STH	R10,O(O,R11)	AND STORE	F9022000
4281		0182C		AH	R11,H'2'	BUHP LOC CTR	F9024000
4282		0000H		CLI	R18,C'	IS THERE MORE	F9026000
4283		01474		BNE	ZEP1	BR IF NO TO NEXT CARD	F9028000
4284		0182E		AH	R8,H'5'	UP CARD POINTER	F9030000
4285		018D4		B	Z1	AND GO AGAIN	F9032000
4286	ZEPENDA	151D	0141E	HVC	ICTXT,ZEPCARD	MOVE SIGNON INTO DEFAULT	F9034000
4287	ZEPEND			NULL	*	END OF REP CARDS	F9036000
4288	ZEPEND			DS	OH		
4289		015A6		B	SINITI8	GO TO INITIALIZATION	F9038000
4290	*						F9040000
4291	*			ZTRAN	- CONVERT EBCDIC HEX TO BINARY		F9042000
4292	*				R8= START OF FIELD ,R9=LINK REG , R10 =BINARY RESULTS		F9044000
4293	*						F9046000
4294	ZTRAN			NULL	*		F9048000
4295	ZTRAN			DS	OH		
4296		01519	8000	HVC	ZHK(4),O(188)	MOVE DATA	F9050000
4297		01519	01519	TR	ZHK(4),ZTAB	TRANSLATE A LITTLE	F9052000
4298		01519	01519	PACK	ZHK(4),ZHK(4)	PACK IT UP	F9054000
4299		01519	01519	HVO	ZHK(4),ZHK(4)	SHIFT IT	F9056000
4300		0151A	0151A	LH	R10,ZHK+1	LOAD RESULT--- INTO REG	F9058000
4301				BR	R9	AND RETURN	F9060000
4302				DS	OH		F9062000
4303				DC	X'00'		F9064000
4304	ZHK			DC	AL3(O)		F9066000
4305				DC	X'00'	SPACER	F9068000
4306	ZEPCARD			DS	80C		F9070000
4307	ZTAB			ZOU	*-X'CI'		F9072000
4308		0A0B0C0D0E0F		DC	X'0A0B0C0D0E0F'		F9074000
4309		0000000000000000		DC	X'F0'-X'CG'X'00'		F9076000
4310		0102030405060708		DC	X'010203040506070809'		F9078000
4311	AIP			AIP	(MACHINE EQ 20).REP360C		F9080000
4312	.REP360C			ANOP			F9086000

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LOC	OBJECT CODE	ADDR1	ADDR2	SRMT	SOURCE STATEMENT	F150CCT70	11/21/72
0015A6					4314 \$INITI8 NULL * 4315 \$INITI8 DS OH 4316 AIF (CONSOLE EQ 0 AND SPTCONS EQ 0).ISKPTMK 4317 .ISKPTMK ANOP		
0015A6					4319 * 4320 * HASP ENVIRONMENT RECORDING ERROR PRINTOUT (HEREP) 4321 * 4322 INIHEREP NULL 4323 INIHEREP DS OH		
0015A6	D50B 164A 018A 0164A 0018A				4324 CLC ILOGTABL*4 (L'LOGTEXT), LOGTABLE*8 IS THE TABLE GOOD	F9112000	
0015A6	4770 163C				4325 BRE INPUTABL BR NO TO INITIALIZE NEW TABLE	F9114000	
					4326 AIF (MACHINE EQ 20).INI1	F9116000	
					4327 .INI1	F9118000	
					4328 SSSH 0	F9120000	
0015B0	00001584				4329+ DC AL4(*+4) SPSWSPSWSPSWSPSWSPSWSPSWSPSWS	F9122000	
0015B0					4330+ ORG *-4 SPSWSPSWSPSWSPSWSPSWSPSWSPSWS	F9124000	
0015B0	8100				4331+ DC X'8100' SPSWSPSWSPSWSPSWSPSWSPSWSPSWS	F9126000	
0015B4					4332+ ORG *+2 SPSWSPSWSPSWSPSWSPSWSPSWSPSWS	F9128000	
0015B4	00001588				4333+ DC AL4(*+4)	F9140000	
					4334 CIO 1,X'45'	F9142000	
0015B8	9B				4335+ DC X'9B'	F9144000	
0015B9	45				4336+ DC AL1(X'45')		
0015BA	0001				4337+ DC S(1)		
0015BC	4770 1588				4338 BC 7,*-4		
0015C0	D240 16D7 005A 016D7 0005A				4339 XIO INITITLE(POPCODE*1),90 PRINT TITLE	F9152000	
0015C0					4340+ HVC INITITLE(POPCODE*1),90 XIOXIOXIOXIOXIO	F9154000	
0015C0	D0				4341+ ORG *-6 XIOXIOXIOXIOXIOXIO		
0015C6					4342+ DC X'D0' XIOXIOXIOXIOXIO		
0015C6	4770 15C0				4343+ ORG *+5 XIOXIOXIOXIOXIO		
					4344+ BC 7,*-6		
0015CA	9B				4345 CIO 3,X'44'	F9156000	
0015CB	44				4346+ DC X'9B'	F9158000	
0015CC	0003				4347+ DC AL1(X'44')		
0015CB	4770 15CA				4348+ DC S(3)		
					4349+ BC 7,*-4		
0015D2	9B				4350 CIO 3,X'44'	F9160000	
0015D3	44				4351+ DC X'9B'	F9162000	
0015D4	0003				4352+ DC AL1(X'44')		
0015D6	4770 15D2				4353+ DC S(3)		
					4354+ BC 7,*-4		
00C000					4355 .INI2 ANOP USING LOGDSECT,R9		
					4356 SLA R9,LOGTABLE		
0015DA	4690 1830				4357 LH R9,-Y(LOGTABLE)		
					4358+ R9 BASE FOR FIRST ENTRY		
0015D2					4359 IMPRTOU NULL		
0015D2	9500 9000				4360+ IMPRTOU DS OH		
0015E2	4780 163C				4361 CLI LOGID,X'00'	F9174000	
0015E6	D20B 178E 9004 0178E 00004				4362 BE INEWTABL BF YES INITIALIZE NEW TABLE	F9176000	
0015EC	48A0 9002				4363 HVC INITEXT,LOGTEXT HVC LOGTEXT TO PRINT BUFFER	F9178000	
					4364 LH R10,LOGCOUNT LOAD ERROR COUNT INFO R10	F9180000	

LOC	OBJECT CODE	ADDR1	ADDR2	STMT	SOURCE STATEMENT	PH50C170	11/21/72
0015F0	D207 17B0 17B8 017B8			4365	AIF (SMACHINE EQ 20).IN13	F9182000	
0015F6	4BA0 1832			4366	AIF (SMACHINE NE 20).IN14	F9186000	
0015FA	4740 1608			4367	HVC INICVD,IN1PZERO HVC PACKED ZERO	F9188000	
0015FE	F270 17B0 17C0 017C0			4368	INCVDLOP NULL	F9190000	
001604	47F0 15F6			4369	INCVDLOP DS		
0015F6	4BA0 1832			4370	SH R10,=H'1'	F9192000	
0015FA	4740 1608			4371	INCVDONE	F9194000	
0015FE	F270 17B0 17C0 017C0			4372	AP INICVD,IN1PONE	F9196000	
001604	47F0 15F6			4373	B INICVDLOP	F9198000	
001608				4374	ANOP	F9200000	
001608	F367 17A4 17B0 017A4			4375	INCVDONE NULL	F9202000	
00160E	48A0 1834			4376	INCVDONE DS		
001612				4377	UNPK IN1ERCNT,IN1CVD	F9204000	
001616	95F0 A000 00000			4378	SLA R10,IN1ERCNT	F9206000	
00161A	9240 A000 00000			4379	LH R10,=Y(IN1ERCNT)		
00161E	43A0 1832			4380	*		
001622	47F0 1612			4381	IN1EDIT NULL	F9208000	
001626	96F0 17A4 017A4			4382	IN1EDIT DS	F9210000	
001630				4383	CLI O(F10),C'0'	F9212000	
001634	D240 177A 0031 0177A			4384	BNE IN1WRITE	F9214000	
001638	47F0 15D8			4385	MVI O(R10),C'	F9216000	
001642	47F0 17C2			4386	AH R10,=H'1'	F9218000	
001646				4387	B IN1EDIT	F9220000	
001650				4388	IN1WRITE NULL	F9222000	
001654	4A90 1836			4389	IN1WRITE DS		
001658	47F0 15D8			4390	OI IN1ERCNT+6,X'F0'	F9224000	
001662				4391	AIF (SMACHINE EQ 20).IN16	F9226000	
001666				4392	AIF (SMACHINE NE 20).IN16	F9228000	
001670				4393	XIO IN1BUFF(SOPCODE+1),L'LOGTEXT+37	F9230000	
001674				4394	HVC IN1BUFF(SOPCODE+1),L'LOGTEXT+37	F9232000	
001678				4395	ORG *-6 XIOXIOXIOXIOXIOXIO	F9234000	
001682				4396	DC X'D0' XIOXIOXIOXIOXIOXIO		
001686				4397	ORG *-5 XIOXIOXIOXIOXIOXIO		
001690				4398	BC 7,*-6		
001694				4399	ANOP	F9236000	
001698				4400	AH R9,=(LOGEND-LOGID)	F9238000	
001702				4401	B IN1PRTOUT	F9240000	
001706				4402		F9242000	
001710				4403	IN1EWRTABL NULL		
001714				4404	IN1EWRTABL DS	F9246000	
001718				4405	HVC LOGTABL(X'92'),ILOGTABL RECREATE THE LOG TABL	F9248000	
001722				4406	B INCONTIN	F9250000	
001726				4407	LOGTABL DS	F9252000	
001730				4408	LOGTABL DS	F9254000	
001734				4409	LOGTABL DS		
001738				4410	DC AL1(XBCBCHK)		
001742				4411	DC AL1(10)		
001746				4412	DC H'0'		
001750				4413	DC CL(L'LOGTEXT)+BLOCK SEQ CK'		
001754				4414	DC SLOCENT ID=XBARREC,CLASS=7,TEXT=ERROR REPLY		
001758				4415	SLOCENT EQU 2	F9256000	
001762				4416	XBARREC DS		
001766				4417	DS OH		

HR2 S I M I T I B -- INITIALIZATION PROCESSOR

1	LCC	OBJECT CODE	ADDR1	ADDR2	STMT	SOURCE STATEMENT	
2	001656	02			4418+	DC AL1(XVAKREC)	
3	001657	07			4419+	DC AL1(7)	
4	001658	0000			4420+	DC H'0'	
5	00165A	C5D9D9D6D940D9C5			4421+	DC CL(L'LOGTEXT)'ERROR REPLY'	
6					4422	SLOGENT ID=XVAKRES,CLASS=10,TEXT='INVALID RESP'	P9258000
7	000003				4423+XVAKRES	EQU 3	
8	001666				4424+	DC OH	
9	001666	03			4425+	DC AL1(XVAKRES)	
10	001667	0A			4426+	DC AL1(10)	
11	001668	0000			4427+	DC H'0'	
12	00166A	C9D5E5C1D3C9C4H0			4428+	DC CL(L'LOGTEXT)'INVALID RESP'	
13					4429	SLOGENT ID=XVAKTEXP,CLASS=10,TEXT='UNIT EXCPTR'	P9260000
14	000004				4430+XVAKTEXP	EQU 4	
15	001676				4431+	DC OH	
16	001676	04			4432+	DC AL1(XVAKTEXP)	
17	001677	0A			4433+	DC AL1(10)	
18	001678	0000			4434+	DC H'0'	
19	00167A	E4D5C9E340C5E7C3			4435+	DC CL(L'LOGTEXT)'UNIT EXCPTR'	
20					4436	SLOGENT ID=XVAKTCHK,CLASS=7,TEXT='UNIT CHECK'	P9262000
21	000005				4437+XVAKTCHK	EQU 5	
22	001686				4438+	DC OH	
23	001686	05			4439+	DC AL1(XVAKTCHK)	
24	001687	07			4440+	DC AL1(7)	
25	001688	0000			4441+	DC H'0'	
26	00168A	E4D5C9E340C3C8C5			4442+	DC CL(L'LOGTEXT)'UNIT CHECK'	
27					4443	SLOGENT ID=XVAKSLERR,CLASS=15,TEXT='UNUSUAL END'	P9264000
28	000006				4444+XVAKSLERR	EQU 6	
29	001696				4445+	DC OH	
30	001696	06			4446+	DC AL1(XVAKSLERR)	
31	001697	0F			4447+	DC AL1(15)	
32	001698	0000			4448+	DC H'0'	
33	00169A	E4D5E4E2E4C1D3H0			4449+	DC CL(L'LOGTEXT)'UNUSUAL END'	
34					4450	SLOGENT ID=XSI0BAD,CLASS=12,TEXT='SIO FAILURE'	P9266000
35	000007				4451+XSI0BAD	EQU 7	
36	0016A6				4452+	DC OH	
37	0016A6	07			4453+	DC AL1(XSI0BAD)	
38	0016A7	0C			4454+	DC AL1(12)	
39	0016A8	0000			4455+	DC H'0'	
40	0016AA	E2C9D640C6C1C9D3			4456+	DC CL(L'LOGTEXT)'SIG FAILURE'	
41					4457	SLOGENT ID=XTPWRITE,CLASS=1,TEXT='H' LOG OF WRITES	P9268000
42	000008				4458+XTPWRITE	EQU 8	
43	001886				4459+	DC OH	
44	001886	08			4460+	DC AL1(XTPWRITE)	
45	001887	01			4461+	DC AL1(1)	
46	001888	0000			4462+	DC H'0'	
47	00188A	E640404040404040			4463+	DC CL(L'LOGTEXT)'H'	
48					4464	SLOGENT ID=XTPREAD,CLASS=1,TEXT='H' LOG FOR ALL HEADS	P9270000
49	000009				4465+XTPREAD	EQU 9	
50	0016C6				4466+	DC OH	
51	0016C6	09			4467+	DC AL1(XTPREAD)	
52	0016C7	01			4468+	DC AL1(1)	
53	0016C8	0000			4469+	DC H'0'	
54	0016CA	5940404040404040			4470+	DC CL(L'LOGTEXT)'H'	
55	0016D6	00			4471	DC X'00'	
56	0016D7				4472 ILTABEND	DS OC	

LCC		OBJECT CODE	ADDR1	ADDR2	SINT	SOURCE STATEMENT	F150CT70	11/21/72
					4473	AIP (SMACHINE EQ 20).INI7		F9276000
					4474	.INI7 ANOP		F9332000
	0016D7	4040404080404040			4475	INITIAL DC #5C' ,C'HASP ENVIRONMENT RECORDING ERROR PRINTOUT'		F9334000
	00172D	4040404040404040			4476	INHED DC 20C' ,C'ERROR ID',14C' ,		F9336000
	001757	C3D6D4EAD3C1E3C9			4477	DC C'CONULATIVE COUNT SINCE THE LAST IPL'		F9338000
	00177A	4040404040404040			4478	INIBUFF DS 0CL(L'LOGTEXT'+37)		F9340000
	00178E	4040404040404040			4479	DC 20C' ,		F9342000
	00179A	4040404040404040			4480	INILTEXT DC CL(L'LOGTEXT)' ,		F9344000
	0017A4	F0F0F0F0F0F0F0			4481	DC 10C' ,		F9346000
	0017AB	0000000000			4482	INIERCNT DC C'00000000'		F9348000
	0017B0	0000000000000000			4483	INICVD DC 0D'0',PL8'0'		F9350000
	0017B8	0000000000000000			4484	INIPZERO DC PL8'0'		F9352000
	0017C0	1C			4485	INIPONE DC P:1'		F9354000

HE2 \$ I N I T I 8 -- INITIALIZATION PROCESSOR

LOC	OBJECT CODE	ADDR1	ADDR2	STMT	SOURCE STATEMENT
001820				4550	
001820	40404040			4551	LTORG
001824	000017F6			4552	=C'
001828	152D			4553	=R18(INITINT)
00182A	1525			4554	=Y(ZEPCARD*16)
00182C	0002			4555	=Y(ZEPCARD*8)
00182E	0005			4556	=H'2'
001830	01R6			4557	=H'5'
001832	0001			4558	=Y(LOGTABLE)
001834	17A4			4559	=H'1'
001836	0010			4560	=Y(INIERCNT)
001838	0EF2			4561	=Y(LOGENE-LOGID)
00183A	1070			4562	=Y(\$SYC)
00183C	D9C5D7			4563	=AL1(XDLE,XACK0)
					=C'REP'

F15OCT70 11/21/72

F9536000

LCC	OBJECT CODE	ADDR1	ADDR2	STMT	SOURCE STATEMENT	F15OCT70	11/21/72
00183P	4566	SL		EQU	*-HASPBJE+SHPRTFORG	V03.1	F9542000
000000	4567	SL1		EQU	*-HASPBJE+128 V03.1		
000000	4568	SL2		EQU	SL/100000*100000		F9544000
001770	4569	SL3		EQU	(SL-SL1)/10000*10000		F9546000
000000	4570	SL4		EQU	(SL-SL1-SL2)/1000*1000		F9548000
000000	4571	SL5		EQU	(SL-SL1-SL2-SL3)/100*100		F9550000
000007	4572	SL6		EQU	(SL-SL1-SL2-SL3-SL4)/10*10		F9552000
					SL-SL1-SL2-SL3-SL4-SL5		F9554000
4575 *	4575 *						F9558000
4576 *	4576 *				THE FOLLOWING VALUE OF \$DLNGTH INDICATES THE		F9560000
4577 *	4577 *				MINIMUM MEMORY SIZE TO CONTAIN THIS PROGRAM.		F9562000
4578 *	4578 *				THE VALUE REPRESENTS THE DECIMAL VALUE		F9564000
006207	4579	\$DLNGTH	EQU	SL1/100000*10+8576*SL2/10000*65536+SL3/1000*4096+SL4/100CP9568000			F9566000
					+256*SL5/10*16*SL6		F9570000

LINE	LCC	OBJECT CODE	ADDR1	ADDR2	SYMT	SOURCE STATEMENT	DATE	TIME
1						F15OCT70		11/21/72
2								
3								
4								
5								
6								
7								
8								
9	4632	*				***** EIGHT CHARACTER ERROR MESSAGE CODES *****		
10	4633	*						F9680000
11	4634	*						F9682000
12	4635	*						F9684000
13	4636	*				01 = BLOCK COUNT CHECK (DUPLICATE OR LOST RECORDS)		F9686000
14	4637	*				II= INPUT BLOCK CHECK FROM HASP		F9688000
15	4638	*				EE= EXPECTED BLOCK CHECK		F9690000
16	4639	*						F9692000
17	4640	*				02 = NEGATIVE ACKNOWLEDGEMENT RECEIVED		F9694000
18	4641	*				02000000		F9696000
19	4642	*						F9698000
20	4643	*				03 = UNKNOWN RESPONSE RECEIVED		F9700000
21	4644	*				IIII= DATA BYTES 1,2 OF INPUT.		F9702000
22	4645	*				(IF CORRECT CONTROL SEQUENCE		F9704000
23	4646	*				ENDING SEQUENCE IS INVALID)		F9706000
24	4647	*						F9708000
25	4648	*				04 = UNIT EXCEPTION - EOT RECEIVED		F9710000
26	4649	*				0400AAAA AAAA= DEVICE ADDRESS. 0 FOR BSCA		F9712000
27	4650	*						F9714000
28	4651	*				05 = UNIT CHECK		F9716000
29	4652	*				05SSAAAA SS= SENSE BYTE		F9718000
30	4653	*				AAAA= DEVICE ADDRESS. 0 FOR BSCA		F9720000
31	4654	*						F9722000
32	4655	*				06 = UNUSUAL END		F9724000
33	4656	*				06CCAAAA CC= CSW BYTE 5		F9726000
34	4657	*				AAAA= DEVICE ADDRESS. 0 FOR BSCA		F9728000
35	4658	*						F9730000
36	4659	*				07 = SIO FAILURE		F9732000
37	4660	*				07000000		F9734000
38	4661	*						F9736000
39	4662	*				08 = ADAPTER WRITE LOGGING		F9738000
40	4663	*				08000000 000000= BYTES 2,3,4 OF OUTPUT DATA		F9740000
41	4664	*						F9742000
42	4665	*				09 = ALL COMMUNICATION ADAPTER INTERRUPTS		F9744000
43	4666	*				09IIIIII IIIII= BYTES 2,3,4 OF INPUT DATA		F9746000
44	4667	*						F9748000
45	4668	*				*****		F9750000
46						***** F9752000		

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APPENDIX BTERMINAL IMPLEMENTATION RECOMMENDATIONS

This appendix contains random notes which do not seem to fit elsewhere in the document, recommendations on features and problems to look at.

1. IBM logs all errors on the console, if the console exists, and in an environment table. When the terminal is loaded, the contents of the environment table, if it is still intact, are printed on the terminal.
2. IBM terminals can dynamically change core locations via cards read by the terminal at load time (before the SIGNON or blank card).
3. If a NAK block is transmitted by the terminal as a response and a NAK block is received by the terminal, a problem can occur if the IBM implementation of the error procedure is implemented. The problem occurs if the terminal's NAK block is in response to an EOF block from the central processor. The EOF block will be lost. A better error procedure implementation is to keep track of the fact that a NAK block was transmitted by the terminal and if a NAK block is received from the central processor, to transmit a BCB error block.
4. The standard transmission buffer size {&MLBFSZ} is 400 bytes. The buffer size can be changed if both processors have the same length buffers. The buffer size must be established at assembly time. {HASPGEN parameter}
5. If multiple devices are to be defined on a terminal, the following HASPGEN parameters must be changed according to the configuration &NUMTPRD, &NUMTPPU, and &NUMTPPR. Also a DCT for each additional device must be generated for the central processor.
6. If the terminal is communicating with the central processor via a leased line, the central processor ignores the SIGN-ON card.
7. The number of buffers {&NUMBUF} must correspond to the number of remote device functions plus the number of local device functions. {Punch and print functions must have two buffers each to do double buffering.} {HASPGEN parameters.}

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8. &NUMRJE and &NUMLNES must be equal to the largest teleprocessing line identification number or the largest remote terminal identification number {HASPGEN parameters}.
9. &NUMTPPR and &NUMTPPU must specify the maximum number of HASP Remote Terminal print-output and punch-output streams that can simultaneously be active. {HASPGEN parameters}
10. The RMTnn parameter must be defined for each HASP Remote Terminal. {HASPGEN parameter}
11. If ASCII code is going to be used, the &USASCII parameter must be set to YES. {HASPGEN parameter}
12. The type of character compression used should be an assembly option for the terminal. Types available are: trailing blank compression; leading, embedded and trailing blank compression; all duplicate character compression.
13. The minimum number of duplicate characters necessary to trigger compression should be an assembly option for the terminal.
14. Transparent or non-transparent communication code should be an assembly option for the terminal.
15. The SRCB {Section 4.2.4} for General Control Records that are defined but not in use are:

```

SSSSSSS = 1000010 - Final terminal SIGN-OFF
          = 1000011 - Print initialization record
          = 1000100 - Punch initialization record
          = 1000101 - Input initialization record
          = 1000110 - Data set transmission initialization
          = 1000111 - System configuration status
          = 1001001 - 1011001 - Reserved
          = 1011010 - 1111111 - Available for local modification
  
```

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APPENDIX C

EBCDIC CHARACTER ASSIGNMENTS

S/360 Main Storage Bit Positions 0, 1, 2, 3																	
Bit Positions 4, 5, 6, 7	0000	0001	0010	0011	0100	0101	0110	0111	1000	1001	1010	1011	1100	1101	1110	1111	
Hex	0	1	2	3	4	5	6	7	8	9	A	B	C	D	E	F	
0000	0	NUL	DLE	DS	SP	&	-						{	}	\	0	
0001	1	SOH	DC1	SOS					a	j	~		A	J		1	
0010	2	STX	DC2	FS	SYN				b	k	s		B	K	S	2	
0011	3	ETX	DC3						c	l	t		C	L	T	3	
0100	4	PF	RES	BYP	PN				d	m	u		D	M	U	4	
0101	5	HT	NL	LF	RS				e	n	v		E	N	V	5	
0110	6	LC	BS	EOB ETB	UC				f	o	w		F	O	W	6	
0111	7	DEL	IL	PRE ESC	EOT				g	p	x		G	P	X	7	
1000	8		CAN						h	q	y		H	Q	Y	8	
1001	9	RLF	EM						\	i	r	z		I	R	Z	9
1010	A	SMM	CC	SM		¢	!	!	:								
1011	B	VT				.	\$,	#								
1100	C	FF	IFS		DC4	<	*	%	@								
1101	D	CR	IGS	ENQ	NAK	()	-	'								
1110	E	SO	IRS	ACK		+	:	>	=								
1111	F	SI	IUS	BEL	SUB		~	?	"								

ACKO=X'70'



Duplicate Assignment

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APPENDIX D

ASCII CHARACTER ASSIGNMENTS

S/360 Main Storage Bit Positions 0, 1, 2, 3																	
Bit Positions 4, 5, 6, 7		0000	0001	0010	0011	0100	0101	0110	0111	1000	1001	1010	1011	1100	1101	1110	1111
	HEX	0	1	2	3	4	5	6	7	8	9	A	B	C	D	E	F
0000	0	NUL	DLE	SP	0	@	P	\	p								
0001	1	SOH	DC1	!	1	A	Q	o	q								
0010	2	STX	DC2	"	2	B	R	b	r								
0011	3	ETX	DC3	#	3	C	S	c	s								
0100	4	EOT	DC4	\$	4	D	T	d	t								
0101	5	ENQ	NAK	%	5	E	U	e	u								
0110	6	ACK	SYN	&	6	F	V	f	v								
0111	7	BEL	ETB	'	7	G	W	g	w								
1000	8	BS	CAN	(8	H	X	h	x								
1001	9	HT	EM)	9	I	Y	i	y								
1010	A	LF	SUB	*	:	J	Z	j	z								
1011	B	VT	ESC	+	;	K	[k	{								
1100	C	FF	FS	,	<	L	\	l									
1101	D	CR	GS	-	=	M]	m	}								
1110	E	SO	RS	.	>	N	^	n	~								
1111	F	SI	US	/	?	O	_	o	DEL								

ACKD=X'30'

