

# 2N7000



## N-Channel Enhancement Mode MOSPOWER FETlington™

### APPLICATIONS

- CMOS or TTL Logic Compatible
- Bipolar Darlington Replacement
- Lamp, Relay Driver or Buffer
- Analog Signal Switching

### PRODUCT SUMMARY

Part Number	$V_{DSS}$ Volts	$r_{DS(ON)}$ (ohms)	Package
2N7000	60	5	T0-92



PIN 1 - Source  
PIN 2 - Gate  
PIN 3 - Drain

T0-92

1 2 3

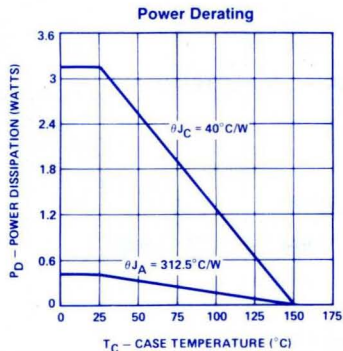
**NEW  
LOGIC-TO-LOAD  
DESIGN  
5 Volts in-100 mA out**

### ABSOLUTE MAXIMUM RATINGS ( $T_C = 25^\circ\text{C}$ unless otherwise noted)

Parameter		2N7000	Units
$V_{DS}$	Drain-Source Voltage	60	V
$V_{DGR}$	Drain-Gate Voltage ( $R_{GS} = 1\text{ M}\Omega$ )	60	V
$I_D @ T_C = 25^\circ\text{C}$	Continuous Drain Current	$\pm 200$	mA
$I_D @ T_C = 100^\circ\text{C}$	Continuous Drain Current	$\pm 123$	mA
$I_{DM}$	Pulsed Drain Current <sup>1</sup>	$\pm 500$	mA
$V_{GS}$	Gate-Source Voltage	$\pm 40$	V
$P_D$	Max. Continuous Power Dissipation	400	mW
$P_D$	Max. Pulsed Power Dissipation <sup>2</sup>	3.125	W
Junction to Case	Linear Derating Factor	25	$\text{mW}/^\circ\text{C}$
Junction to Ambient	Linear Derating Factor	3.2	$\text{mW}/^\circ\text{C}$
$T_J$	Operating and		$^\circ\text{C}$
$T_{stg}$	Storage Temperature Range	-55 To +150	$^\circ\text{C}$
Lead Temperature	(1/16" from case for 10 secs.)	300	$^\circ\text{C}$

1 Pulse Test: Pulsewidth  $\leq 300\mu\text{sec}$ , Duty Cycle  $\leq 2\%$

2 One Second Single, Power Pulse



**Siliconix**

# ELECTRICAL CHARACTERISTICS ( $T_C = 25^\circ\text{C}$ unless otherwise noted)

## STATIC

Parameter		Type	Min.	Typ.	Max.	Units	Test Conditions
BV <sub>DSS</sub>	Drain-Source Breakdown Voltage	2N7000	60	80		V	$V_{GS} = 0$ $I_D = 10\ \mu\text{A}$
V <sub>GS(th)</sub>	Gate-Threshold Voltage	2N7000	0.8	1.8	3	V	$V_{DS} = V_{GS}$ , $I_D = 1\ \text{mA}$
I <sub>GSSF</sub>	Gate-Body Leakage Forward	2N7000		1	10	nA	$V_{GS} = +15\text{V}$
I <sub>GSSR</sub>	Gate-Body Leakage Reverse	2N7000		-1	-10	nA	$V_{GS} = -15\text{V}$
I <sub>DSS</sub>	Zero Gate Voltage Drain Current	2N7000		0.1	1	$\mu\text{A}$	$V_{DS} = 48\text{V}$ , $V_{GS} = 0$
		2N7000		0.1	1	mA	$V_{DS} = 48\text{V}$ , $V_{GS} = 0$ $T_C = 125^\circ\text{C}$
I <sub>D(on)</sub>	On-State Drain Current <sup>1</sup>	2N7000	75	100		mA	$V_{GS} = 4.5\text{V}$ , $V_{DS} = 10\text{V}$
V <sub>DS(on)</sub>	Static Drain-Source On-State Voltage <sup>1</sup>	2N7000		1.2	2.5	V	$V_{GS} = 10\text{V}$ , $I_D = 0.5\text{A}$
		2N7000			0.40	V	$V_{GS} = 4.5\text{V}$ , $I_D = 75\ \text{mA}$
R <sub>DS(on)</sub>	Static Drain-Source On-State Resistance <sup>1</sup>	2N7000		2.4	5	$\Omega$	$V_{GS} = 10\text{V}$ , $I_D = 0.5\text{A}$
R <sub>DS(on)</sub>	Static Drain-Source On-State Resistance <sup>1</sup>	2N7000		4.3	9	$\Omega$	$V_{GS} = 10\text{V}$ , $I_D = 0.5\text{A}$ , $T_C = 125^\circ\text{C}$


## DYNAMIC

$g_{fs}$	Forward Transconductance <sup>1</sup>	2N7000	100	200		mS( $\bar{t}$ )	$V_{DS} = 10\text{V}$ , $I_D = 0.2\text{A}$
C <sub>iss</sub>	Input Capacitance	2N7000		30	60	pF	$V_{GS} = 0$ , $V_{DS} = 25\text{V}$ $f = 1\ \text{MHz}$
C <sub>oss</sub>	Output Capacitance	2N7000		14	25	pF	
C <sub>rss</sub>	Reverse Transfer Capacitance	2N7000		2	5	pF	
t <sub>(ON)</sub>	Turn-On Time	2N7000		6	10	ns	$V_{DD} = 15\text{V}$ , $I_D \approx 0.50\text{A}$ $R_{\theta} = 25\ \Omega$ , $R_L = 25\ \Omega$ (MOSFET switching times are essentially independent of operating temperature.)
t <sub>(OFF)</sub>	Turn-Off Time	2N7000		6	10	ns	

## THERMAL RESISTANCE

R <sub>thJC</sub>	Junction-to-Case	2N7000		33	40	$^\circ\text{C}/\text{W}$	
R <sub>thJA</sub>	Junction-to-Ambient	2N7000			312.5	$^\circ\text{C}/\text{W}$	Free Air Operation

## BODY-DRAIN DIODE RATINGS AND CHARACTERISTICS

I <sub>S</sub>	Continuous Source Current (Body Diode)	2N7000			-0.2	A	Modified MOSPOWER symbol showing the integral P N Junction rectifier 
I <sub>SM</sub>	Source Current <sup>1</sup> (Body Diode)	2N7000			-0.5	A	
V <sub>SD</sub>	Diode Forward Voltage <sup>1</sup>	2N7000		-0.85		V	$T_C = 25^\circ\text{C}$ , $I_S = -0.2\text{A}$ , $V_{GS} = 0$

<sup>1</sup> Pulse Test: Pulse Width  $\leq 300\ \mu\text{sec}$ , Duty Cycle  $\leq 2\%$